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(54)	ARRAY SUBSTRATE FOR DISPLAY,
	METHOD OF MANUFACTURING ARRAY
	SUBSTRATE FOR DISPLAY AND DISPLAY
	DEVICE USING THE ARRAY SUBSTRATE

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U.S.C. 154(b) by 54 days.

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Feb. 6, 2001 (JP) 2001-029587

(51) Int. Cl.⁷ H01L 21/00

(52) **U.S. Cl.** **438/25**; 438/149; 438/73; 257/72; 257/748

(56) References Cited

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(57) ABSTRACT

Disclosed is to provide an array substrate for display, a method of manufacturing the array substrate for display and a display device using the array substrate for display.

The present invention is an array substrate for display, which includes: a thin film transistor array formed on an insulating substrate 1; a plurality of wirings 23 and 24 arranged on the insulating substrate 1; connection pads 25 and 27 arranged on unilateral ends of the wirings 23 and 24 and respectively connected therewith; and pixel electrodes 22, wherein dummy conductive patterns 29 are arranged between the ends of the connection pads 25 and 27 and ends of the pixel electrodes 22.

16 Claims, 11 Drawing Sheets

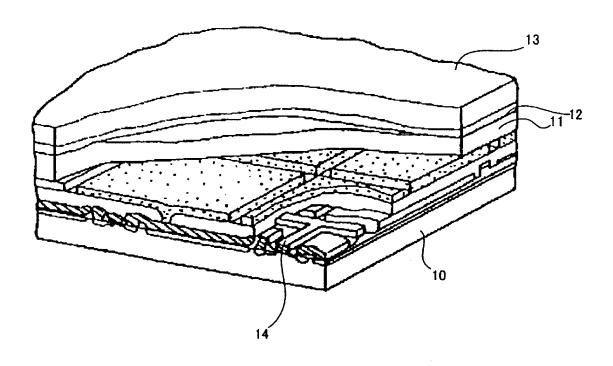




FIG. 1

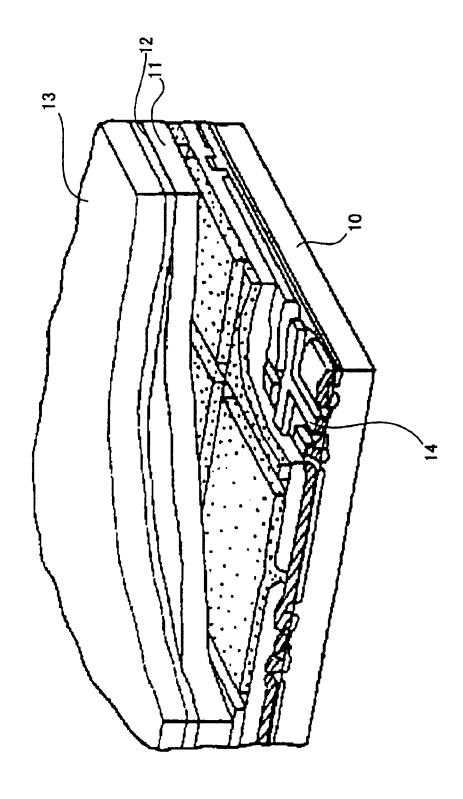


FIG. 2

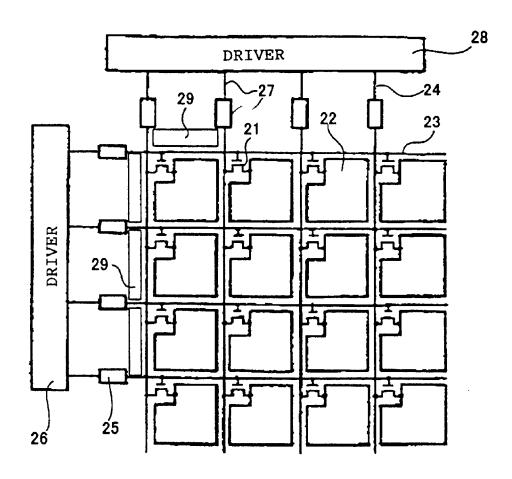


FIG. 3

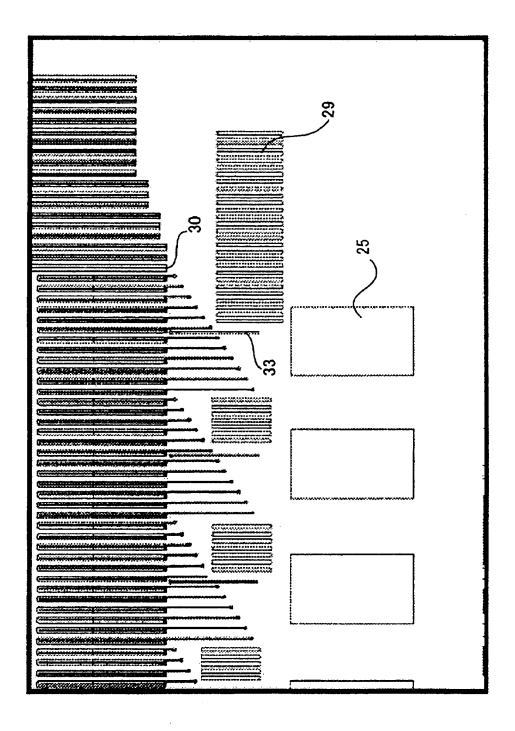
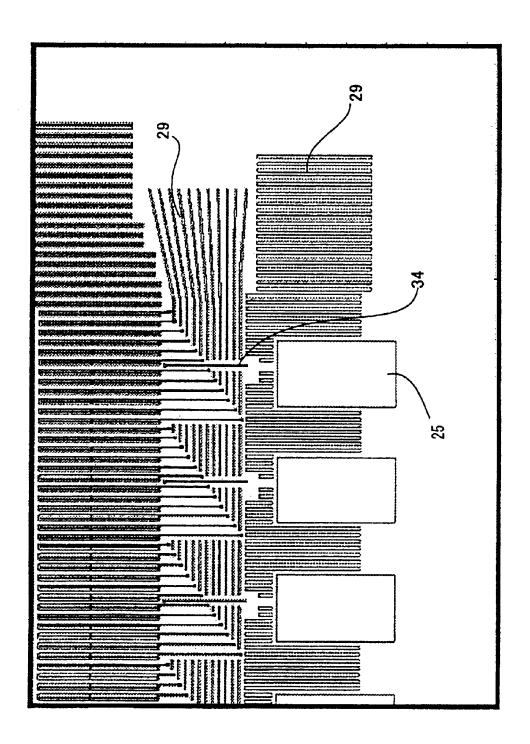


FIG. 4



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