

PAUL D. FRANZON

B. BRIEF RESUME

1. Education

- Doctor of Philosophy, Electrical and Electronic Engineering 1989, University of Adelaide, Australia . Advisor: Kamran Eshraghian.
- Bachelor of Engineering with First Class Honours, Electrical and Electronic Engineering: 1984, University of Adelaide, Australia
- Bachelor of Science, Physics and Mathematics: 1983, University of Adelaide, Australia.

2. Professional Experience

- Assistant Professor, Associate Professor, and Professor, North Carolina State University, Department of Electrical and Computer Engineering, Raleigh, North Carolina, January 1989 - present
- Cofounder, LightSpin Technologies Inc., 2001-. Vice-President of Engineering, 2001-2002, Raleigh NC.
- PhD Candidate, University of Adelaide, Department of Electrical and Electronic Engineering, Adelaide, South Australia, Australia, April 1987 - December 1988.
- Director and Co-Founder, Network Communications Pty. Ltd., Adelaide, South Australia, Australia, April 1987 - April 1989.
- Consultant, AT&T Bell Laboratories, Holmdel NJ, January 1986 - April 1987.
- PhD Candidate, University of Adelaide, Department of Electrical and Electronic Engineering, Adelaide, South Australia, Australia, August 1984-December 1985.
- Engineer, Defence Science and Technology Organization, Salisbury, South Australia, Australia, January 1984--July 1984
- Intern, Defense Science and Technology Organization, Salisbury, South Australia, Australia, December 1982--March 1983.
- Intern, Telecom Australia, Adelaide, South Australia, Australia, December 1981-March 1982.
- Infantry Soldier and Officer, (Ranks held: Private - Captain), Royal Australian Infantry Corps, Australian Army Reserve, December 1979 - December 1991.

3. Scholarly and Creative activities

<i>Type</i>	<i>Number</i>
Books	3
Solution Manuals	1
Edited Book	11
Refereed Journal article	78
Other Journal (submitted)	2

Conference Paper (refereed)	192
Patents filed	17
Research Presentations, Invited	38 conference + 56 other

4. Membership in professional organizations

Fellow, Institute of Electrical and Electronic Engineers, 1984-
 Member, IMAPS
 Member, SPIE
 Member, Association of Computing Machinery

5. Scholarly and professional honors

- NRL Alan Berman Research Publication Award, 2008
- Babbage Award, Synopsys, 2008
- Fellow of the IEEE, 2006
- Alcoa Research Award, 2005
- ECE Graduate Teacher of the year award, 2007
- ECE Most Helpful Teacher of the year award, 2007
- ECE Teacher of the year award, 2006
- ECE Graduate Advisor of the year award, 2006
- Alumni Undergraduate Distinguished Professor, 2003-2005.
- Graduate teacher of the year, ECE department, 2005
- NSW Australia Expatriate Scientist Award, 2003
- Selected to the NCSU Academy of Outstanding Teachers, 2001
- First round prize winner, SRC copper challenge, 2000.
- Teacher of the Year Award, presented by the IEEE Student Branch, 1997
- National Science Foundation Young Investigator's Award, 1993.
- 13 prizes while a student at the University of Adelaide

6. Professional service on campus

- Member, STRAG 2003-2005
- Instructor, PE preparation course 1995-2000

7. Professional service off campus

- Consultant to Lerner, David, Littenberg, Kruholz and Mentlik, 2013, Patent issues
- Consultant to DARPA, thermal evaluation, 2012-13
- Consultant to DARPA, Exascale Computing Study, 2007-9.
- Consultant to Rambus, Semiconductors, 2009-
- Consultant to Techsearch, 2008.
- Consultant, NTU, 2004-9. ASIC Design.
- Consultant to Tessera, 2009. 3DIC advising.
- Consultant, Irvine Sensors, 2006. Secure chip design.
- Consultant, Cisco Systems, 2006, Signal Integrity.

- Consultant, Talon Logic, 2005. Secure system Design.
- Consultant to O'Malveny and Meyers, 2000-2002, Patent issues.
- Consultant to Venture 2000, 2000, Due Diligance.
- Consultant to CAPPs, 1999-2000, IP Development.
- Consultant to Sofrent, 1999-2000, IP Development.
- Consultant to Ericsson, 1997, Synthesis Methodology.
- Consultant to Cadence, 1996. Evaluated possible company acquisition.
- Consultant to Polychip, 1994 - 2000. Circuit Design.
- Consultant to Square-D, 1996. Interconnect Design.
- Consultant to Mentor Graphics, 1995, 1996. Technical advisory board.
- Consultant to Cadence Design Systems, 1992, 1996. Technical advisory board.
- Consultant to DCT, 1995-1996. ASIC Design.
- Consultant to Techsearch International, 1989-1991. Report Preparation.
- Consultant to BNR, HP, Sun. 1992-4. Interconnect Design.
- Consultant to MCNC, 1989. CAD

II. TEACHING AND MENTORING OF UNDERGRADUATE AND GRADUATE STUDENTS

A. TEACHING EFFECTIVENESS

1. Courses Taught

Course	When	Enrollment	Instructor effectiveness	Course excellence
ECE 520-651 ASIC Design	Sum '12	18		
ECE 733 Digital Electronics	S 2012	52	4.2	4.2
ECE 520-601	S 2012	12	4.3	4.2
ECE 520-001 ASIC Design	S 2012	116	4.3	4.3
ECE 464 ASIC Design	S 2012	14	4.3	4.3
ECE 406 Des. Complex Systems	F 2011	58	4.6	4.6
ECE 520-651 ASIC Design	Sum '11	13	4.2	4.2
ECE 733 Digital Electronics	S 2011	18	4.5	4.5
ECE 520-601	S 2011	12	4.3	4.2
ECE 520-001 ASIC Design	S 2011	75	4.5	4.5
ECE 464 ASIC Design	S 2011	25	4.5	4.5
ECE 406 Des Complex Systems	F 2010	75	4.6	4.6
ECE 733 Digital Circuits 001	S 2010	36	4.3	4.2
ECE 733 Digital Circuits 601	S 2010	3	5.0	5.0
ECE 520 ASIC Design 001	S 2010	57	4.3	4.4
ECE 520 ASIC Design 601	S 2010	17	4.2	4.4
ECE 464 ASIC Design	S 2010	9	4.3	4.4
ECE 733 Digital Circuits 001	S 2009	57	4.6	4.5

ECE 733 Digital Circuits 601	S 2009	7	4.4	4.2
ECE 520 ASIC Design 001	S 2009	128	4.6	4.4
ECE 520 ASIC Design 601	S 2009	17	4.5	4.20
ECE 520 ASIC Design 620	S 2009	1		
ECE 464 ASIC Design	S 2009	16	4.6	4.4
ECE 733 Digital Circuits 001	S 2008	67	4.64	4.64
ECE 733 Digital Circuits 601	S 2008	2		
ECE 520 ASIC Design 001	S 2008	101	4.76	4.59
ECE 520 ASIC Design 601	S 2008	15	4.5	4.20
ECE 464 ASIC Design	S 2008	27	4.76	4.59
ECE 733	S 2007	58	4.64	4.46
ECE 520 ASIC Design 001	S 2007	129	4.75	4.68
ECE 520 ASIC Design 601	S 2007	6		
ECE 464 ASIC Design	S 2007	17	4.75	4.68
ECE 745 ASIC Verification	F 2006	20		4.6
ECE 733 Digital Circuits 001	S 2006	43	4.5	4.6
ECE 520 ASIC Design 001	S 2006	65		
ECE 520 ASIC Design 601	S 2006	8		
ECE 464 ASIC Design 001	S 2006	17	4.4	4.1
ECE 733 Digital Circuits 001	S 2005	30	4.5	4.6
ECE 733 Digital Circuits 601	S 2005	5		
ECE 520 ASIC Design 001	S 2005	44	4.6	4.3
ECE 520 ASIC Design 002	S 2005	34	4.6	4.5
ECE 520 ASIC Design 601	S 2005	2		
ECE 464 ASIC Design 001	S 2005	15	4.9	4.1
ECE 464 ASIC Design 002	S 2005	13	4.9	4.1
ECE 733 Digital circuits	S 2004	45	4.2 (4.1)	4.2 (3.9)
ECE 520 ASIC Design	S 2004	76	4.4 (4.1)	4.3 (3.9)
ECE 520 ASIC Design 601	S 2004	17		
ECE 464 ASIC Design	S 2004	69	4.5 (4.1)	4.0 (3.9)
ECE 733 Digital Circuits	S 2003	84	4.3 (4.0)	4.2 (3.8)
ECE 520 ASIC Design	S 2003	81	4.2 (4.0)	3.9 (3.8)
ECE 520 ASIC Design 601	S 2003	30		
ECE 464 ASIC Design	S 2003	63	3.9 (4.0)	3.5 (3.8)
ECE 406 Design Complex DS	F 2002	167	4.2 (4.1)	3.8 (3.9)
ECE 520 ASIC Design	S 2002	239	4.4 (4.1)	4.2 (3.9)
ECE 520 ASIC Design 601	S 2002			
ECE 464 ASIC Design	S 2002	39	4.1 (4.1)	3.6 (3.9)
ECE 406 Des. Complex Dig Sys	F 2000		4.7 (4.0)	4.5 (3.7)
ECE 704 Design For Test	F 2000		4.6 (4.0)	4 (3.8)
ECE 520 ASIC Design 001	S 2000		4.5 (4.1)	4.4 (3.9)
ECE 520 ASIC Design 002	S 2000		4.6 (4.1)	4.3 (3.9)
ECE 520 ASIC Design 601	S 2000			
ECE 492B ASIC Design	S 2000		4.5 (4.1)	4.3 (3.9)
ECE 342 Des. Complex Dig Sys	F 1999		4.4 (4.1)	4 (3.9)

ECE 520 ASIC Design 005	S 1999		4.6 (4.2)	4.5 (4.0)
ECE 520 ASIC Design 006	S 1999		4.5 (4.2)	4.3 (4.0)
ECE 342 Des. Complex Dig Sys	F 1998		4.3 (4.1)	4.1 (3.9)
ECE 520 ASIC Design	S 1998		4.6 (4.1)	4.4 (3.9)
ECE 492B ASIC Design	S 1998		4.7 (4.1)	4.8 (4.1)
ECE 342 Des. Complex Dig Sys	F 1997		4.6 (4.0)	4.4 (4.0)
ECE 520 ASIC Design	S 1997		4.7 (4.1)	4.7 (3.9)
ECE 492B ASIC Design	S 1997		4.7 (4.1)	4.4 (3.9)
AVERAGE				

- ECE 342 Design of Complex Digital Systems, Fall 1996, Overall Rating: 4.6/5.0.
- ECE 592B ASIC Design, Spring 1996, Overall Rating: 4.75/5.0. (With Dr. Liu.)
- ECE 492B ASIC Design, Spring 1996, Overall Rating: 4.00/5.0. (With Dr. Liu.)
- ECE 544, Design of Electronic Packaging and Interconnects, Spring 1999, Overall Rating : 4.60/5.0.
- ECE 520, Fundamentals of Logic Systems, Fall 1995, Overall Rating: 4.50/5.0.
- ECE 218, Computer Organization and Microprocessors, Both Sections, Spring, 1995: Section 001: 4.45/5.0; Section 002: 4.62/5.0.
- ECE 592V, VLSI Microprocessor Project, Spring 1995. (13 students but not rated).
- ECE 681/693A, Computer Engineering Seminar. Spring 1996 and Fall 1996.
- ECE 521 Computer Design and Technology, Fall 1994, Overall Rating: 4.24/5.0.
- ECE 691F, High Speed VLSI, Fall 1994, Overall Rating: 4.45/5.0.
- ECE 691P, Superscalar Processor Design, Spring 1994, Overall Rating: 4.67/5.0.
- ECE 591F, Design of Electronic Packaging and Interconnects, Spring 1994, Overall Rating: 4.60/5.0.

B. INSTRUCTIONAL DEVELOPMENT

1. NSF-funded CISE Infrastructure effort, "Experimental High Performance Computing and Communications Systems". (Total: \$1,338,283 including \$503,046 in matching.) Approximately \$283,000 of this funding went towards outfitting the ECE Design Center.

2. CAD Tools. Modern design is done with sophisticated Computer – Aided Design Tools, not with pencil and paper. I have spent considerable effort bringing such tools into the Unity environment, gaining the 'corporate knowledge' about how to use these tools effectively and obtaining additional computers for use with these tools. *Students tell me that knowledge of these tools is highly regarded by potential employers. In fact one student stated that 'Dr. Franzon teaches courses that gets jobs'. In addition, in 1999, we won the Cadence University Alliance Best Web site Awards.* Through my funded research efforts and Corporate Donations introduced the following Computer Aided Design Tools into the curricula:

Explore Litigation Insights

Docket Alarm provides insights to develop a more informed litigation strategy and the peace of mind of knowing you're on top of things.

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Keep your litigation team up-to-date with **real-time alerts** and advanced team management tools built for the enterprise, all while greatly reducing PACER spend.

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Docket Alarm offers a powerful API (application programming interface) to developers that want to integrate case filings into their apps.

LAW FIRMS

Build custom dashboards for your attorneys and clients with live data direct from the court.

Automate many repetitive legal tasks like conflict checks, document management, and marketing.

FINANCIAL INSTITUTIONS

Litigation and bankruptcy checks for companies and debtors.

E-DISCOVERY AND LEGAL VENDORS

Sync your system to PACER to automate legal marketing.