

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

PLAID TECHNOLOGIES INC.,
Petitioner

v.

YODLEE, INC. and YODLEE.COM, INC.,
Patent Owner

Case No. IPR2015-_____
Patent 6,199,077

DECLARATION OF TODD MOWRY IN SUPPORT OF PETITION FOR
INTER PARTES REVIEW

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I. INTRODUCTION AND QUALIFICATIONS

1. I have been retained on behalf of the Petitioner, Plaid Technologies, Inc., to provide this Declaration concerning technical subject matter relevant to the *inter partes* review of a covered business method patent of U.S. Patent No. 6,199,077 (“the ’077 Patent,” and Ex. 1001).

2. I am over 18 years of age. I have personal knowledge of the facts stated in this Declaration and could testify competently to them if asked to do so.

II. PROFESSIONAL QUALIFICATIONS

3. I am a Professor in the Department of Computer Science at Carnegie Mellon University. I also have a courtesy appointment in the Department of Electrical and Computer Engineering. I have served on the faculty of Carnegie Mellon University for eighteen (18) years starting in 1997 through the present (2015).

4. I also served on the faculty of the University of Toronto for four (4) years between 1993 and 1997, in the Department of Electrical and Computer Engineering and a courtesy appointment in the Department of Computer Science. Prior to that appointment, I served as a Graduate Research Assistant in the Department of Electrical Engineering at Stanford University for four (4) years between 1989 and 1993.

5. As a faculty member, I have taught and continue to teach courses and

directed research in computer systems and software, operating systems, distributed and network systems, object-oriented programming and design, and mobile computing.

6. I received a B.S. degree in Electrical Engineering with Highest Distinction from the University of Virginia in May 1988. I received an M.S. in Electrical Engineering from Stanford University in June 1989, and a Ph.D. in Electrical Engineering from Stanford University in March 1994.

7. I have worked in the computer industry in various capacities. I was a part-time Computer Architect and then Computer Architecture Consultant at Silicon Graphics, Inc. in Mountain View, California (formerly MIPS Computer Systems in Sunnyvale, California) from 1989 to 1993 and 1993 to 1996, respectively. I was a Visiting Scientist at IBM in Toronto from 1996 to 2004. During that same time period (1996 to 2004), I was also a Member of the Technical Advisory Board of SandCraft, Inc. in Santa Clara, California. I was the Director of the Intel Research Pittsburgh Lab at Intel Corporation in Pittsburgh, Pennsylvania from 2004 to 2007.

8. I have authored 19 journal articles and 55 conference papers. I am also an inventor on 5 patents.

9. I have published a number of papers in the top research conferences in the fields of operating systems and data storage and retrieval (including a paper

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