

UNITED STATES PATENT AND TRADEMARK OFFICE

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BEFORE THE PATENT TRIAL AND APPEAL BOARD

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PLAID TECHNOLOGIES, INC.  
Petitioner

v.

YODLEE, INC.  
Patent Owner

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Case No. CBM2016-\_\_\_\_\_  
U.S. Patent No. 6,317,783

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DECLARATION OF TODD C. MOWRY IN SUPPORT OF PETITION FOR  
COVERED BUSINESS METHOD REVIEW OF U.S. PATENT NO. 6,317,783

Mail Stop **Patent Board**  
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Plaid Technologies Inc.  
Exhibit 1002

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## **I. INTRODUCTION AND QUALIFICATIONS**

1. I have been retained on behalf of the Petitioner, Plaid Technologies, Inc., to provide this Declaration concerning technical subject matter relevant to the post-grant review of a covered business method patent, 6,317,783 (“the ’783 Patent,” Ex. 1001).

2. I am over 18 years of age. I have personal knowledge of the facts stated in this Declaration and could testify competently to them if asked to do so.

## **II. PROFESSIONAL QUALIFICATIONS**

3. I am a Professor in the Department of Computer Science at Carnegie Mellon University. I also have a courtesy appointment in the Department of Electrical and Computer Engineering. I have served on the faculty of Carnegie Mellon University for nineteen (19) years starting in 1997 through the present (2016).

4. I also served on the faculty of the University of Toronto for four (4) years between 1993 and 1997, in the Department of Electrical and Computer Engineering, and with a courtesy appointment in the Department of Computer Science. Prior to that appointment, I served as a Graduate Research Assistant in the Department of Electrical Engineering at Stanford University for four (4) years between 1989 and 1993.

5. As a faculty member, I have taught and continue to teach courses and

directed research in computer systems and software, operating systems, distributed and network systems, object-oriented programming and design, and mobile computing.

6. I received a B.S. degree in Electrical Engineering with Highest Distinction from the University of Virginia in May 1988. I received an M.S. in Electrical Engineering from Stanford University in June 1989, and a Ph.D. in Electrical Engineering from Stanford University in March 1994.

7. I have worked in the computer industry in various capacities. I was a part-time Computer Architect and then a Computer Architecture Consultant at Silicon Graphics, Inc. in Mountain View, California (formerly MIPS Computer Systems in Sunnyvale, California) from 1989 to 1993 and 1993 to 1996, respectively. I was a Visiting Scientist at IBM in Toronto from 1996 to 2004. During that same time period (1996 to 2004), I was also a Member of the Technical Advisory Board of SandCraft, Inc. in Santa Clara, California. I was the Director of the Intel Research Pittsburgh Lab at Intel Corporation in Pittsburgh, Pennsylvania from 2004 to 2007.

8. I have authored 19 journal articles and 55 conference papers. I am also an inventor on five patents.

9. I have published a number of papers in the top research conferences in the fields of operating systems and data storage and retrieval (including the paper

that won the Best Paper Award at the USENIX 2nd Symposium on Operating Systems Design and Implementation (OSDI '96)). I have also been an active member of the Parallel Data Lab at Carnegie Mellon University since 1997, described as “academia’s premiere storage systems research center.”<sup>1</sup>

10. I am the recipient of several honors and awards: the Arthur Samuel Thesis Award (awarded by the Stanford Computer Science department to the top two Ph.D. theses in a given year), several IBM Faculty Development Awards (1996, 1997, 1998, 2000, 2001, 2002, and 2003), several Best Paper Awards (the Second Symposium on Operating Systems Design and Implementation in 1996; the 20th International Conference on Data Engineering (ICDE) in 2004), the Alfred P. Sloan Research Fellowship (awarded to researchers in recognition of distinguished performance and a unique potential to make substantial contributions to their field), the Most Thought-Provoking Idea Award (awarded by the Architectural Support for Programming Languages and Operating Systems (ASPLOS), in 2004), and the TR100 Award (awarded by MIT’s *Technology Review* magazine to the top 100 most promising young innovators in science and technology, in 1999).

11. I am a member of the Institute of Electrical and Electronics Engineers

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<sup>1</sup> See the Parallel Data Lab website at <http://www.pdl.cmu.edu/>.

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