Paper No	
UNITED STATES PATENT AND TRADEMARK OFFICE	
BEFORE THE PATENT TRIAL AND APPEAL BOARD	

SK hynix Inc., SK hynix America Inc., SK hynix memory solutions Inc., and Hynix Semiconductor Manufacturing America Inc.

Petitioners,

V.

DSS Technology Management, Inc.
Patent Owner.

Patent No. 6,784,552 Issued: August 31, 2004 Filed: March 31, 2000

Inventors: James E. Nulty, Christopher J. Petti
Title: STRUCTURE HAVING REDUCED LATERAL SPACER
EROSION

Inter Partes Review No.

PETITION FOR INTER PARTES REVIEW



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EXHIBITS

HYNIX-1001 - U.S. Patent No. 6,784,552 to Nulty et al., filed Mar. 31, 2000 HYNIX-1002 - DSS Technology Management Inc. v. SK Hynix, Inc. et al., Case No. 6:15-cv-00691, Plaintiff's Original Complaint for Patent Infringement (July 16, 2015)

HYNIX-1003 – Declaration of Dr. Vivek Subramanian

HYNIX-1004 - U.S. Patent No. 5,482,894 to Havemann, filed Aug. 23, 1994

HYNIX-1005 - U.S. Patent No. 4,686,000 to Heath, filed Feb. 19, 1986

HYNIX-1006 – Prosecution History of U.S. Patent No. 6,784,552 to Nulty et al.,

filed Mar. 31, 2000

HYNIX-1007 - Gary W. Jones and Sanjay Tandon, Multilevel Metal

Interconnection Utilizing CVD Tungsten and Liftoff Processing, 29 Journal of

Electronic Materials (1990)

HYNIX-1008 – Claim Chart A (cited in Declaration of Dr. Vivek Subramanian)

HYNIX-1009 – Claim Chart B (cited in Declaration of Dr. Vivek Subramanian)

HYNIX-1010 – Claim Chart C (cited in Declaration of Dr. Vivek Subramanian)

HYNIX-1011 – Claim Chart D (cited in Declaration of Dr. Vivek Subramanian)



SK hynix Inc.; SK hynix America Inc.; SK hynix memory solutions Inc.; and Hynix Semiconductor Manufacturing America Inc., (collectively, "Petitioner" or "Hynix") respectfully petitions for *Inter Partes* Review ("IPR") under 35 U.S.C. §§ 311–319 and 37 C.F.R. § 42 of claims 1-12 (the "Challenged Claims") of U.S. Patent No. 6,784,552 (the "'552 Patent") (Ex. HYNIX-1001), of apparent assignee DSS Technology Management, Inc. ("Patentee" or "DSS"). As explained in this Petition, there exists a reasonable likelihood that Hynix will prevail with respect to at least one of the Challenged Claims.

I. MANDATORY NOTICES UNDER 37 C.F.R. § 42.8(A)(1)

A. Real Party-In-Interest Under 37 C.F.R. § 42.8(b)(1)

The real parties of interest of this petition are: SK hynix Inc. with its principal place of business and home office at 2091, Gyeongchung-daero, bubaleub, Icheon-si, Gyeonggi-do, South Korea; SK hynix America Inc. with its principal place of business at 3101 North First Street, San Jose, CA 95134; SK hynix memory solutions Inc. with its principal place of business at 3103 North First Street, San Jose, CA 95134; and Hynix Semiconductor Manufacturing America Inc. with its principal place of business at 1830 Willow Creek Circle, Eugene, Oregon 97402.



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