

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re *Inter Partes* Review of:                    )  
U.S. Patent No. 8,252,675                        )  
Issued: Aug. 28, 2012                            )  
Application No.: 12/942,763                     )  
Filing Date: Nov. 9, 2010                      )

**For: Methods of Forming CMOS Transistors with High Conductivity Gate  
Electrodes**

**FILED VIA PRPS**

**PETITIONER'S POWER OF ATTORNEY FOR  
PETITION FOR *INTER PARTES* REVIEW  
OF U.S. PATENT NO. 8,252,675**

NVIDIA Corp.  
*Inter Partes* Review of USPN 8,252,675

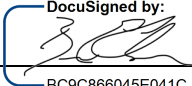
**PETITIONER'S POWER OF ATTORNEY**

Petitioner NVIDIA Corporation ("NVIDIA") hereby appoints the below Practitioners associated with Customer Number 117694 as its attorneys to transact all business before the Patent Trial and Appeal Board of the United States Patent and Trademark Office connected with the above-captioned Petition for *Inter Partes* Review of U.S. Patent No. 8,252,675.

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The individual signing below has the authority to execute this document on behalf of NVIDIA.

Name: Brian E. Cabrera

Signature: 

Title: Sr. Vice President & General Counsel

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Date: November 2, 2015