Jack C. Lee is a Professor of the Electrical and Computer Engineering Department and holds the Cullen Trust for Higher Education Endowed Professorship in Engineering # 4 at The University of Texas at Austin. He received the B.S. and M.S. degrees in electrical engineering from University of California, Los Angeles, in 1980 and 1981, respectively; and the Ph.D. degree in electrical engineering from University of California, Berkeley, in 1988. From 1979 to 1984, he was a Member of Technical Staff at the TRW Microelectronics Center, CA, in the High-Speed Bipolar Device Program. He worked on bipolar device and circuit design, fabrication and testing. In 1988, he joined the faculty of The University of Texas at Austin. His current research interests include semiconductor device (i.e. MOSFETs) fabrication processes, characterization and modeling, dielectric process, characterization and reliability, high-K gate dielectrics and electrode, semiconductor memory applications, and alternative channel materials. Dr. Lee has over 30 years of experience in semiconductor technology and dielectric processing. He has published over 500 journal publications and conference proceedings and several patents; and co-authored one book and two book chapters on high-K gate dielectrics. He has also been recognized with many teaching and research awards including the prestigious SRC Inventor Recognition Award from Semiconductor Research Corporation for his work on dielectric technology and characterization. Dr. Lee is a Fellow of IEEE and a Distinguished Lecturer for the IEEE Electron Devices Society.

Professional Experience

Professor, Department of Electrical and Computer Engineering
   Cullen Trust For Higher Education Endowed Professorship in Engineering #4
   The University of Texas at Austin, September 1996 - present
Associate Professor, Department of Electrical and Computer Engineering
   The University of Texas at Austin, September 1992 - August 1996
Assistant Professor, Department of Electrical and Computer Engineering
   The University of Texas at Austin, September 1988 - August 1992
Lecturer, EECS Department
   University of California at Berkeley, Spring 1988
Member of Technical Staff, Microelectronics Center
   TRW, Redondo Beach, CA, June 1979 – August 1984

Education

Ph.D. in Electrical Engineering, University of California at Berkeley,
   August 1988
M.S. in Electrical Engineering, University of California at Los Angeles, December 1981
B.S. in Electrical Engineering (with highest honors) UCLA,
   June 1980

Selected Awards

IEEE Electron Devices Society Distinguished Lecturer, 2004 - present.
Fellow, The Institute of Electrical and Electronic Engineers (IEEE), 2002 "For contributions
   to the understanding and development of ultra-thin dielectrics and their application to
   silicon devices"
Gordon Lepley IV Memorial Teaching Award, ECE Department, The University of Texas at
   Austin, 2004

Cullen Trust For Higher Education Endowed Professorship in Engineering, 2000-
Dean's Fellow, College of Engineering, The University of Texas at Austin, 1999, 2003
Lockheed Fort-Worth Division Award for Excellence in Engineering Teaching,
        College of Engineering, The University of Texas at Austin, 1996
Award of Excellence, Halliburton Foundation, 1993
Departmental Teaching Award, College of Engineering, The University of Texas at Austin,
        1993
SRC Inventor Recognition Award, Semiconductor Research Corporation, 1991
Hughes Aircraft Company Endowed Faculty Fellowship in Engineering,
        The University of Texas at Austin, 1991- 2000
Outstanding Engineering Teaching by an Assistant Professor, College of Engineering,
        The University of Texas at Austin, 1991
Best Paper Award, SEMATECH Centers of Excellence Coordination Meeting, 1990.
Dow Outstanding Young Faculty Award, American Society for Engineering Education, 1990
Engineering Research Initiation Award, Engineering Foundation of the United Engineering
        Trustees, 1989
Best Paper Award, IEEE International Reliability Physics Symposium, 1988

Litigation Experience as Testifying Experts:
My litigation experience includes writing expert reports on validity/ invalidity,
infringement/non-infringement, claim construction, reverse engineering, advising clients
during discovery, deposition and cross-examination of opposing experts, and testimony at
hearings and at trial.
   o   Expert Reports – I have written about 20 expert reports
   o   Depositions – I have been deposed about 7 times
   o   Trials – 2 cases have gone to trials (Elpida Memory v. Nanya Technology
        Corporation, and Agere Systems v. Rohm)
   o   Tutorial – I have given 1 tutorial to the judge in the Macronix v. Spansion case

• Macronix, Inc. v. Spansion, Inc.
  September 2014 –
  → Flash memory devices and fabrication methods, an ITC case
  [Ropes and Gray LLP]
  (Representing defendant, Spansion, Inc.)

• Keranos, LLC v. MicrochipTechnology, Inc.
  August 2012 – February 2014
  → Flash memory devices and fabrication methods
  [King and Spalding, LLP]
  (Representing defendant, MicrochipTechnology)

• University of Illinois v. Micron Technology, Inc.
  April 2012 – March 2013 →Semiconductor processes
  [Stadheim and Grear, Ltd.]
  (Representing plaintiff, University of Illinois)

- Xicor LLC v. Silicon Storage Technology, Inc.
  January 2011 – December 2011
  → Semiconductor processes
  [Covington and Burling LLP]
  (Representing defendant, Silicon Storage Technology)

- Elpida Memory, Inc. v. Nanya Technology Corporation
  April 2012 – December 2012
  → DRAM memory processes, an ITC case
  [Freitas Tseng & Kaufman LLP]
  (Representing defendant, Nanya Technology Corporation)

- Spansion LLC v. Samsung
  May 2010 – June 2011
  → Semiconductor devices
  [McDermott Will & Emery LLP]
  (Representing plaintiff, Spansion LLC)

- Spansion LLC v. Samsung
  July 2009 – March 2010
  → Semiconductor devices, an ITC case
  [King and Spalding, LLP]
  (Representing plaintiff, Spansion LLC)

- Advanced Micro Devices v. Samsung
  March 2008 – September 2010
  → Semiconductor devices;
  [Robins, Kaplan, Miller & Ciresi, LLP]
  (Representing plaintiff, Advanced Micro Devices)

- Agere Systems Inc. v. Samsung
  January 2007 – July 2010
  → Semiconductor devices
  [Townsend and Townsend and Crew, LLP]
  (Representing plaintiff, Agere Systems Inc.)

- Agere Systems Inc. v. Rohm Co. Ltd.
  April 2006 – November 2007
  → Semiconductor devices and fabrication methods
  [Townsend and Townsend and Crew, LLP]
  (Representing plaintiff, Agere Systems Inc.)

Jack Lee is the Cullen Trust for Higher Education Endowed Professor in Engineering in the Department of Electrical and Computer Engineering at The University of Texas at Austin. He received the B.S. and M.S. degrees in electrical engineering from University of California at Los Angeles in 1980 and 1981, respectively; and the Ph.D. degree in electrical engineering from University of California at Berkeley in 1988. From 1981 to 1984, he was a member of technical staff at the TRW Microelectronics Center, California, in the High-Speed Bipolar Device Program. He worked on bipolar circuit design, fabrication and testing. In 1988, he joined the faculty of The University of Texas at Austin. His current research interests include semiconductor devices, fabrication processes, characterization and modeling, dielectric processes, characterization and reliability, high-K gate dielectrics and electrode, semiconductor memory applications, and alternative channel materials.

He has published over 500 journal publications and conference proceedings; 1 book and 2 book chapters. He has been awarded two best paper awards, numerous teaching / research awards and several patents. He has supervised and graduated 40 Ph.D. students. Six of his PhD graduates are now professors in other universities and the rest are working in research and development programs in semiconductor industry. Dr. Lee is a fellow of the Institute of Electrical and Electronic Engineers (IEEE) and Distinguished Lecturer for IEEE Electron Devices Society.

At The University of Texas at Austin, Dr. Lee continues to teach undergraduate circuit analysis and design courses, as well as the graduate level course which he developed entitled "Nanoscale Device Physics and Technologies". This course focuses on the topics of current research on ultra-small high-speed semiconductor devices used in integrated circuits, and is designed for graduate students wishing to pursue research in the microelectronics area.

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The University of Texas at Austin, September 1988 - August 1992
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University of California at Berkeley, Spring 1988
Member of Technical Staff, Microelectronics Center
TRW, Redondo Beach, CA, May - August 1984 and June 1979 - September 1983

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Award of Excellence, Halliburton Foundation, 1993
Departmental Teaching Award, College of Engineering, The University of Texas at Austin, 1993
SRC Inventor Recognition Award, Semiconductor Research Corporation, 1991
Hughes Aircraft Company Endowed Faculty Fellowship in Engineering,
     The University of Texas at Austin, 1991- 2000
Outstanding Engineering Teaching by an Assistant Professor, College of Engineering,
     The University of Texas at Austin, 1991
Best Paper Award, SEMATECH Centers of Excellence Coordination Meeting, 1990.
Dow Outstanding Young Faculty Award, American Society for Engineering Education, 1990
Engineering Research Initiation Award, Engineering Foundation of the United Engineering
     Trustees, 1989
Best Paper Award, IEEE International Reliability Physics Symposium, 1988

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