UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

MICRON TECHNOLOGY, INC.
Petitioner

v.

LIMESTONE MEMORY SYSTEMS LLC
Patent Owner

Case IPR. No. **Unassigned**
U.S. Patent No. 6,233,181

Corrected Declaration of R. Jacob Baker, Ph.D. in Support of Petition For Inter Partes Review of U.S. Patent No. 6,233,181 Under 37 C.F.R. § 1.68

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I, R. Jacob Baker, hereby declare as follows:

I. INTRODUCTION AND QUALIFICATIONS

1. My name is R. Jacob Baker. My findings, as set forth herein, are based on my education and background in the fields discussed below.

2. I have been retained on behalf of Petitioner Micron Technology, Inc. ("Micron") to provide this Declaration concerning technical subject matter relevant to the *inter partes* review petition ("Petition") concerning U.S. Patent No. 6,233,181 ("the 181 Patent"). I reserve the right to supplement this Declaration in response to additional evidence that may come to light.

3. I am over 18 years of age. I have personal knowledge of the facts stated in this Declaration and could testify competently to them if asked to do so.

4. My compensation is not based on the resolution of this matter. My findings are based on my education, experience, and background in the fields discussed below.

5. I currently serve as a Professor of Electrical and Computer Engineering at the University of Nevada, Las Vegas (UNLV). I have been teaching electrical engineering at UNLV since 2012. Prior to this position, I was a Professor of Electrical and Computer Engineering at Boise State University from 2000. Prior to my position at Boise State University, I was an Associate Professor Electrical Engineering between 1998 and 2000 and Assistant Professor of

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Electrical Engineering between 1993 and 1998 at the University of Idaho. I have been teaching electrical engineering since 1991.

6. I received my Ph.D. in Electrical Engineering from the University of Nevada, Reno in 1993. I also received a MS and BS in Electrical Engineering from UNLV in 1988 and 1986, respectively.

7. As described in my CV (MICRON-1008), I am a licensed Professional Engineer in the state of Idaho and have more than 25 years of experience, including extensive experience in circuit design and manufacture of Dynamic Random Access Memory (DRAM) integrated circuit chips and CMOS Image Sensors (CISs) at Micron in Boise, Idaho. I also spent considerable time working on the development of Flash Memory while at Micron. My efforts resulted in more than a dozen Flash-memory related patents. Among other experiences, I led development of the Delay-Locked Loop (DLL) in the late 90s so that Micron products could transition to the DDR memory standard. I have worked as a consultant at other companies designing memory chips, including Sun, Oracle, and Contour Semiconductor. I have also worked as a consultant at other companies designing CISs, including OmniVision and Lockheed Martin.

8. I have taught courses in integrated circuit design (analog, digital, mixed-signal, etc.), linear circuits, microelectronics, communication systems, and

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