

United States Patent [19]
Halpern

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[54] **ELECTRONIC MONEY PURSE AND FUND TRANSFER SYSTEM**

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[73] Assignee: **Paperless Accounting, Inc.**,
Washington, D.C.

[21] Appl. No.: **201,339**

[22] Filed: **May 31, 1988**

Related U.S. Application Data

[63] Continuation of Ser. No. 470,689, Feb. 28, 1983, abandoned.

[51] Int. Cl.⁴ **G06F 15/30**

[52] U.S. Cl. **235/379; 235/380; 235/492; 902/2; 380/24**

[58] Field of Search **235/379, 380, 381, 438, 235/492; 902/2; 380/24**

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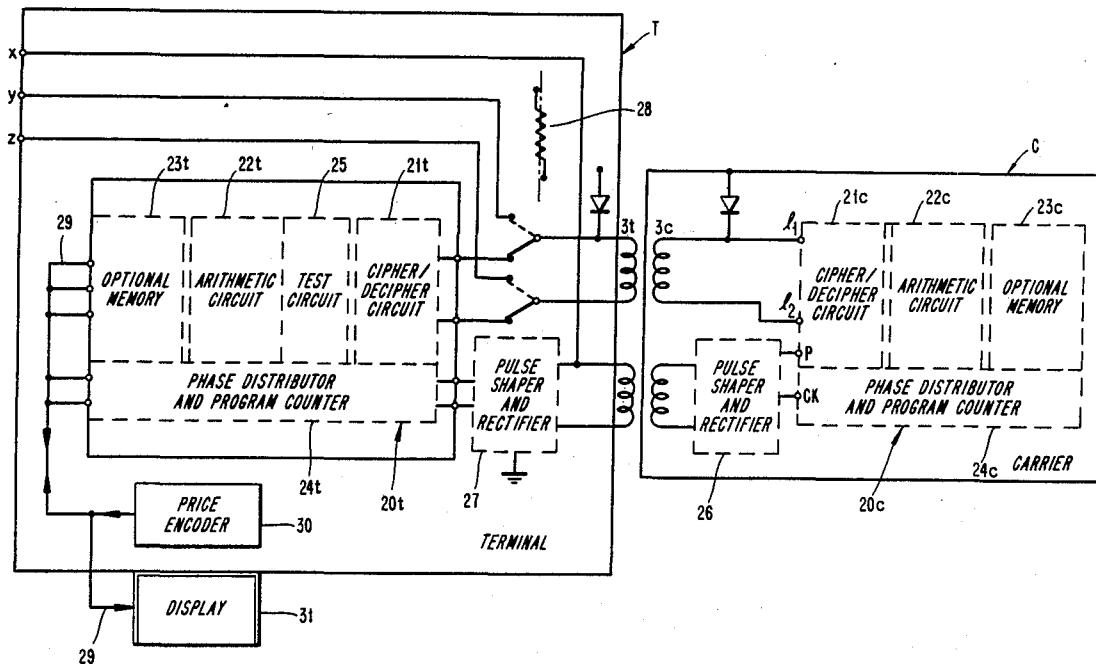
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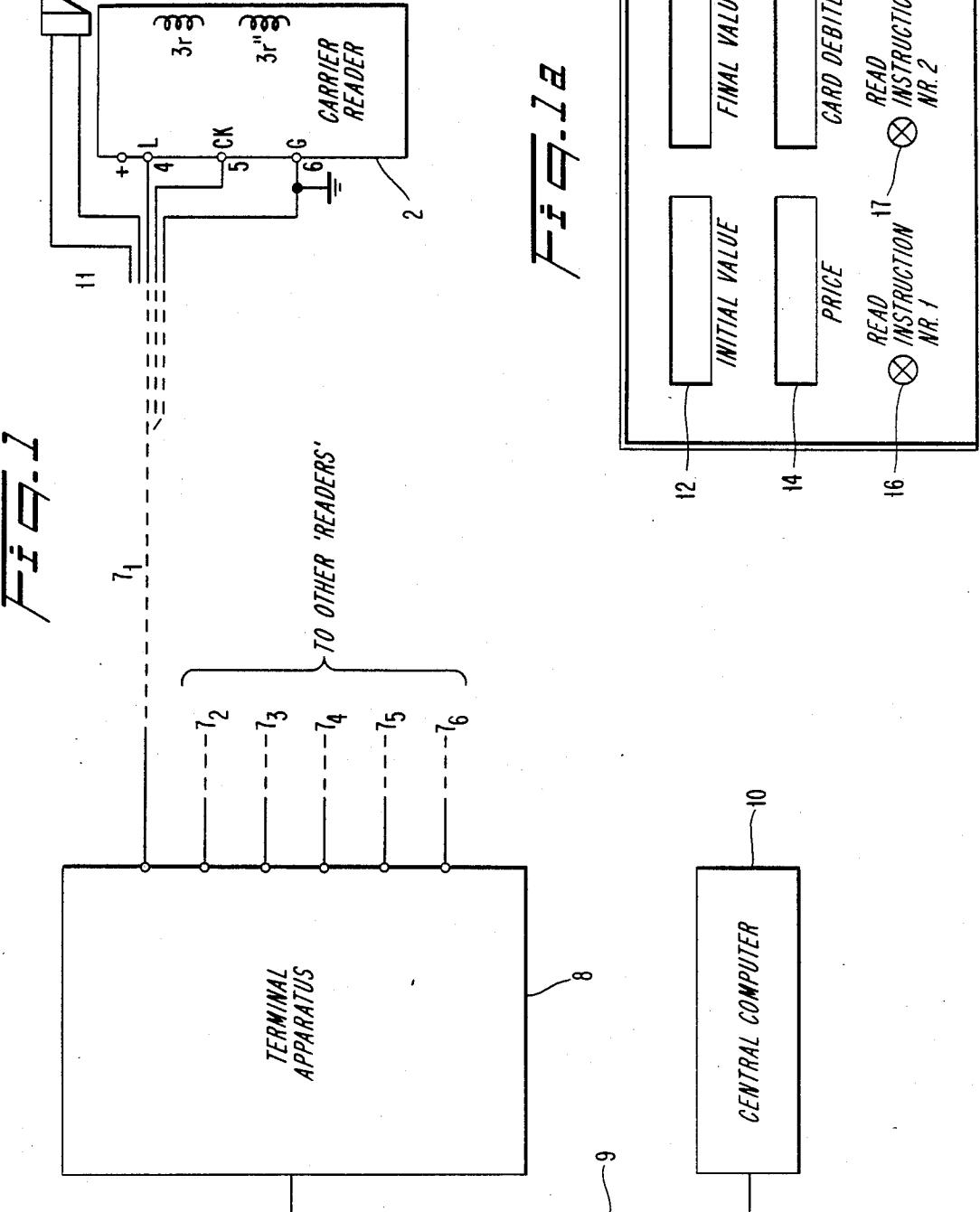
Primary Examiner—David L. Trafton
Attorney, Agent, or Firm—Burns, Doane, Swecker & Mathis

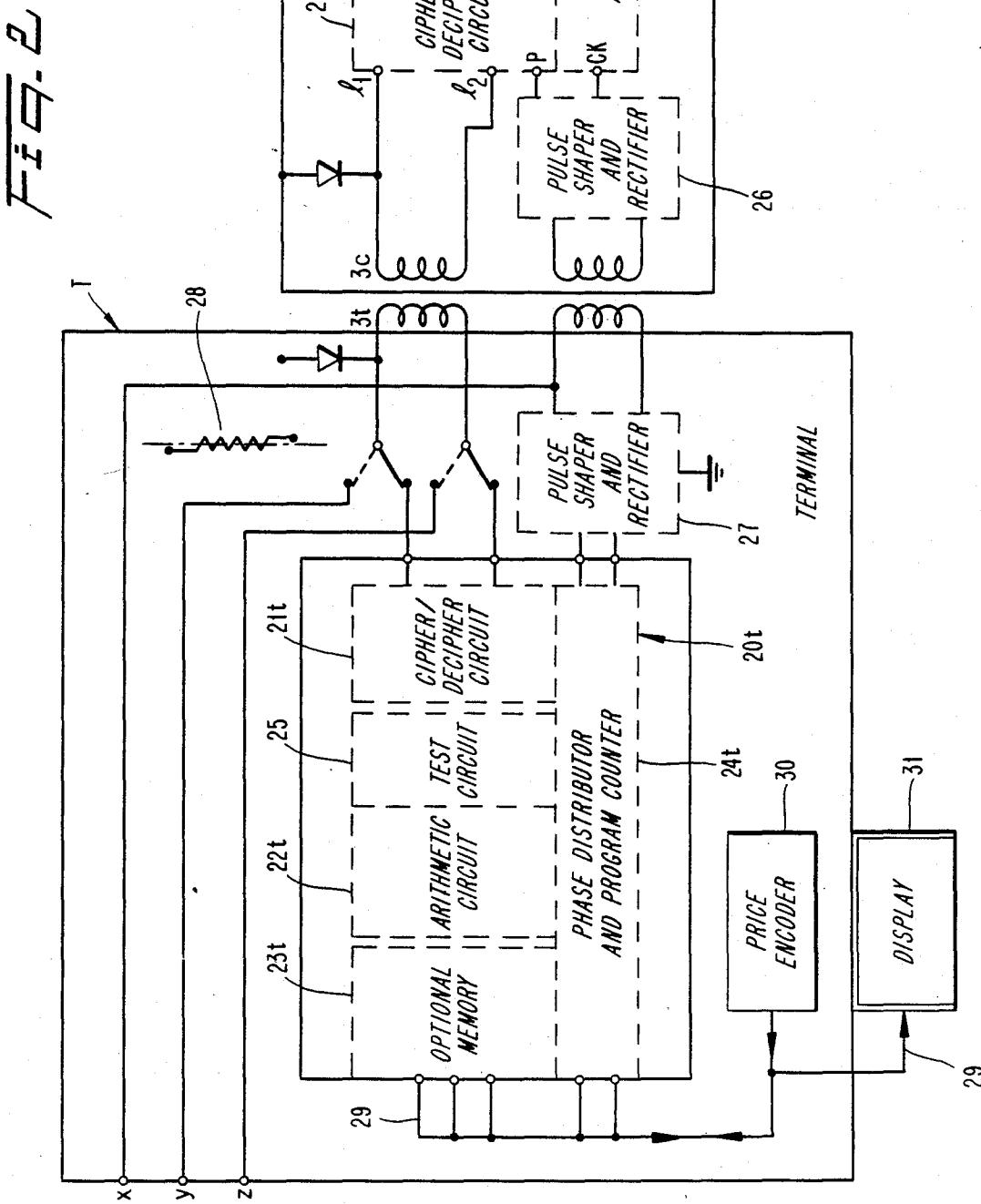
[57] **ABSTRACT**

The essence of the invention is a socalled "data bit variable spacer generator", block 79, which contributes to the generation of a control output "c" from a combinatory logic circuit group 78. The logic level of "c" determines whether a clear data bit from the parity flip flop FF6 is to be sent out or a random bit from block RMG. The 'variable data bit generator' is controlled by a number of parallel bit outputs from registers SH1 and SH2 which hold an encryption key after being conditioned by other logic inputs derived from key parity flip flops (FF1 and FF2) and clear data bit levels from block 81. Registers SH1 and SH2 shift and recirculate when the 'c' output is high. Similar principles are used when data are decrypted. The circuit is suitable for integration with other functions on a single substrate chip.

43 Claims, 10 Drawing Sheets







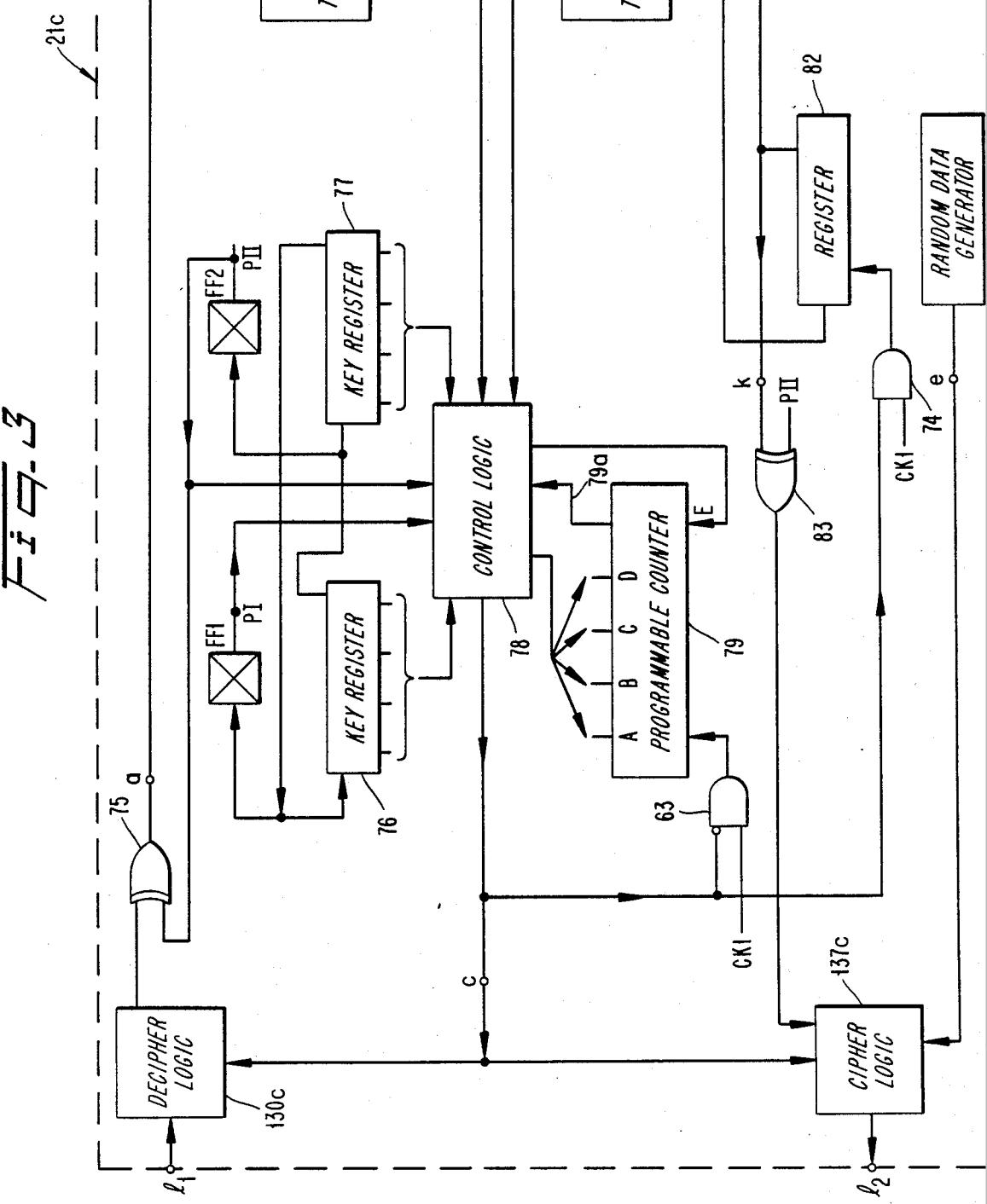
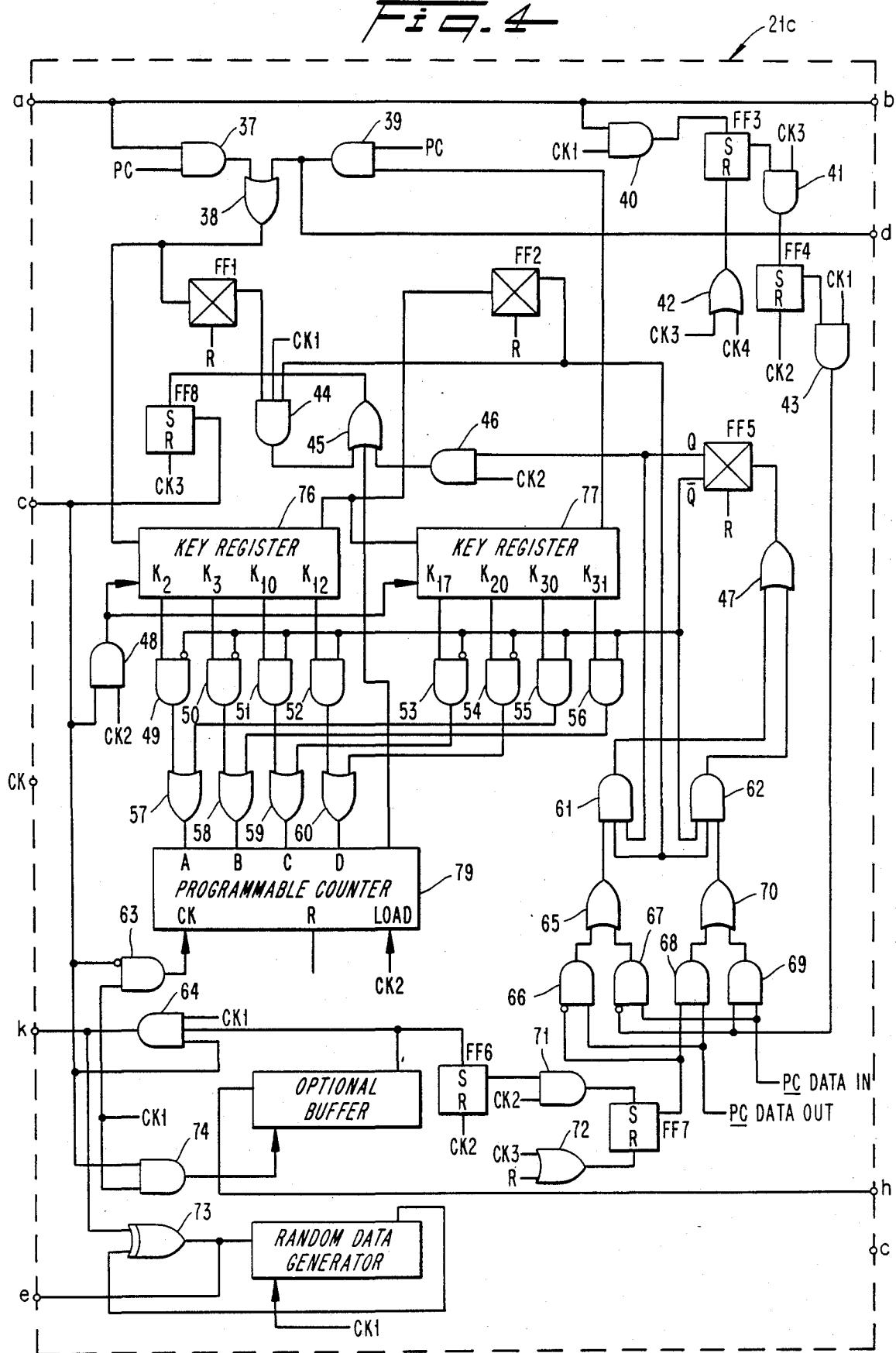


FIG. 4



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