

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent of Conley:

U.S. Patent No. 7,818,490

Issued: October 19, 2010

Title: PARTIAL BLOCK DATA  
PROGRAMMING AND  
READING OPERATIONS IN A  
NON-VOLATILE MEMORY

Petition for *Inter Partes* Review

Attorney Docket No.:  
337722-000080.490

Customer No.: 26379

Petitioner: Apple Inc.

Real Party in Interest: Apple Inc.

**DECLARATION OF DR. VIVEK SUBRAMANIAN**

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<b><u>Exhibit Number</u></b>	<b><u>Description</u></b>
1001	U.S. Patent No. U.S. Patent 7,818,490 (“the ’490 patent”)
1002	File History for U.S. Patent 7,818,490
1003	Declaration of Dr. Vivek Subramanian (“Subramanian Decl.”)
1004	CV of Dr. Vivek Subramanian
1005	U.S. Patent 5,822,781 (“Wells”)
1006	U.S. Patent No. 5,457,658 (Nijjima)
1007	U.S. Patent No. 5,627,783 (“Miyachi”)
1008	Flash Memories, edited by Cappelletti, et al (1999) (“Cappelletti”)
1009	PC Card Standard, Volumes 1 and 3 (1999) (“PC Card Standard”)
1010	PCT WO 99/35650 (“Hazen”)
1011	Designing With Flash Memory, Brian Dipert and Markus Levy (1994) (“Dipert”)

1. I, Vivek Subramanian, declare as follows:
2. I am making this Declaration at the request of Petitioner Apple Inc. regarding its Petition for *Inter Partes* Review of U.S. Patent No. 7,818,490 (the “490 patent”).
3. I am being compensated for my work at my standard rate of \$550 per hour. My compensation does not depend on the outcome of this proceeding.
4. As part of my analysis, I reviewed the following materials:

Exhibit 1001	U.S. Patent No. U.S. Patent 7,818,490
Exhibit 1002	File History for U.S. Patent 7,818,490
Exhibit 1005	U.S. Patent 5,822,781 (“Wells”)
Exhibit 1006	U.S. Patent No. 5,457,658 (Niijima)
Exhibit 1007	U.S. Patent No. 5,627,783 (“Miyuchi”)
Exhibit 1008	Flash Memories, edited by Cappelletti, et al (1999) (“Cappelletti”)
Exhibit 1009	PC Card Standard, Volumes 1 and 3 (1999) (“PC Card Standard”)
Exhibit 1010	PCT WO 99/35650 (“Hazen”)
Exhibit 1011	Designing With Flash Memory, Brian Dipert and Markus Levy (1994) (“Dipert”)

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