

Paper No. \_\_\_\_\_  
Filed: May 24, 2016

UNITED STATES PATENT AND TRADEMARK OFFICE

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BEFORE THE PATENT TRIAL AND APPEAL BOARD

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Samsung Electronics Co., Ltd., and  
Samsung Electronics America, Inc.  
Petitioner

v.

Parthenon Unified Memory Architecture LLC  
Patent Owner

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Case IPR2015-01944  
Patent No. 5,812,789

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**PETITIONERS' CURRENT LIST OF EXHIBITS**

## LIST OF EXHIBITS

Exhibit	Description	Previously Submitted
Ex. 1001	U.S. Patent No. 5,812,789 (“the ’789 patent”)	X
Ex. 1002	File History for U.S. Patent No. 5,812,789	X
Ex. 1003	Reserved	X
Ex. 1004	ISO/IEC 11172-2:1993: Information technology— Coding of moving pictures and associated audio for digital storage media at up to about 1,5 Mbit/s—Part 2: Video,” (1 <sup>st</sup> ed. August 1, 1993) (“ <i>MPEG Standard</i> ”)	X
Ex. 1005	S. Rathnam <i>et al.</i> , “An Architectural Overview of the Programmable Multimedia Processor, TM-1,” IEEE Proceedings of COMPCON ’96, pp. 319-326 (1996) (“ <i>Rathnam</i> ”)	X
Ex. 1006	R.J. Gove, “The MVP: A Highly-Integrated Video Compression Chip,” Proceedings of the IEEE Data Compression Conference (DCC ’94), pp. 215-224 (March 29-31, 1994).	X
Ex. 1007	U.S. Patent No. 5,774,676 (“ <i>Stearns</i> ”)	X
Ex. 1008	Reserved	X
Ex. 1009	Reserved	X
Ex. 1010	WorldCat Entry for <i>Rathnam</i>	X
Ex. 1011	Patent Owner Claim Construction Brief in Case No. 2:14-cv-690, April 7, 2015	X
Ex. 1012	Patent Owner Claim Construction Brief in Case No. 2:14-cv-902, June 18, 2015	X
Ex. 1013	Reserved	X
Ex. 1014	Brad Hansen, <i>The Dictionary of Multimedia</i> , 1997	X
Ex. 1015	U.S. Patent No. 8,681,164	X
Ex. 1016	Excerpt of File History for U.S. Patent No. 8,681,164	X
Ex. 1017	Reserved	X
Ex. 1018	Reserved	X
Ex. 1019	Shanley, et al., “PCI System Architecture,” Addison-	X

<b>Exhibit</b>	<b>Description</b>	<b>Previously Submitted</b>
	Wesley Publishing Company, 1995 (3rd ed.) (" <i>Shanley</i> ")	
Ex. 1020	Stone, H., "Microcomputer Interfacing," Addison-Wesley Publishing Company, 1982	X
Ex. 1021	Reserved	X
Ex. 1022	Reserved	X
Ex. 1023	U.S. Patent No. 5,797,028 (" <i>Gulick 028</i> ")	X
Ex. 1024	"Accelerated Graphics Port Interface Specification," Intel Corporation, July 31, 1996 (Revision 1.0) (" <i>AGP</i> ")	X
Ex. 1025	VESA Unified Memory Architecture Hardware Specifications Proposal," Version 1.0p (" <i>VUMA</i> ")	X
Ex. 1026	Reserved	X
Ex. 1027	Reserved	X
Ex. 1028	Reserved	X
Ex. 1029	Curriculum Vitae of Dr. Harold Stone	X
Ex. 1030	Expert Declaration of Dr. Harold Stone (" <i>Stone Decl.</i> ")	X
Ex. 1031	Reserved	X
Ex. 1032	U.S. Patent No. 5,682,484 (" <i>Lambrecht</i> ")	X
Ex. 1033	Reserved	X
Ex. 1034	Slavenburg, G., "The TriMedia VLIW-Based PCI Multimedia Processor," Microprocessor Forum 1995, Oct. 10-11, 1995 (" <i>Slavenburg</i> ")	X
Ex. 1035	G. Moore, "Cramming more components onto integrated circuits," Electronics, Vol. 38, No. 8, Apr. 19, 1965 (" <i>Moore</i> ")	X
Ex. 1036	U.S. Patent No. 5,579,052 (" <i>Artieri</i> ")	X
Ex. 1037	Reserved	X
Ex. 1038	Reserved	X
Ex. 1039	Reserved	X
Ex. 1040	Reserved	X
Ex. 1041	Settlement Agreement	

Respectfully submitted,

Dated: May 24, 2016

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## CERTIFICATE OF SERVICE

Pursuant to 37 C.F.R. § 42.6(e), I certify that I caused to be served on counsel a true and correct copy of the foregoing Petitioners' Current List of Exhibits by electronic means on the date below at the following address of record:

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