

(12) United States Patent Burrus, IV

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(54) CABLE OR MODULE IDENTIFICATION APPARATUS AND METHOD

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patent is extended or adjusted under 35

U.S.C. 154(b) by 539 days.

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U.S. Cl. 455/559; 320/106; 307/125

455/550.1, 559, 572-573; 307/112, 125; 320/106-110, 112, 125; 324/66

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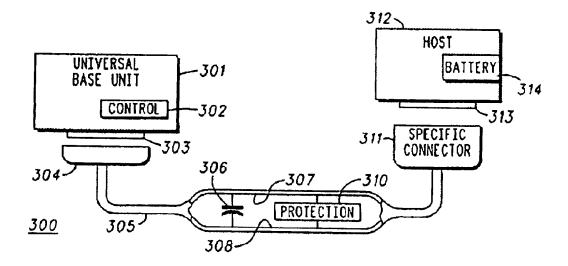
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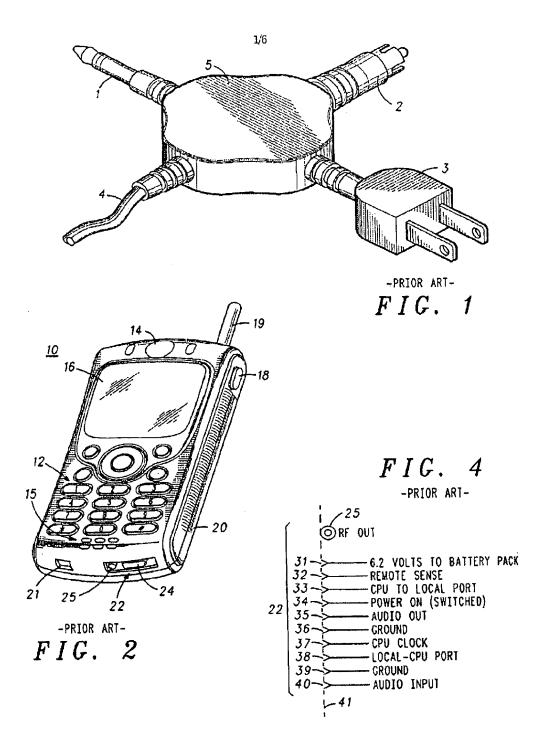
(57)**ABSTRACT**

This invention includes a universal base unit having a plurality of cables, each corresponding to a unique electronic host device. The cable includes a capacitor having a value corresponding to a particular electronic host device. The capacitor is coupled in series with a resistor to ground in the universal base unit. The cable further includes a unique mating connector for coupling to the electronic host device. The universal base unit identifies the cable by actuating a step function across the capacitor-resistor network, causing an exponentially decaying waveform. A microprocessor in the host device then measures the width of the waveform to determine the identity of the cable or host device. The system allows a user to carry a single power supply or universal base unit with multiple cables as opposed to having to transport a different accessory for each electronic device.

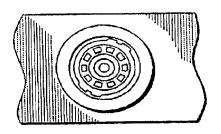
9 Claims, 6 Drawing Sheets



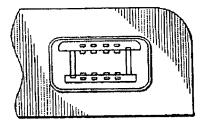








-PRIOR ART-FIG. 3A



-PRIOR ART-FIG. 3B

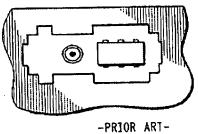


FIG. 3C

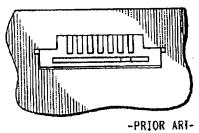


FIG. 3D

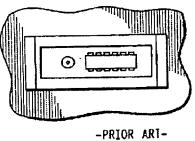


FIG. 3E

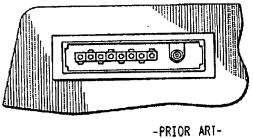


FIG. 3F

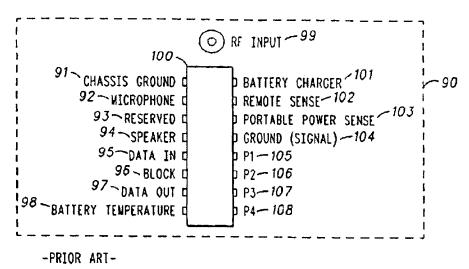
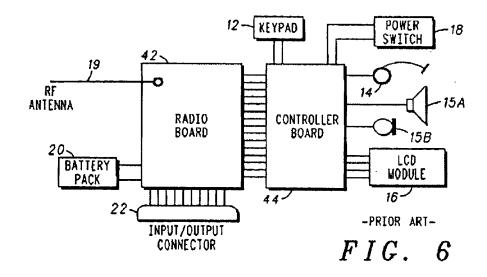
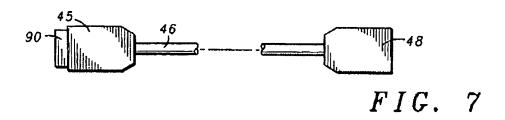
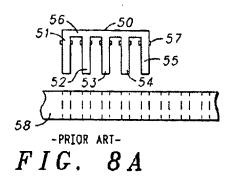
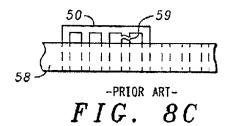


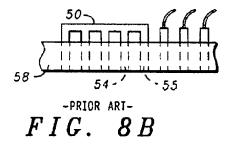
FIG. 5

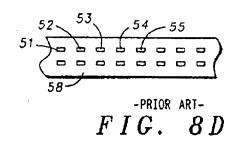


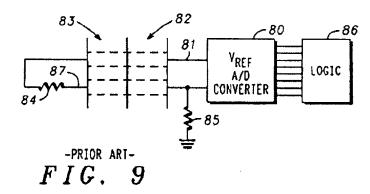














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