



[54] **UNIVERSAL POWER-SUPPLY CONNECTION SYSTEM FOR MULTIPLE ELECTRONIC DEVICES, AND DEVICES FOR USE THEREWITH**

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[21] Appl. No.: **08/739,460**

[22] Filed: **Oct. 29, 1996**

Related U.S. Application Data

[63] Continuation-in-part of application No. 08/198,994, Feb. 18, 1994, Pat. No. 5,570,002.

[51] **Int. Cl.⁷** **G05F 1/56**

[52] **U.S. Cl.** **323/283; 307/31**

[58] **Field of Search** **323/234, 283, 323/285, 322; 307/11, 31, 33**

[56] **References Cited**

U.S. PATENT DOCUMENTS

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[57] **ABSTRACT**

The system provides electricity to operate any of a multiplicity of device and most typically receives power from a source and passes power to any of the devices. The system also accepts electronic-device identification information for any particular one of such devices. The system also has one or more programmed digital electronic microprocessors that use the identification information to select power parameters—for passage of power from the source to the powered device. The system activates its own power-passing capability to apply power to each device according to the power parameters selected by the microprocessor. A cable, which for some purposes may regarded as part of the system and for some purposes is a separate invention, connects the microprocessor and power-passing parts of the system to the powered device; the identification information is held either in the cable—typically in a ROM, PROM, EPROM or other memory chip in one of the end connectors of the cable—or in the device to be powered, or in a power adapter which is at either end of the cable or within the device to be powered, and is read by the system microprocessor before power is supplied. In some forms of the invention, a power adapter preferably receives a. c. power from the system and derives, from the a. c. power, power in a modified form for operation of the device.

41 Claims, 7 Drawing Sheets

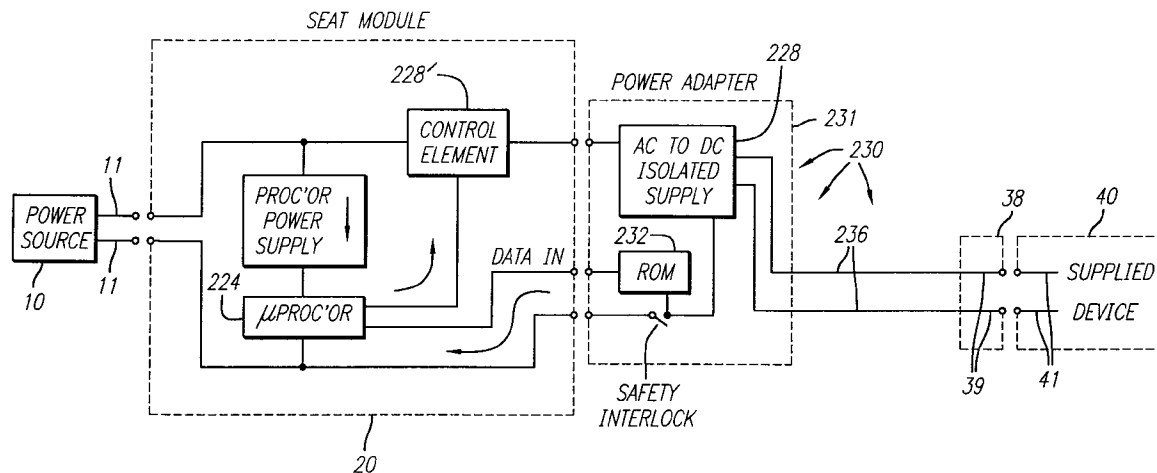


FIG. 1

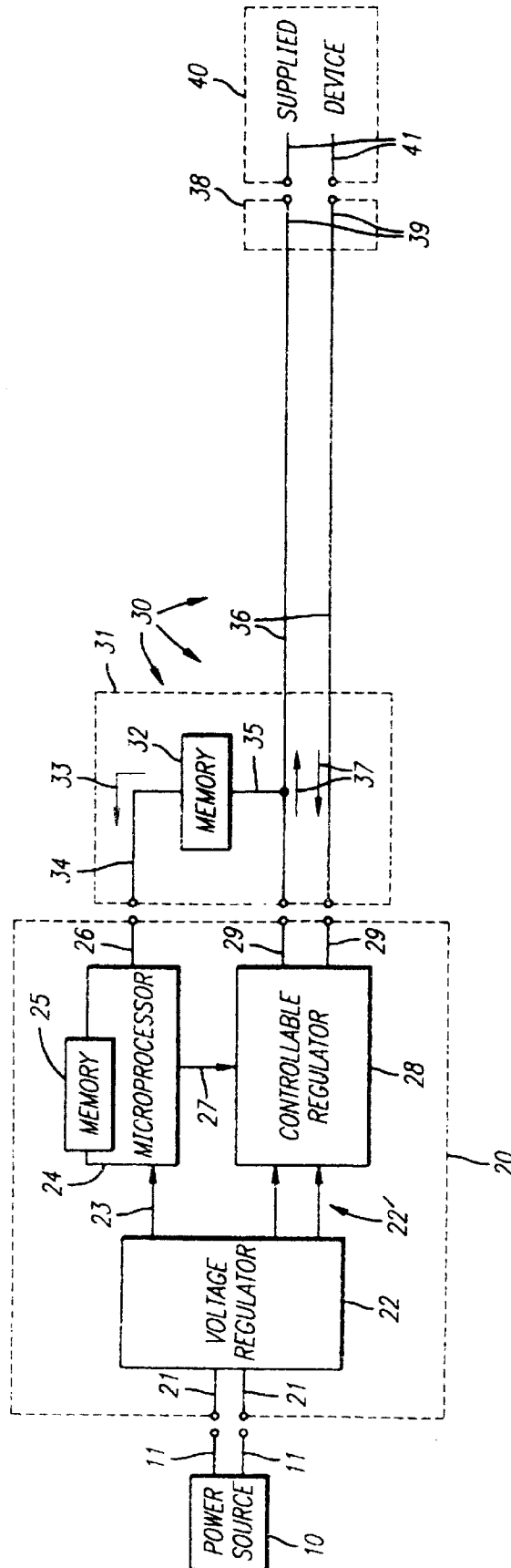


FIG. 2

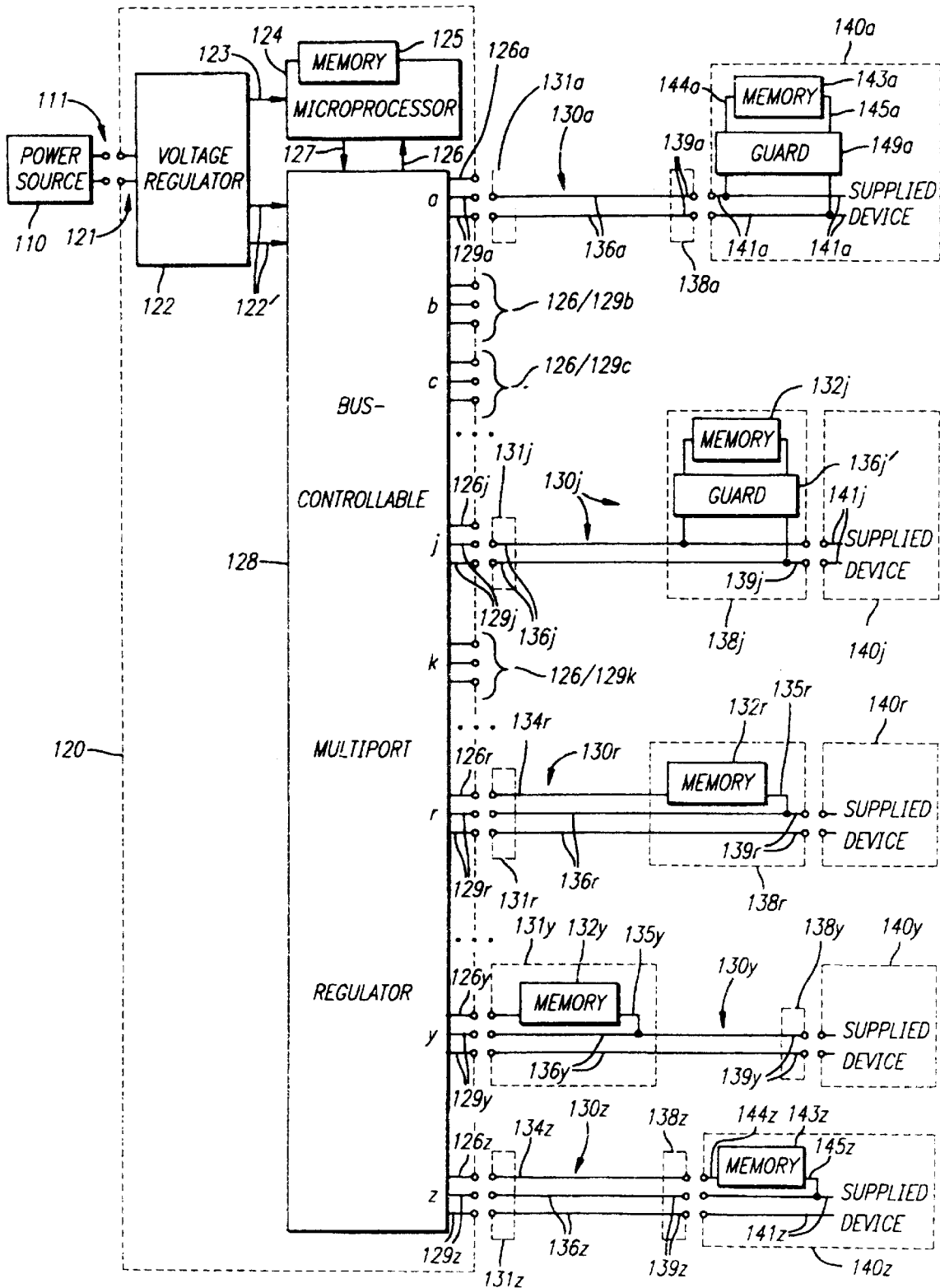


FIG. 3

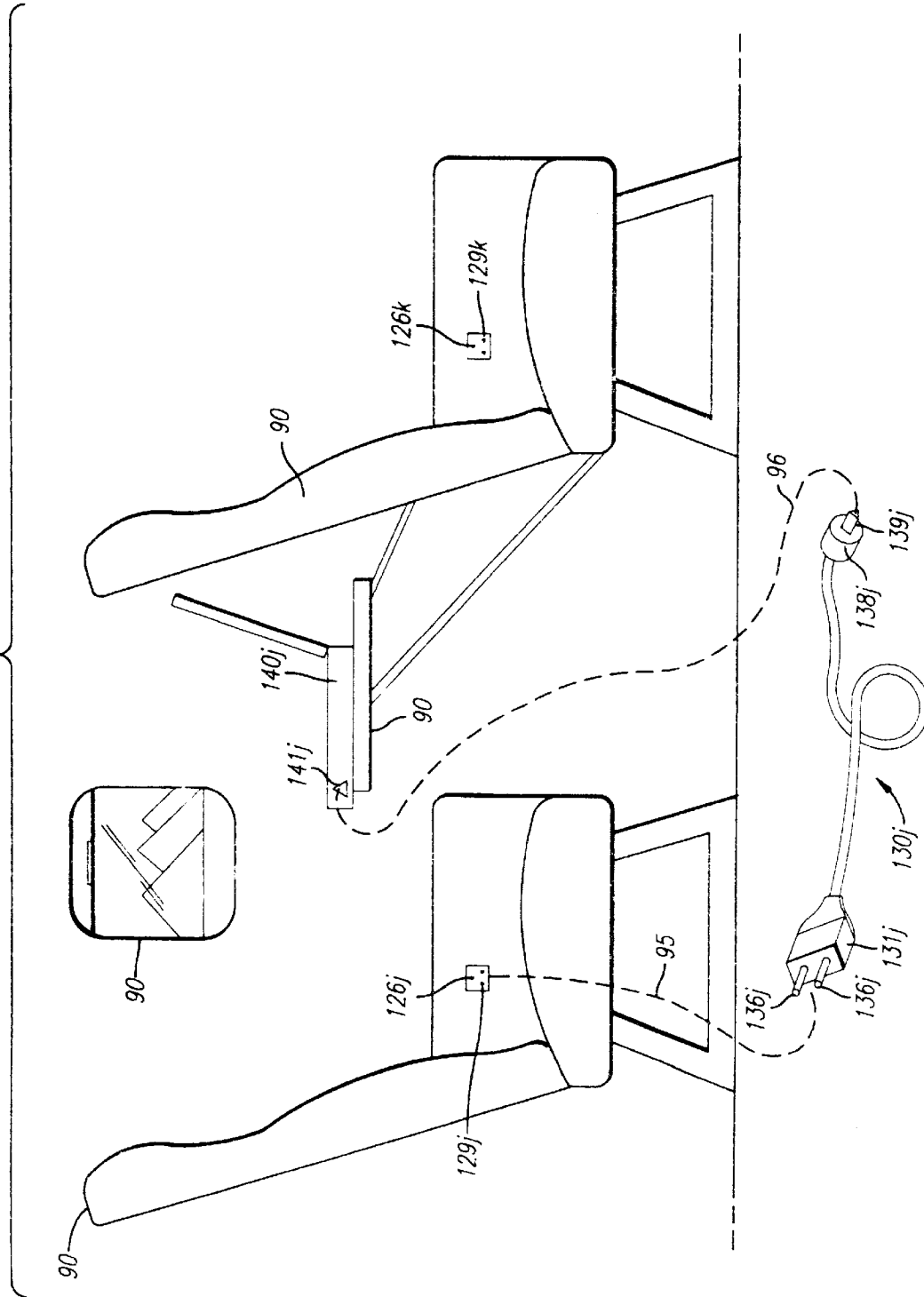
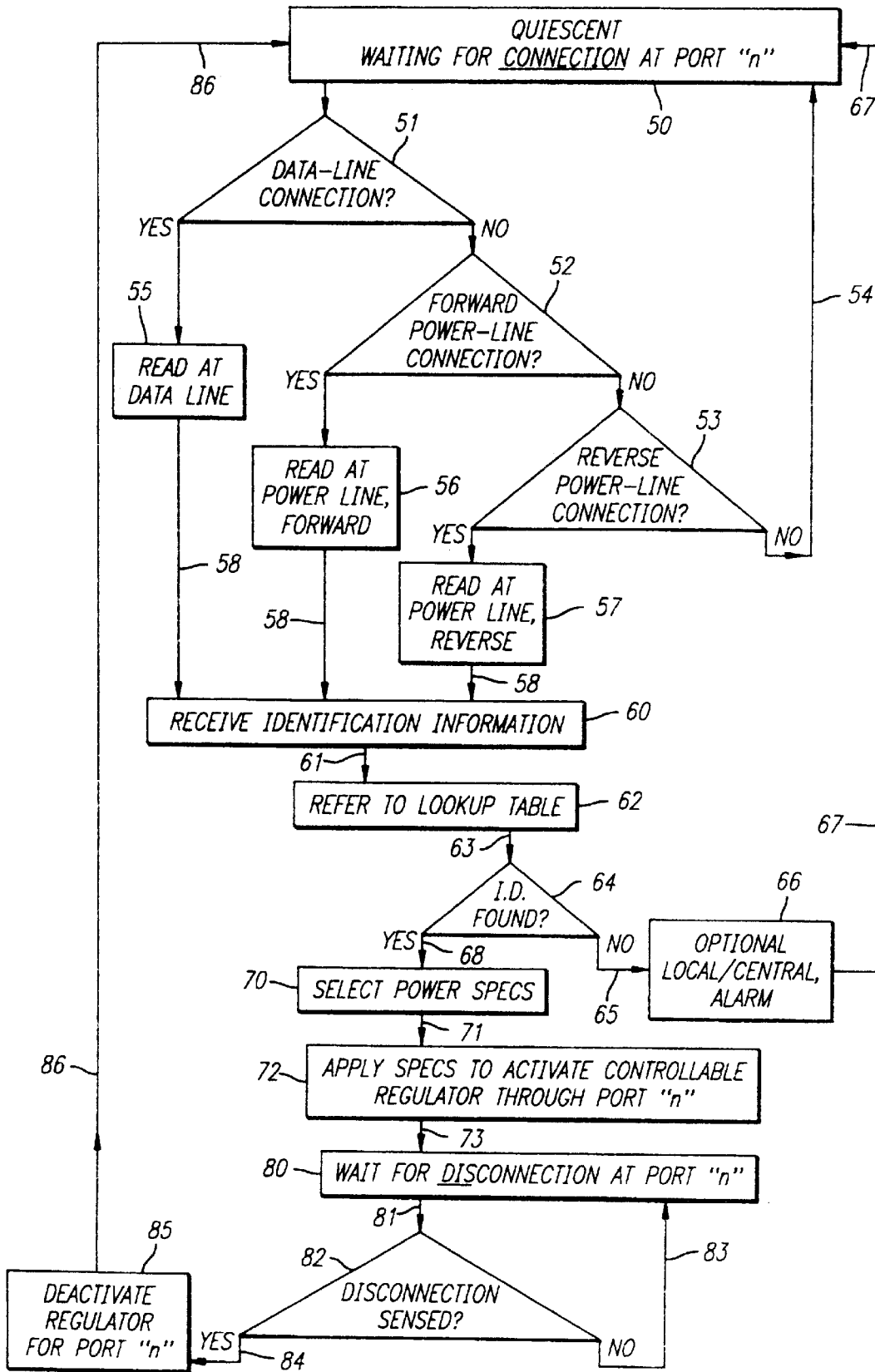


FIG. 4



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