#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#### BEFORE THE PATENT TRIAL AND APPEAL BOARD

#### SMART MODULAR TECHNOLOGIES INC.

Petitioner

v.

### JAMES B. GOODMAN

**Patent Owner** 

Case No. TBD

Patent 6,243,315

## PETITION FOR *INTER PARTES* REVIEW OF CLAIMS 1, 5, 10, AND 16 OF U.S. PATENT NO. 6,243,315

#### **VIA PRPS**

Patent Trial and Appeal Board United States Patent and Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450

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1002	Declaration of Nader Bagherzadeh
1003	Plaintiff's Amended Infringement Chart For U.S. Patent No. 6,243,315
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1004	U.S. Patent No. 5,600,605 to Schaefer
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1006	U.S. Patent No. 5,204,840 to Mazur
1007	JEDEC DDR SDRAM Specification JESD79 R1, June 2000
1008	JEDEC DDR2 SDRAM Specification JESD79-2F, November 2009
1009	Micron MT48LC2M8S1(S) Datasheet, April 1994
1010	Micron MT48LC2M8S1(S) Specification Sheet, June 1993
1011	Dkt. 7 From Case No. 4:14-cv-01380 GHM (S.D. Tex.)
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