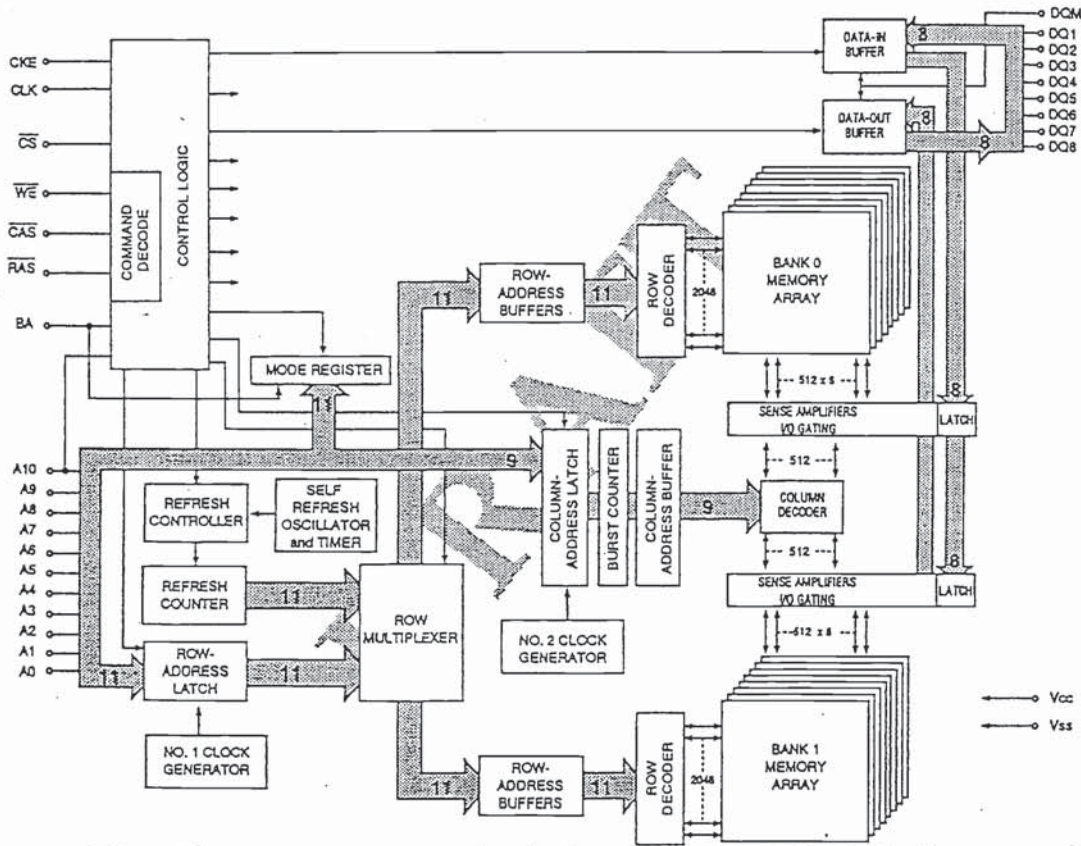


SMART EXHIBIT 1010

NEW SYNCHRONOUS DRAM

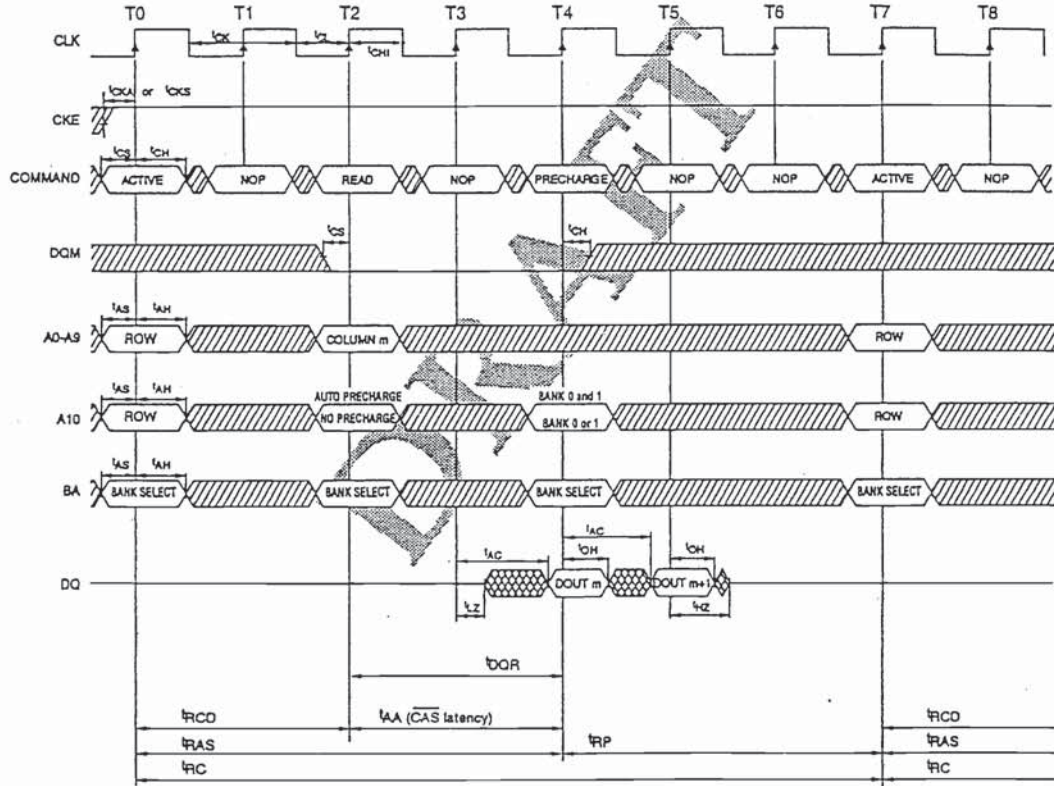
FUNCTIONAL BLOCK DIAGRAM



Command truth table

Function	Symbol	CKE		CS	RAS	CAS	WE	A11	A10	A9-0
		n-1	n							
Device Deselect	DESL	H	X	H	X	X	X	X	X	X
No Operation	NOP	H	X	L	H	H	H	X	X	X
Burst stop	BST	H	X	L	H	H	L	X	X	X
Read	READ	H	X	L	H	L	H	V	L	V
Read with Auto Precharge	READA	H	X	L	H	L	H	V	H	V
Write	WRIT	H	X	L	H	L	L	V	L	V
Write with Auto Precharge	WRITA	H	X	L	H	L	L	V	H	V
Bank Activate	ACT	H	X	L	L	H	H	V	V	V
Precharge select Bank	PRE	H	X	L	L	H	L	V	L	X
Precharge All Banks	PALL	H	X	L	L	H	L	X	H	X
Mode register set	MRS	H	X	L	L	L	L	L	L	V

READ - 2 CYCLE BURST



Clocks depict -10 Speed Device at 66MHz

▨ DON'T CARE
▩ UNDEFINED

