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United States Patent [19]

Mazur

[56]

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[54]		ND METHODS FOR PRESERVING ROCESSOR MEMORY
[76]	Inventor:	Jeffrey G. Mazur, 8041 Sadring Ave., Canoga Park, Calif. 91304
[21]	Appl. No.:	391,096
[22]	Filed:	Aug. 8, 1989
[51]	Int. Cl.5	G11C 5/14; G06F 12/14;
		G06F 12/16
[52]	U.S. Cl	365/228 ; 365/229;
	395/425	; 364/DIG. 1; 364/969.4; 364/DIG. 2

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364/969.3; 365/226, 227, 228, 229

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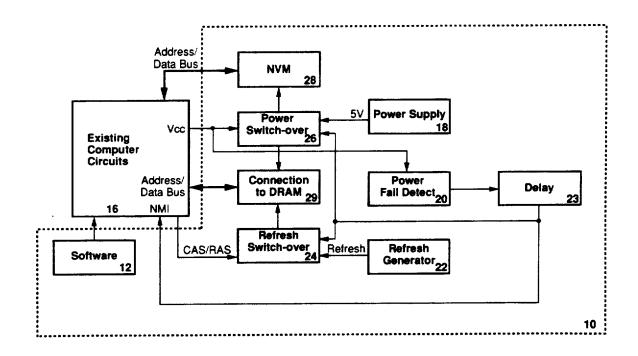
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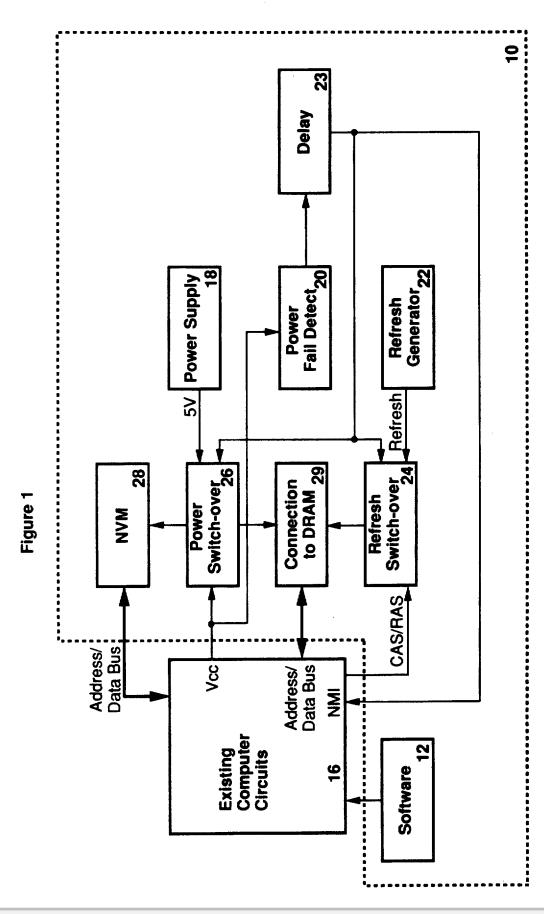
Primary Examiner-Alyssa H. Bowler

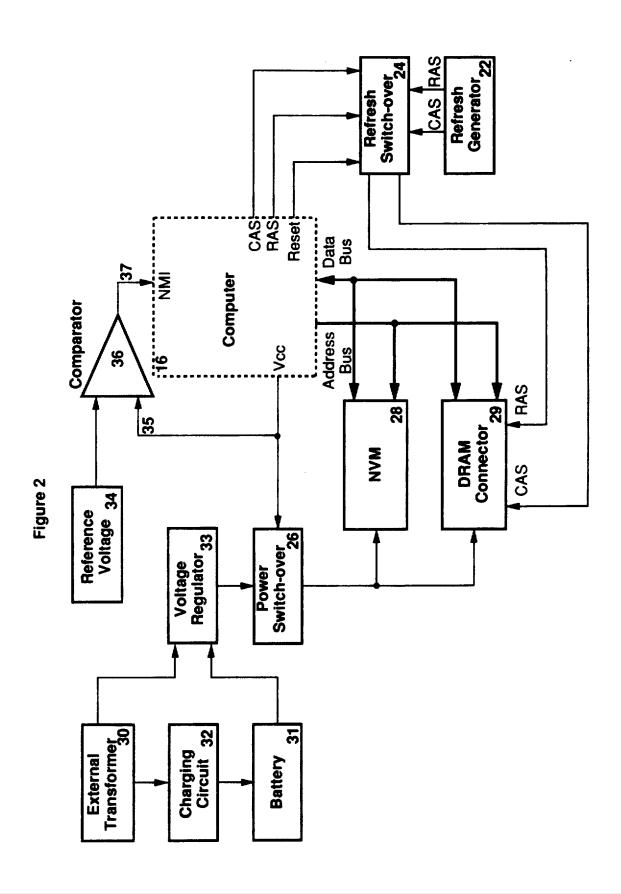
[57] ABSTRACT

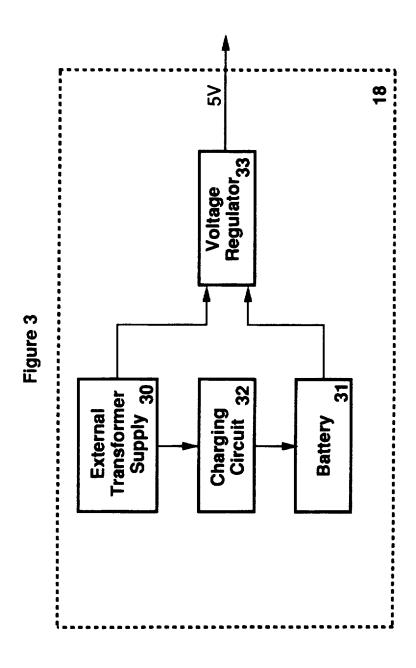
Means and methods for preserving the RAM of an externally powered microprocessor on the occasion of a loss in external power. When the power loss is detected, a signal is generated which initiates a sequence to isolate the RAM and refresh it with an independent power supply. When main power is restored, the microprocessor is restored to its precise location at the moment of power loss. Shutdown, startup and security routines are provided by software embodied in the system.

5 Claims, 4 Drawing Sheets









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