

US005521922A

Date of Patent:

Patent Number: 5,521,922

May 28, 1996

Fujinami et al.

[54] DATA DEMULTIPLEXER

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- [21] Appl. No.: 245,768
- [22] Filed: May 18, 1994
- [30] Foreign Application Priority Data

May 25, 1993 [JP] Japan 5-122269

- [51] Int. Cl.⁶ H04J 3/06

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Primary Examiner-Douglas W. Olms

Assistant Examiner-Russell W. Blum

[11]

[45]

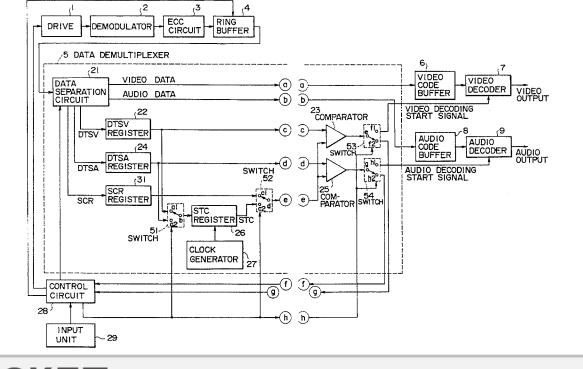
[57]

Attorney, Agent, or Firm-William S. Frommer; Alvin Sinderbrand

ABSTRACT

A data demultiplexer for separating multiplex data including at least coded video data, coded audio data, timing data indicative of a video decoding start time, timing data indicative of an audio decoding start time, and timing data indicative of a system clock reference time. The demultiplexer comprises a circuit for separating the multiplex data into the component data, and comparators for comparing input data with the reference timing data. When the timing data indicative of a video decoding start time is inputted and detected, the video data is written in a video code buffer at the maximum transfer rate. During such time period, the reference timing data indicative of the current time is monitored, and the operation of decoding the video data is started when the reference timing data has become equal to the video timing data. A similar operation is performed with regard to the audio data as well, so that the video and audio data can be inputted fast to consequently enhance the responsivity at the start of reproduction.

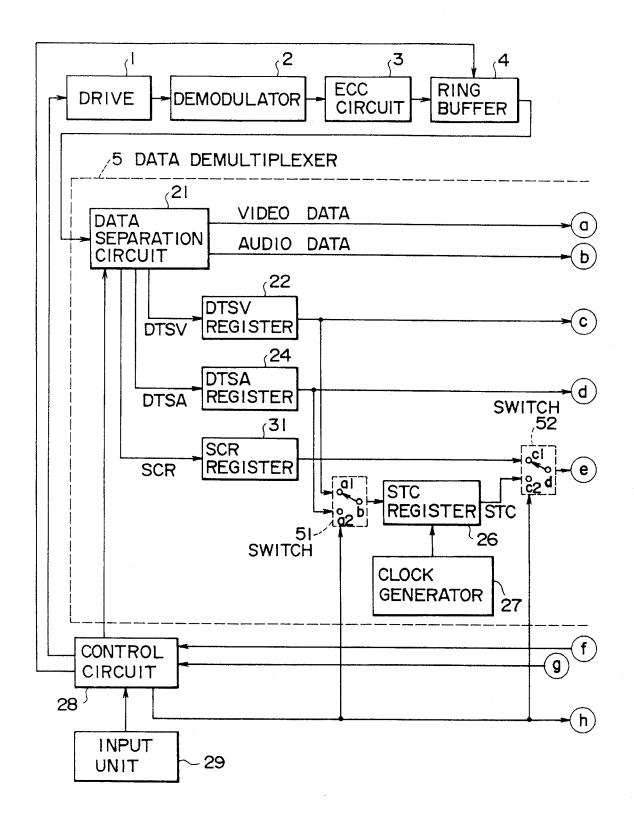
16 Claims, 11 Drawing Sheets



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FIG. 1(A)



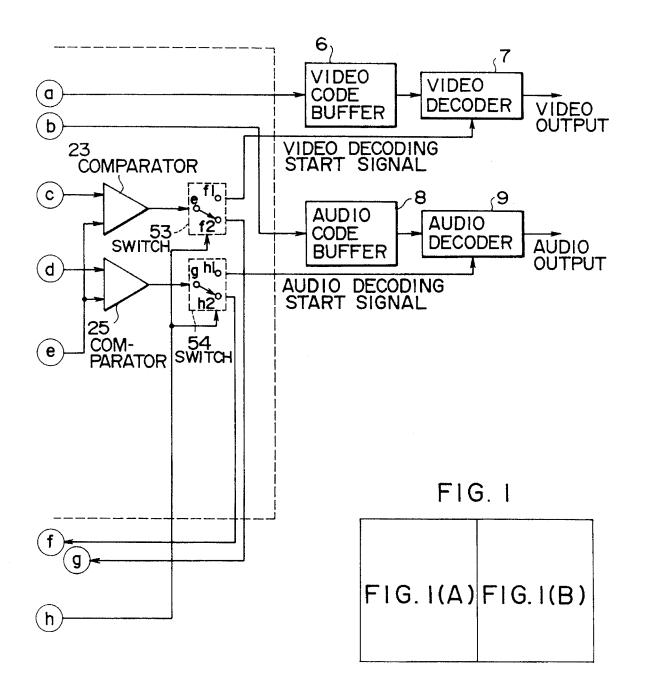
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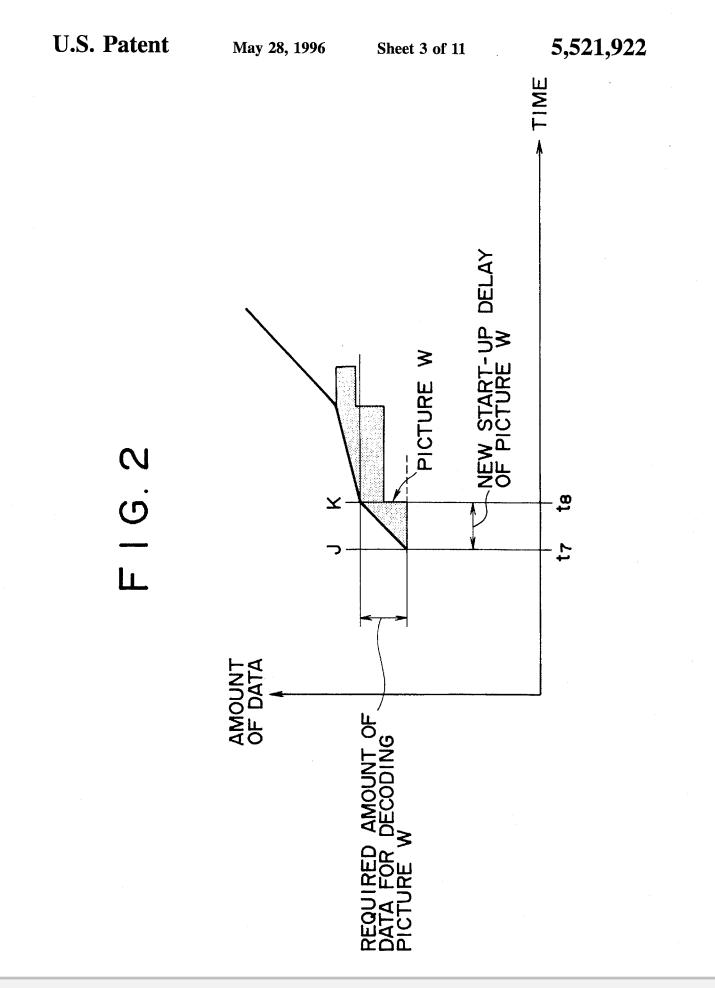
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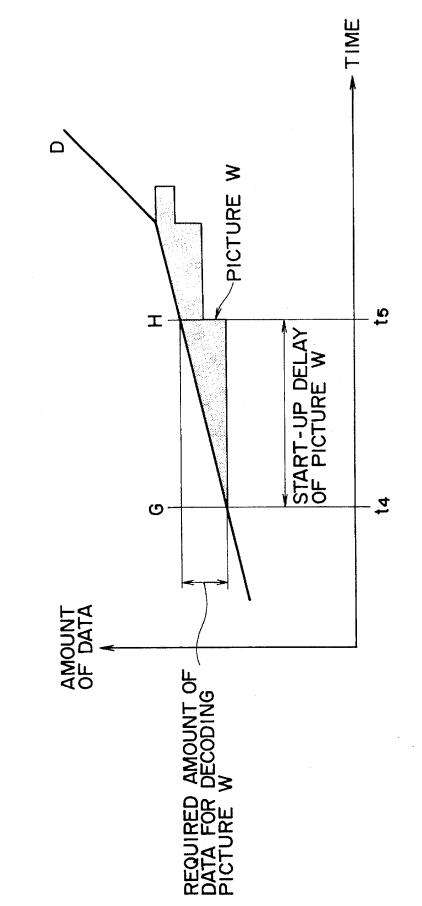
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