

## United States Patent [19]

Casorso et al.

#### [54] METHOD AND APPARATUS FOR ENSURING DATA INTEGRITY IN A DYNAMICALLY MAPPED DATA STORAGE SUBSYSTEM

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- [73] Assignee: Storage Technology Corporation, Louisville, Colo.
- [21] Appl. No.: 919,897
- [22] Filed: Jul. 27, 1992
- [51] Int. Cl.<sup>6</sup> ...... H03M 13/00

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**Patent Number:** 

**Date of Patent:** 

[11]

[45]

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Apr. 4, 1995

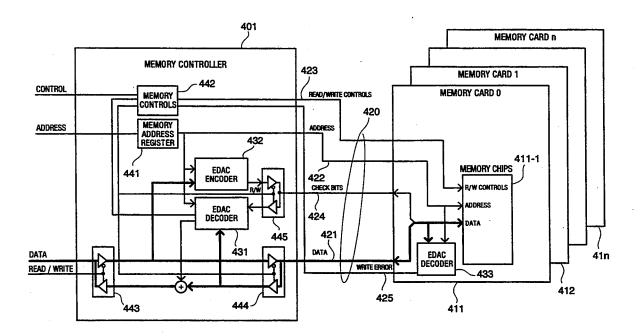
Primary Examiner—Charles E. Atkinson Assistant Examiner—Phung Chung

Attorney, Agent, or Firm-Duft, Graziano & Forest

#### [57] ABSTRACT

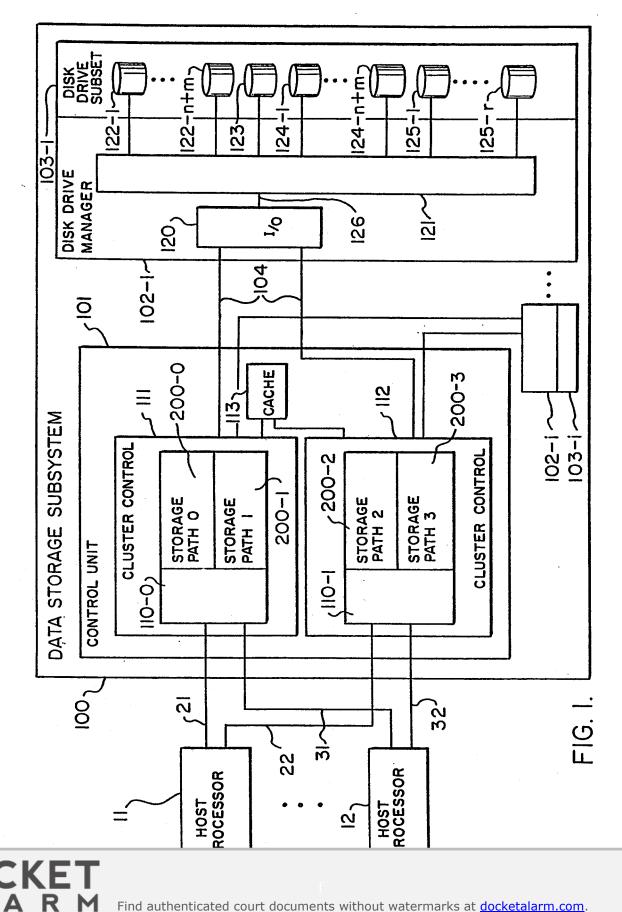
The dynamically mapped data storage subsystem generates a two error correction, three error detection code of extent sufficient to cover not only the data but also the corresponding memory address for each data record stored therein. The error correction code is transmitted and stored with the data within the data storage subsystem to ensure the integrity of both the data and its memory address.

#### 10 Claims, 6 Drawing Sheets



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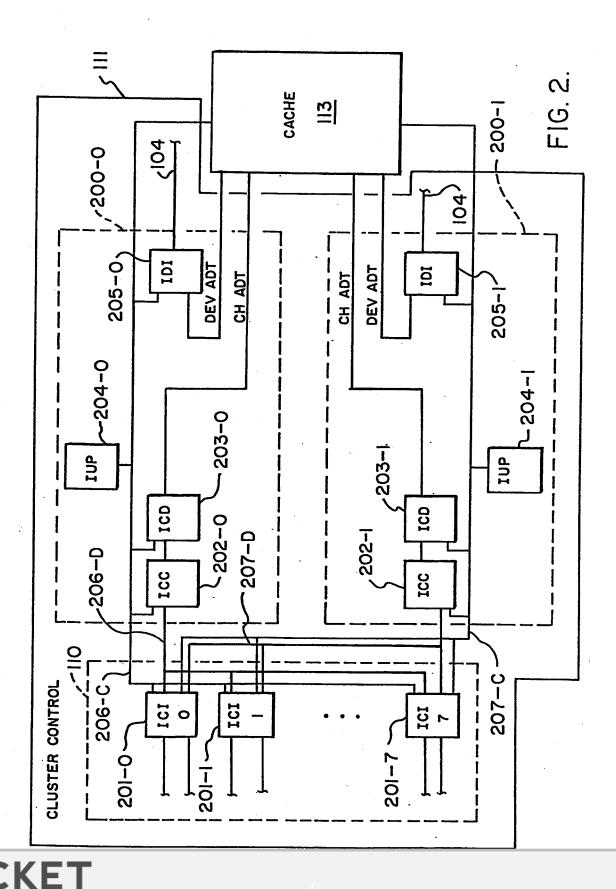
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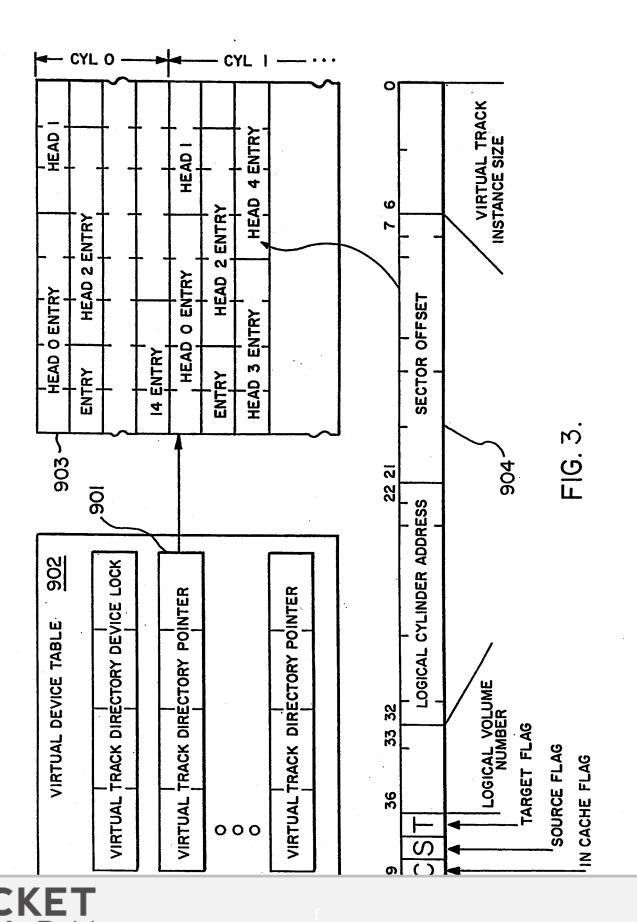


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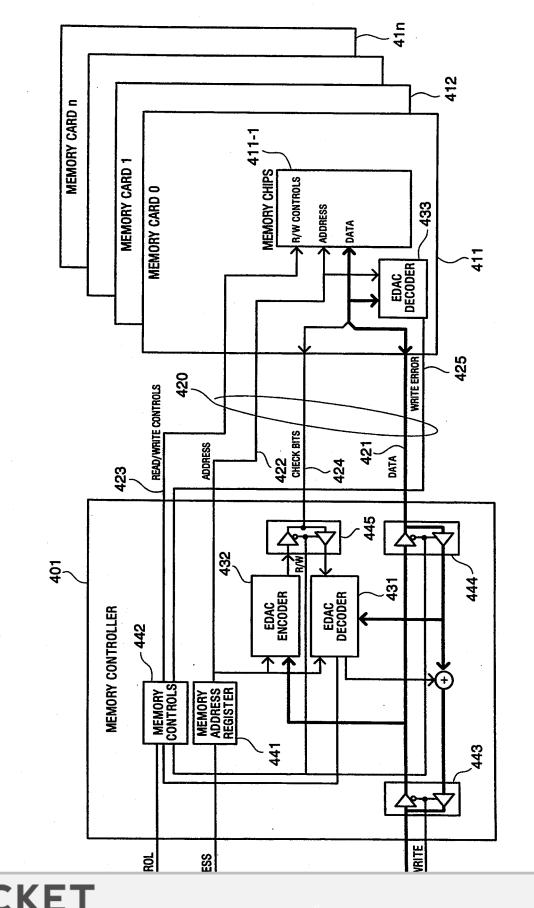


FIG. 4

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