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UNITED STATES	S PATENT AND TRA	DEMARK OFFICE
BEFORE THE P	PATENT TRIAL AND	— APPEAL BOARD

# PETITION FOR *INTER PARTES* REVIEW OF U.S. PATENT NO. 7,477,624

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		1.	Ove	rview of Gerten9	)
		2.	Gert	en anticipates independent claim 1	
			a.	Gerten discloses "[a] communications device for use in a network of devices" [1P]11	
			b.	Gerten discloses "a memory for storing instructions" [1A] and "a processor that is communicatively coupled to the memory, wherein the memory includes instructions which, when processed by the processor, causes" [1B] a set of actions to occur.	
			c.	Gerten discloses "selecting, based upon performance of a plurality of communications channels at a first time, a first set of two or more communications channels from the plurality of communications channels" [1Bi]	3



d.	Gerten discloses "selecting, based upon performance of the plurality of communications channels at a second time that is later than the first time, a second set of two or more communications channels from the plurality of communications channels" [1Bii].
e.	Gerten discloses "a transceiver that is communicatively coupled to the memory and that is configured to transmit to and receive from another communications device" [1C].
f.	Gerten discloses "for a first period of time, the first set of two or more communications channels is used to transmit to and receive from the other communications device" [1Di] and "for a second period of time that is after the first period of time, the second set of two or more communications channels is used to transmit to and receive from the other communications device instead of the first set of two or more communications channels."  [1Dii]
g.	Gerten discloses "wherein the communications device is a first communications device, the other communications device is a second communications device, a default set of two or more communications channels is associated with a hopping sequence and is not changed based on the performance of the plurality of communications channels" [1E].
h.	Gerten discloses "the transceiver is configured to transmit to and receive from a third communications device over the default set of two or more communications channels while transmitting to and receiving from the second communications device over the first set of two or more communications channels and while transmitting to and receiving from the second communications device over the second set of two or more communications channels" [1F]



	3.	Gerte	en anticipates claim 4	19
		a.	Gerten discloses "the memory further includes instructions, which when processed by the processor, causes: after selecting the first set of two or more communications channels, causing the first set of two or more communications channels to be loaded into a first register of the communications device and a second register of the other communications device" [4A].	19
		b.	Gerten discloses "the memory further includes instructions, which when processed by the processor, causes after selecting the second set of two or more communications channels, causing the second set of two or more communications channels to be loaded into the first register of the communications device and the second register of the other communications device" [4B].	21
	4.	Gerte	en anticipates claims 13, 16, 25, 28 and 29	22
В.			The combination of Gerten and Cuffaro renders 14, 15, 26, and 27 obvious.	26
	1.	Gerte	en/Cuffaro renders claims 2, 14, and 26 obvious	26
		a.	Gerten/Cuffaro discloses the hopping sequence claim limitations of claims 2, 14, and 26	26
		b.	Gerten/Cuffaro discloses "the performance of the plurality of communications channels is based on channel performance data that is transmitted over one or more of the plurality of communications channels based on the hopping sequence according to the frequency hopping protocol" of claims 2, 14, and 26.	28
		c.	A POSITA would have combined Gerten and Cuffaro.	30
	2.	Gerte	en/Cuffaro renders claims 3, 15, and 27 obvious	32
C.			The combination of Gendel and Haartsen renders , 4, 13, 15, 16, 25, 27, 28, and 29 obvious	37



1.	Overview of Gendel37
	The combination of Gendel and Haartsen renders claim 1 obvious
8	a. Gendel discloses "[a] communications device for use in a network of devices" [1P]
ł	o. Gendel/Haartsen discloses "a memory for storing instructions" [1A] and "a processor that is communicatively coupled to the memory, wherein the memory includes instructions which, when processed by the processor, causes" [1B] a set of actions to occur.
(	c. Gendel discloses "selecting, based upon performance of the plurality of communications channels at a first time, a first set of two or more communications channels from the plurality of communications channels" [1Bi]
(	d. Gendel discloses "selecting, based upon performance of the plurality of communications channels at a second time that is later than the first time, a second set of two or more communications channels from the plurality of communications channels" [1Bii]
•	e. Gendel discloses "a transceiver that is communicatively coupled to the memory and that is configured to transmit to and receive from another communications device" [1C]
1	Gendel discloses "for a first period of time, the first set of two or more communications channels is used to transmit to and receive from the other communications device" [1Di] and "for a second period of time that is after the first period of time, the second set of two or more communications channels is used to transmit to and receive from the other communications device instead of the first set of two or more communications channels."



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