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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
95/000,657	01/19/2012	6366130	19968-0006RX1	8472
74642	7590	01/23/2013	EXAMINER	
CLIFFORD H. KRAFT 320 ROBIN HILL DR. NAPERVILLE, IL 60540			HUGHES, DEANDRA M	
			ART UNIT	PAPER NUMBER
			3992	
			MAIL DATE	DELIVERY MODE
			01/23/2013	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Transmittal of Communication to Third Party Requester <i>Inter Partes</i> Reexamination	Control No.	Patent Under Reexamination	
	Examiner	Art Unit	
	95/000,657	6366130	
	Deandra M. Hughes	3992	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address. --

____ (THIRD PARTY REQUESTER'S CORRESPONDENCE ADDRESS) ____

Fish & Richardson PC (DC)
P.O. Box 1022
Minneapolis, MN 55440-1022

Enclosed is a copy of the latest communication from the United States Patent and Trademark Office in the above-identified reexamination proceeding. 37 CFR 1.903.

Prior to the filing of a Notice of Appeal, each time the patent owner responds to this communication, the third party requester of the *inter partes* reexamination may once file written comments within a period of 30 days from the date of service of the patent owner's response. This 30-day time period is statutory (35 U.S.C. 314(b)(2)), and, as such, it cannot be extended. See also 37 CFR 1.947.

If an *ex parte* reexamination has been merged with the *inter partes* reexamination, no responsive submission by any *ex parte* third party requester is permitted.

All correspondence relating to this *inter partes* reexamination proceeding should be directed to the **Central Reexamination Unit** at the mail, FAX, or hand-carry addresses given at the end of the communication enclosed with this transmittal.

<i>ACTION CLOSING PROSECUTION (37 CFR 1.949)</i>	Control No.	Patent Under Reexamination
	95/000,657 Examiner	6366130 Art Unit
	Deandra M. Hughes	3992

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address. --

Responsive to the communication(s) filed by:

Patent Owner on 01 May, 2012
Third Party(ies) on 06 July, 2012

Patent owner may once file a submission under 37 CFR 1.951(a) within 1 month(s) from the mailing date of this Office action. Where a submission is filed, third party requester may file responsive comments under 37 CFR 1.951(b) within 30-days (not extendable- 35 U.S.C. § 314(b)(2)) from the date of service of the initial submission on the requester. **Appeal cannot be taken from this action.** Appeal can only be taken from a Right of Appeal Notice under 37 CFR 1.953.

All correspondence relating to this inter partes reexamination proceeding should be directed to the **Central Reexamination Unit** at the mail, FAX, or hand-carry addresses given at the end of this Office action.

PART I. THE FOLLOWING ATTACHMENT(S) ARE PART OF THIS ACTION:

1. Notice of References Cited by Examiner, PTO-892
2. Information Disclosure Citation, PTO/SB/08
3. _____

PART II. SUMMARY OF ACTION:

- 1a. Claims 1,2,5-7 and 9-12 are subject to reexamination.
- 1b. Claims 3,4 and 8 are not subject to reexamination.
2. Claims _____ have been canceled.
3. Claims 1,2 and 5-7 are confirmed. [Unamended patent claims]
4. Claims 12 are patentable. [Amended or new claims]
5. Claims 9-11 are rejected.
6. Claims _____ are objected to.
7. The drawings filed on _____ are acceptable are not acceptable.
8. The drawing correction request filed on _____ is: approved. disapproved.
9. Acknowledgment is made of the claim for priority under 35 U.S.C. 119 (a)-(d). The certified copy has:
 - been received. not been received. been filed in Application/Control No _____
10. Other _____

Art Unit: 3992

INTER PARTES REEXAMINATION ACTION CLOSING PROSECUTION

1. This is an action closing prosecution in the *inter partes* reexamination of USP 6,366,130. ("**130 patent**")

- **Claims 1-2 and 5-7** were ordered for reexamination.
- **Claims 3-4 and 8** were not ordered for reexamination.
- New **claims 9-12** of the claim amendment filed May 1, 2012 have been entered.
- Patent Owner's ("PO") remarks filed May 1, 2012 have been entered.
- Third Party Requester's ("3PR") comments filed July 6, 2012 have been entered.

2. Accordingly, **claims 1-2, 5-7, and 9-12** are under reexamination.

References

3. The following references are applied in this action:

- EP 0 597 231 to Hardee published May 18, 1994. ("**Hardee**")
- Declaration of David L. Taylor executed Jul. 6, 2012. ("**Taylor Declaration**")
- Declaration of Dr. Philip Koopman executed Apr. 29, 2012. ("**Koopman Declaration**")

Summary of this Action

4. The following is a summary of this action:

- **Claims 1-2 and 5-7** are confirmed as patentable.
- **Claim 12** is allowed as patentable.
- **Claims 9-11** are rejected under 35 USC 112-1st ¶.

Art Unit: 3992

Response to PO's Remarks and 3PR's Comments

5. PO's remarks, 3PR's comments, the **Taylor Declaration**, and the **Koopman Declaration** have been considered and weighed and 3PR's argument is found not persuasive for the reasons below. As such, the rejection of **claims 1-2 and 5-7** as being anticipated by **Hardee** is withdrawn.

6. **Claims 1-2, 5-7, and 9-12** require, *inter alia*, (1) a voltage precharge source, (2) differential bus, and (3) a differential data bus.

The Examiner's outline of **claim 1**, which is the only independent claim under reexamination, is reproduced below.

A data transfer arrangement comprising:

- two bus drivers;
- a voltage precharge source;
- a differential bus coupled to the bus drivers and to the voltage precharge source;
- a latching sense amplifier coupled to the differential bus;
- wherein the latching sense amplifier comprises:
 - a first stage including a cross-coupled latch coupled to a differential data bus; and
 - an output stage coupled to an output of said first stage;
 - wherein the output of the first stage is coupled to an input of the output stage;
 - wherein the differential bus and the differential data bus are precharge to a voltage V_{pr} between V_{dd} and ground, where $V_{pr}=K*V_{dd}$, and K is a precharging voltage factor.

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