Paper No. _____ Filed: July 25, 2016

UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE PATENT TRIAL AND APPEAL BOARD
SAMSUNG ELECTRONICS COMPANY, LTD. Petitioner
v.
ELBRUS INTERNATIONAL LIMITED. Patent Owner
Case IPR2015-01524
Patent No. 6,366,130

PETITIONER'S CURRENT LIST OF EXHIBITS



LIST OF EXHIBITS

Exhibit	Description	Previously
No.		Submitted
1001	U.S. Patent No. 6,366,130 ("the '130 Patent") to Podlesny	X
	et al.	
1002	Declaration of Dr. R. Jacob Baker	X
1003	File History of the '130 Patent	X
1004	Excerpts from File History of the Inter Partes	X
	Reexamination of the '130 Patent	
1005	U.S. Patent No. 5,828,241 to Sukegawa	X
1006	U.S. Patent No. 6,108,254 to Watanabe et al.	X
1007	U.S. Patent No. 6,249,469 to Hardee	X
1008	"Half-V00 Bit-Line Sensing Scheme in CMOS DRAM's,"	X
	IEEE Journal of Solid-State Circuits, Vol. SC-19, No. 4,	
	August 1984 by Lu et al.	
1009	Excerpts from the Modem Dictionary of Electronics (7th	X
	ed. 1999)	
1010	Excerpts from the IEEE Standard Glossary of Computer	
	Hardware Terminology, IEEE Standard 610.10-1994	
	(1995)	
1011	Deposition of William R. Huber, D.Sc.	
1012	Fairchild Semiconductor, Application Note AN-600	
	Understanding Latch-Up in Advanced CMOS Logic,	
	March 1998	
1013	Decembed	
	Reserved	
1014	Reserved	
1015	Reserved	
1016	Reserved	
1017	Reserved	
1018	Reserved	



Exhibit	Description	Previously
No.		Submitted
1019	Reserved	
1020	United States Patent 6,366,130, Samsung Exhibit 1001, with drawings by Dr. Huber	
1021	U.S. Patent 5,828,241, Samsung Exhibit 1005, with drawings by Dr. Huber	
1022	Declaration of William R. Huber, D.Sc., P.E. in Support of Patent Owner's Response to Petition	
1023	United States Patent 6,366,130, Samsung Exhibit 1001, with drawings by Dr. Huber	
1024	Handwritten drawing by Dr. Huber	

Dated: July 25, 2016 Respectfully submitted,

By: /Naveen Modi/

Naveen Modi (Reg. No. 46,224)

Counsel for Petitioner



CERTIFICATE OF SERVICE

I hereby certify that on this 25th day of July 2016, a copy of Petitioner's Current List of Exhibits was served by electronic means, as agreed by the parties, upon Counsel for Patent Owner at the following addresses of record:

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