

JEDEC STANDARD

Double Data Rate (DDR) SDRAM Specification

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JEDEC SOLID STATE TECHNOLOGY ASSOCIATION



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DOUBLE DATA RATE (DDR) SDRAM SPECIFICATION

(From JEDEC Board Ballot JCB-99-70, and modified by numerous other Board Ballots, formulated under the cognizance of Committee JC-42.3 on DRAM Parametrics.)

Standard No. 79 Revision Log.

Release 1, June 2000

Release 2, May 2002

Release C, March 2003

Release D, January 2004

Release E, May 2004

Scope

This comprehensive standard defines all required aspects of 64Mb through 1Gb DDR SDRAMs with X4/X8/X16 data interfaces, including features, functionality, ac and dc parametrics, packages and pin assignments. This scope will subsequently be expanded to formally apply to x32 devices, and higher density devices as well.

The purpose of this Standard is to define the minimum set of requirements for JEDEC compliant 64Mb through 1Gb, X4/X8/X16 DDR SDRAMs. System designs based on the required aspects of this specification will be supported by all DDR SDRAM vendors providing JEDEC compliant devices.

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