

Parthenon Unified Memory Architecture LLC v. HTC Corporation et al
Case No. 2:14-cv-00690-RSP

Claim Term	Preliminary Construction
"bus"	"a signal line or a set of associated signal lines to which a number of devices are coupled and over which information may be transferred"
"real time"	"fast enough to keep up with an input data stream"
"fast bus"	"bus with a bandwidth equal to or greater than the required bandwidth to operate in real time"
"coupled"	"direct or indirectly connected"
"coupleable"	"directly or indirectly connectable"
"coupling"	"directly or indirectly connecting"
"directly supplied"	"supplied without being stored in main memory for purposes of decoding subsequent images"
"directly supplies"	"supplies without being stored in main memory for purposes of decoding subsequent images"
"display device"	screen and its circuitry

“display adapter”	“an adapter that processes images for a display device”
“control circuit”	Plain and ordinary meaning.
“algorithmically translate the noncontiguous addresses to the contiguous addresses”	“convert the noncontiguous addresses to the contiguous addresses according to at least one mathematical operation”