

### US005797028A

# United States Patent [19]

## Gulick et al.

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5,797,028

Date of Patent: [45]

Aug. 18, 1998

IN	OMPUTER SYSTEM HAVING AN IPROVED DIGITAL AND ANALOG ONFIGURATION
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[73] Assignee: Advanced Micro Devices, Inc.,

Sunnyvale, Calif.

[21] Appl. No.: 526,488

Sep. 11, 1995 [22] Filed:

[51] Int. Cl.<sup>6</sup> ...... G06F 15/00 364/DIG. 1

Field of Search ...... 395/800, 800.32, 395/800.35, 800.01; 364/489, 228.6, DIG. 1

References Cited [56]

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### OTHER PUBLICATIONS

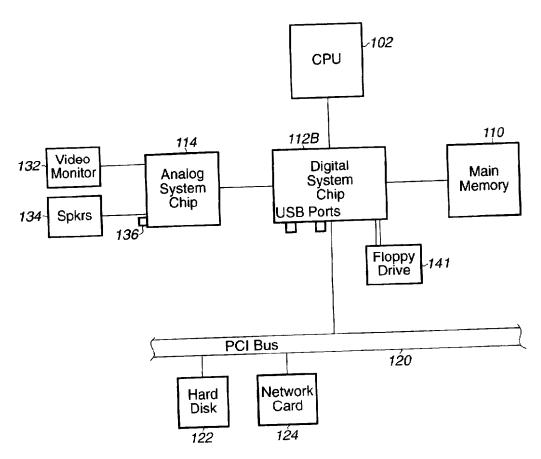
PCI Local Bus-PCI Multimedia Design Guide-Revision 1.0-Mar. 29, 1994, 43 pages.

Primary Examiner-John E. Harrity Attorney, Agent, or Firm-Conley, Rose & Tayon; Jeffrey C. Hood

#### ABSTRACT [57]

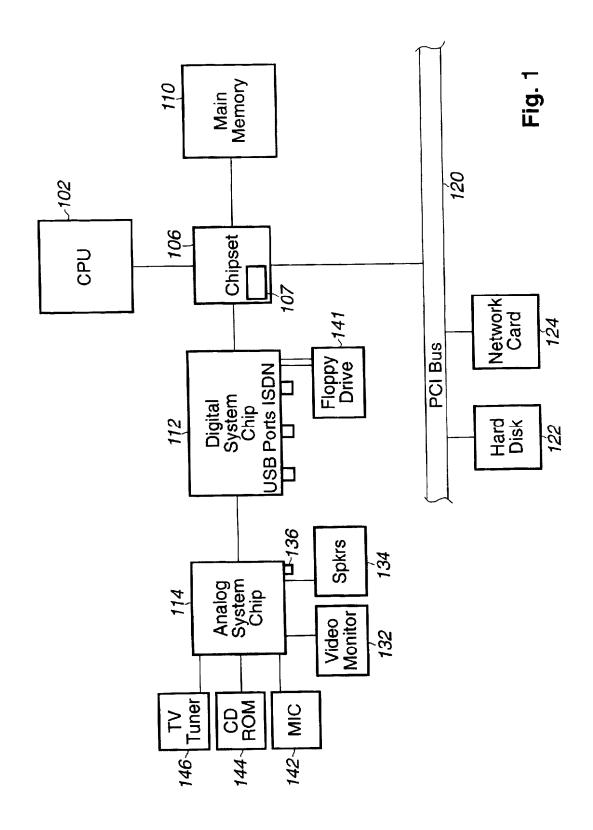
A computer system including separate digital and analog system chips which provides increased performance over current computer architectures. The computer system of the present invention includes a digital system chip which performs various digital functions, including multimedia functions and chipset functions, and a separate analog chip which performs analog functions, including digital to analog and analog to digital conversions. Thus the present invention optimizes silicon use and design by splitting up digital and analog functions on separate chips. The system of the present invention also separates digital noise from analog noise, allowing a higher degree of integration while increasing stability.

## 32 Claims, 11 Drawing Sheets



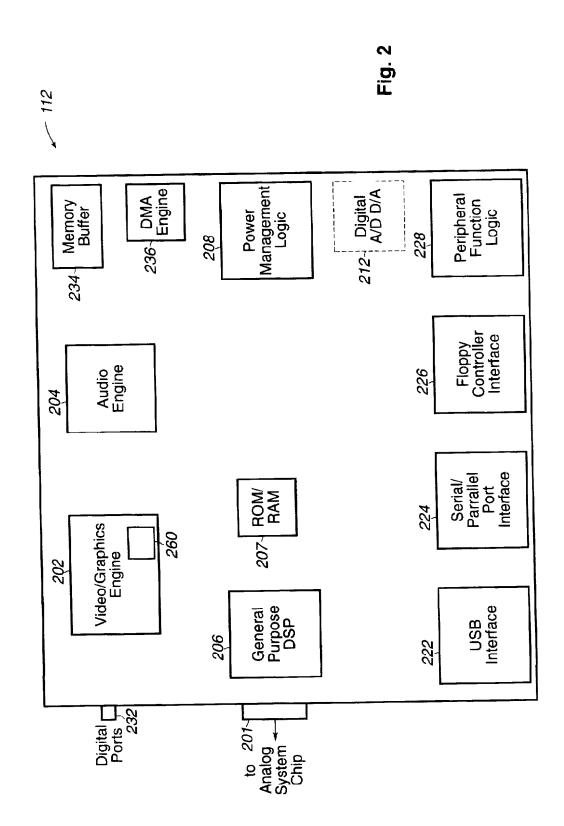


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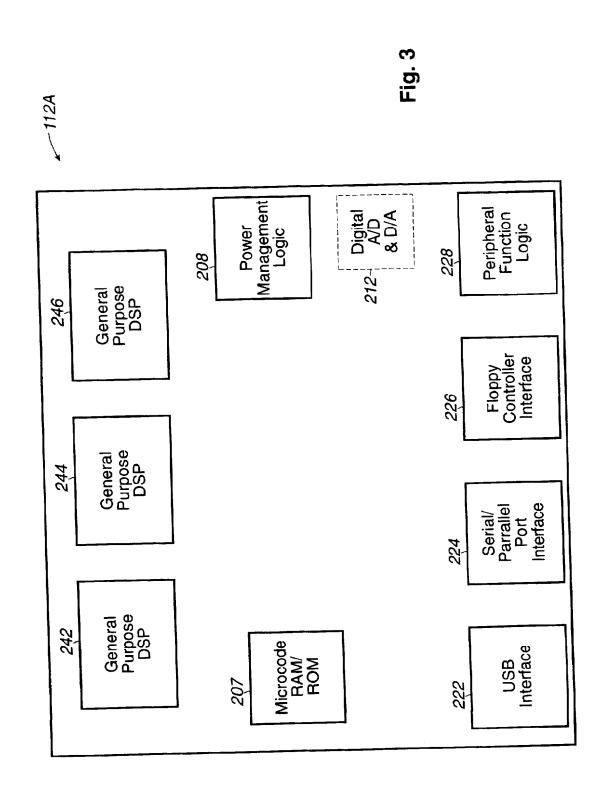


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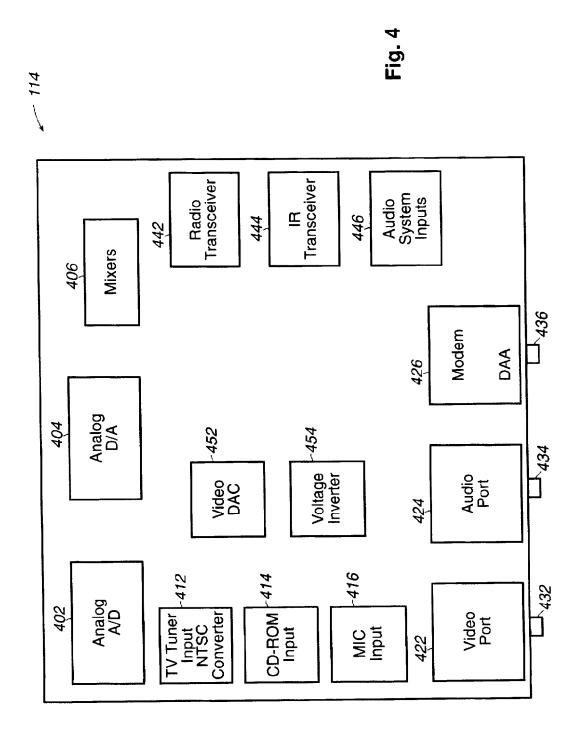




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