

Uesugi et al.

[54] APPARATUS FOR CALCULATING THE SQUARE ROOT OF THE SUM OF TWO SQUARES

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[56]

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Patent Number: 5,459,683

[45] **Date of Patent:** Oct. 17, 1995

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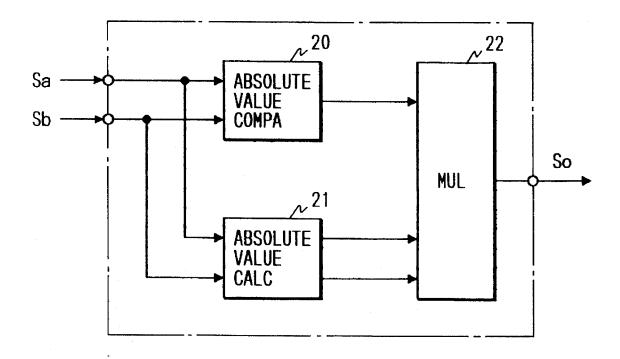
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Primary Examiner-Tan V. Mai Attorney, Agent, or Firm-Lowe, Price, LeBlanc & Becker

ABSTRACT

A first digital signal of a serial form is processed into a second digital signal of a serial form. The second digital signal represents an absolute value of a value represented by the first digital signal. A third digital signal of a serial form is processed into a fourth digital signal of a serial form. The fourth digital signal represents an absolute value of a value represented by the third digital signal. The values represented by the first and third digital signals are compared to generate a comparison-result digital signal representing a result of the comparison. A calculation-result digital signal of a serial form is generated in response to the second digital signal, the fourth digital signal, and the comparison-result digital signal. The calculation-result digital signal represents a value which is approximate to a square root of a sum of a square of the value represented by the first digital signal and a square of the value represented by the third digital signal.

4 Claims, 9 Drawing Sheets



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FIG. 1 PRIOR ART

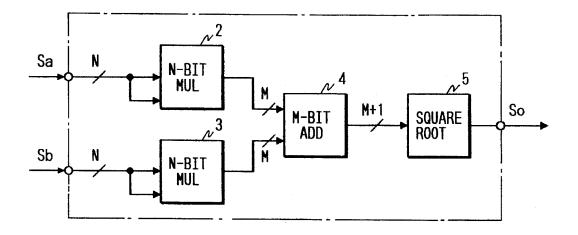
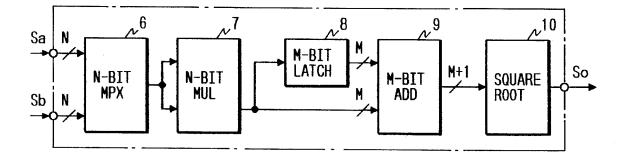


FIG. 2 PRIOR ART



Α

FIG. 3

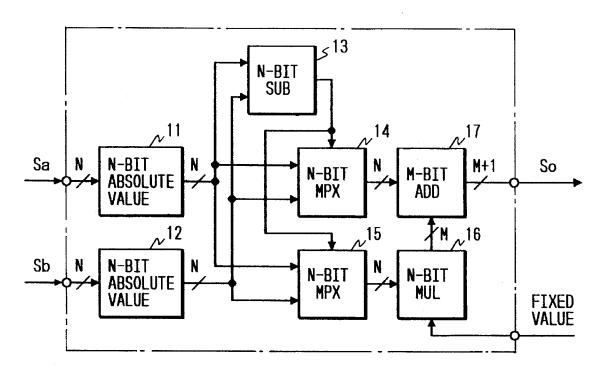
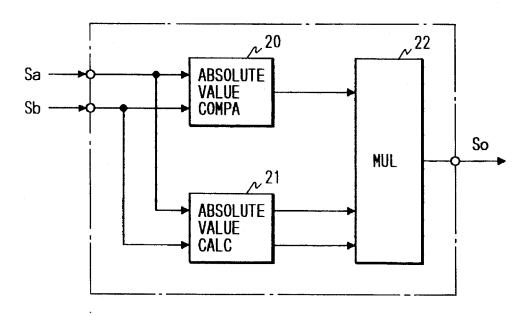
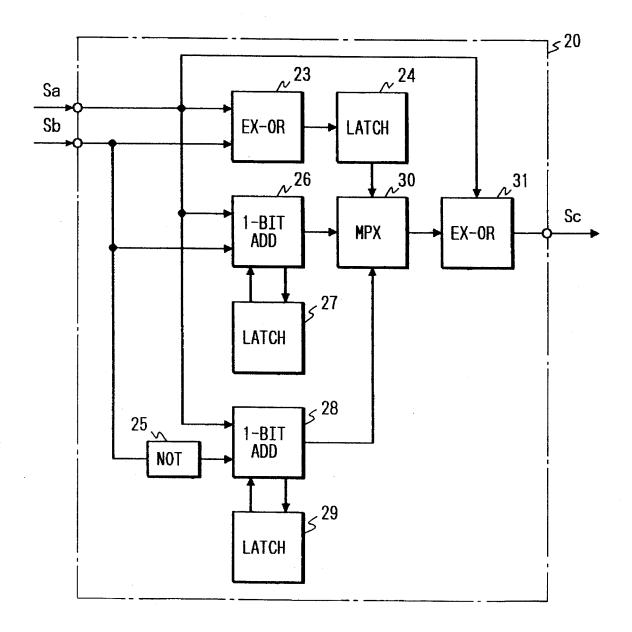


FIG. 4



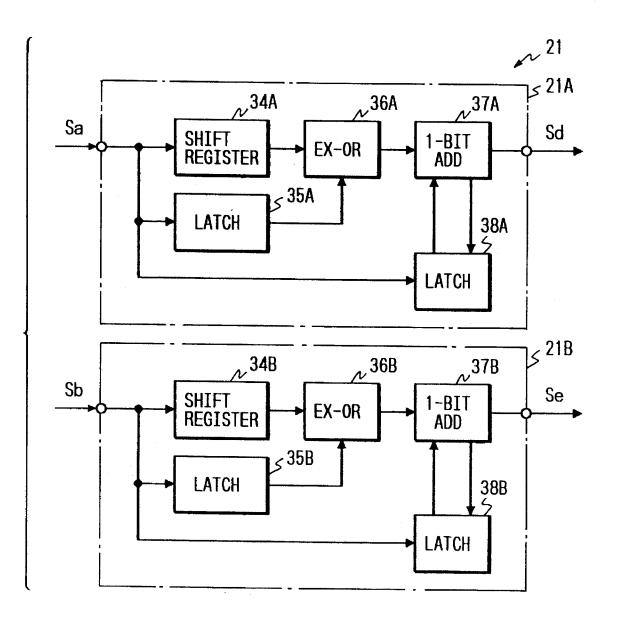
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Α

FIG. 6



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