UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

REALTEK SEMICONDUCTOR CORPORATION
Petitioner

v.

ANDREA ELECTRONICS CORPORATION

Patent Owner

Case: IPR2015-01393

Patent 6,049,607

DECLARATION OF DAVID V. ANDERSON

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I, David V. Anderson, hereby declare, affirm and state the following:

I. Introduction

1. The facts set forth below are known to me personally, and I have firsthand knowledge of them.

2. I make this Declaration in support of a Petition for *inter partes* review of U.S. Patent No. 6,049,607 ("the '607 Patent").

3. I have been retained by Steptoe & Johnson LLP on behalf of Realtek Semiconductor Corporation.

4. I have been asked to provide my technical review, analysis, insights, and opinions on the materials I have reviewed in this case related to the '607 Patent, including the references that form the basis for the grounds of rejection set forth in Petition No. IPR2015-01393 for Inter Partes Review of the '607 Patent ("Petition"), and the scientific and technical knowledge regarding the same subject matter at the time of the alleged inventions disclosed in the '607 Patent.

II. Qualifications and Compensation

5. I am over the age of eighteen and I am a citizen of the United States.

6. I have summarized in this section my educational background, career history, and other relevant qualifications. My curriculum vitae, including my qualifications, a list of the publications that I have authored during my technical

career, and a list of the cases in which, during the previous four years, I have testified as an expert at trial or by deposition, is attached to this declaration as Appendix 1.

7. I earned my Bachelor of Science degree in Electrical Engineering from Brigham Young University in 1993. In 1994 I earned my Master of Science degree in Electrical Engineering, also from Brigham Young University. I earned my Doctorate of Philosophy in Electrical and Computer Engineering from Georgia Institute of Technology in 1999, with my dissertation on "Audio Signal Enhancement Using Multi-resolution Sinusoidal Modeling."

8. After obtaining my Doctorate of Philosophy degree, I worked as an Education Specialist at Texas Instruments, Inc. from April 1999 through September of 1999. In this position, I developed a self-paced course on signal processing fundamentals and implementation for practicing engineers.

9. In September of 1999, I joined the faculty of Georgia Institute of Technology as an Assistant Professor in the School of Electrical and Computer Engineering. While on this faculty, I taught courses in signal processing and computer architecture and performed research in signal processing and low-power implementation of signal processing systems.

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