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[54] BATTERY PACK HAVING A PROCESSOR CONTROLLED BATTERY OPERATING **SYSTEM**

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[52]	U.S. Cl. 320/30; 320/48
[58]	Field of Search 320/5, 19, 20
	320/22, 30, 35, 48; 324/426; 361/96, 106
	364/483, 350, 550

References Cited [56]

U.S. PATENT DOCUMENTS

3,971,980 4,238,839 4,289,836 4,377,787	7/1976 12/1980 9/1981 3/1983	Jungfer et al Redfern et al
4,390,841 4,583,034	6/1983 4/1986	Martin et al Martin .
4,595,880 4,677,363	6/1986 6/1987	Patil . Kopmann .
4,709,202 4,716,354	11/1987 12/1987	Koenck et al 320/43 Hacker
4,724,528 4,725,784	2/1988 2/1988	Eaton . Peled et al
4,737,702 4,743,831	4/1988 5/1988	Koenck .
4,803,416	2/1989	
4,885,523 4,947,123	12/1989 8/1990	Koenck . Minezawa .

4,949,046 8/1990 Seyfang .

(List continued on next page.)

OTHER PUBLICATIONS

Markus Bullinger, "Quick Cahrging with Intelligence-An IC Controls NiCad and NiMH Battery Chargers," Electronik, 42, No. 6, Mar. 23, 1993, pp. 74-77.

Patrick Guelle, "Integrated Circuits for Rapid Chargers", Electronique Radio Plans, Feb. 1993, No. 543, pp. 57-64. Jacques Robert, et al., "A 16-bit Low-Voltage CMOS A/D Converter," IEEE Journal of Solid State Circuits, vol. SC-22, No. 2, Apr. 1987, pp. 157-159.

Primary Examiner-Peter S. Wong Assistant Examiner-Gregory J. Toatley, Jr. Attorney, Agent, or Firm-Scully, Scott, Murphy & Presser

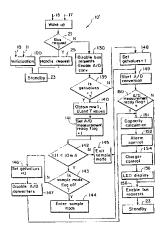
ABSTRACT [57]

A smart battery device which provides electrical power and which reports predefined battery parameters to an external device having a power management system, includes: at least one rechargeable cell connected to a pair of terminals to provide electrical power to an external device during a discharge mode and to receive electrical power during a charge mode, as provided or determined by the remote device; a data bus for reporting predefined battery identification and charge parameters to the external device; analog devices for generating analog signals representative of battery voltage and current at said terminals, and an analog signal representative of battery temperature at said cell; a hybrid integrated circuit (IC) having a microprocessor for receiving the analog signals and converting them to digital signals representative of battery voltage, current and temperature, and calculating actual charge parameters over time from the digital signals, the calculations including one calculation according to the following algorithm;

$$CAP_{rem} = CAP_{FC} - \Sigma I_c \Delta t_d - \Sigma I_s \Delta t + \Sigma \epsilon_c I_c \Delta t_c$$

wherein ϵ_c is a function of battery current and temperature; and I, is a function of battery temperature and CAPFC. Superimposed on this equation is reset logic, that self corrects the value of CAP_{FC} with a capacity calculation at each full charge (EOC) and each end of full discharge.

32 Claims, 31 Drawing Sheets

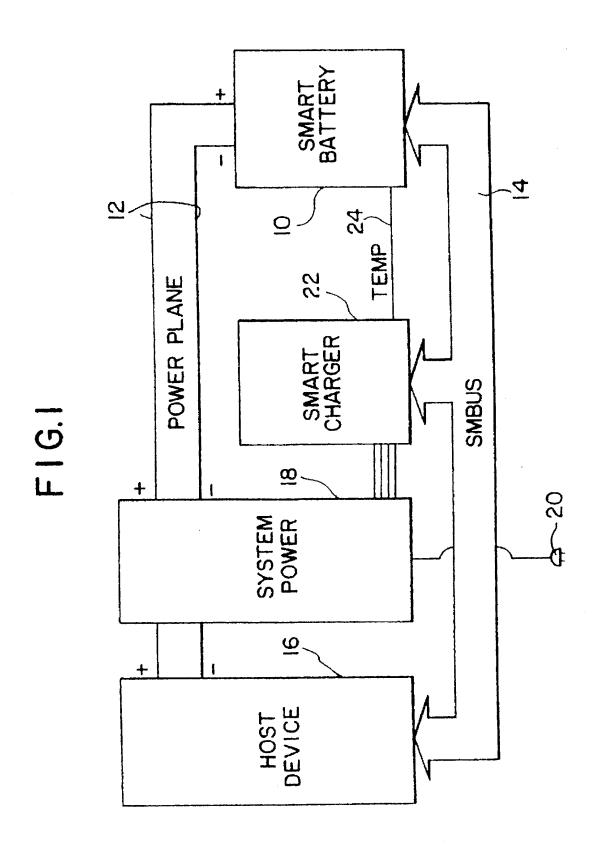




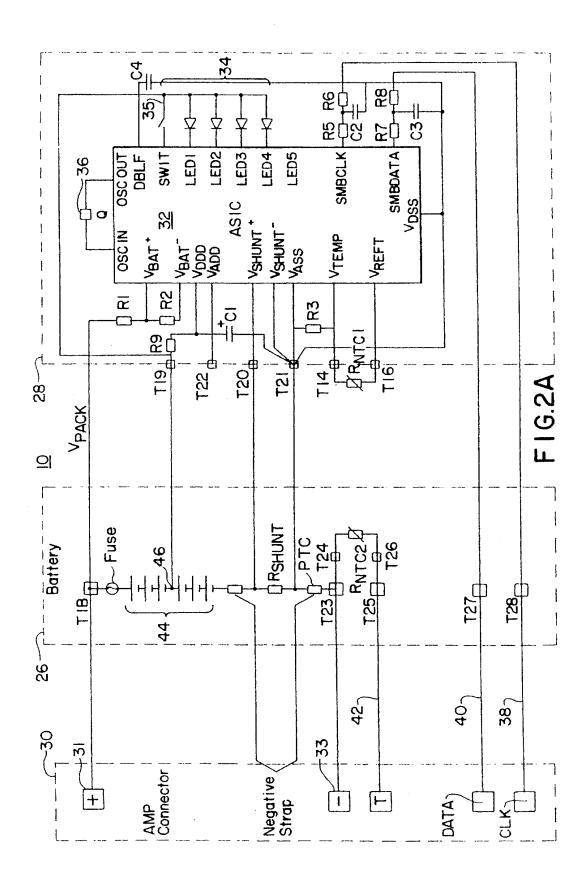
5,652,502 Page 2

U.S. 1	PATENT DOCUMENTS	5,254,92	8 10/1993	Young et al
4,961,043 10/19	V Koensk	5,278,48	7 1/1994	Koenck.
		5,284,71	9 2/1994	Landau et al
	90 Bauer et al	304/483 5 287 28	6 2/1994	Ninomiya .
5,027,294 6/19	91 Fakruddin et al	, ,		•
5,043,651 8/19	Ol Tamura .	5,315,22	8 5/1994	Hess et al
5,047,961 9/19		5,321,62	7 6/1994	Reher.
5,130,659 7/19		5,325,04	1 6/1994	Briggs .
5,196,779 3/19	3 Alexandres et al	5,341,08	4 8/1994	Gotoh et al
5,200,689 4/19	93 Interiano et al	5,349,53	5 9/1994	Gupta 364/483
5,216,371 6/19	93 Nagai.	5,455,49	9 10/1995	Uskali et al 320/43











SMBCLK 38 53 internal 8 bit bus program ROM 72 driver Interface ED. **SMBUS** 35 **20** 65 85 65a F16.2B POWER RESET 8 bit RAM 80 wake-up comparator circuit 67 on-chip 455kHz <u></u> register 55 **₩** extern 9 55



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