

TANDY®

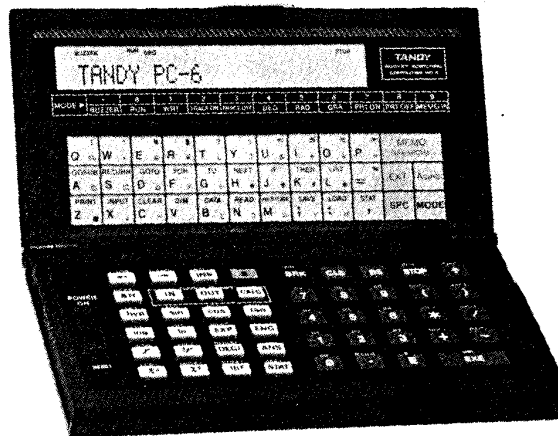
Service Manual

26-3672

POCKET SCIENTIFIC COMPUTER

PC-6

Catalog Number: 26-3672



CUSTOM MANUFACTURED FOR RADIO SHACK, A DIVISION OF TANDY CORPORATION

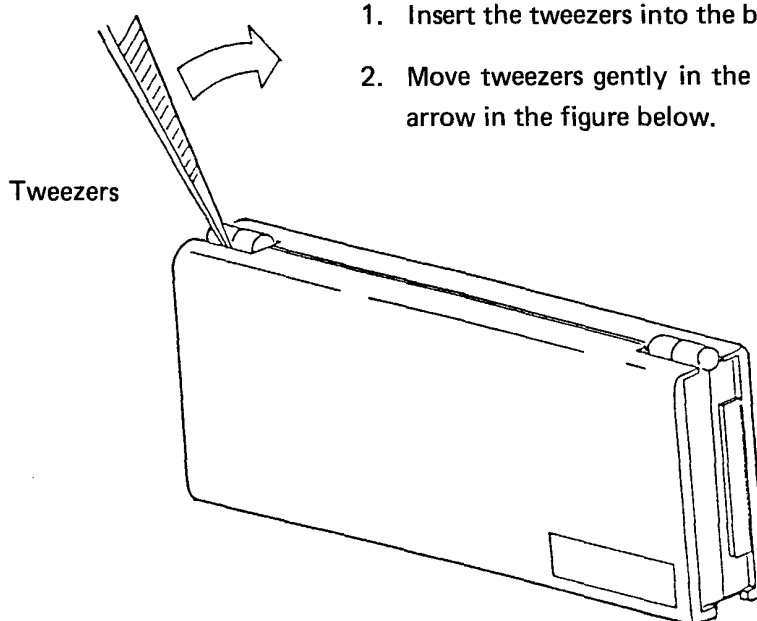
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SPECIFICATIONS

Display Method	5 x 7 dot-matrix crystal
Display	24-digit, dot-matrix liquid-crystal display
Calculation Range	$\pm 10^{-99} \sim \pm 9.999999999 \times 10^{99}$
Program System	Stored System
Program Language	BASIC
Number of Variables	26 fixed variables, exclusive character variable and array variables
RAM Capacity	7520 bytes (system area: 672 bytes)
Program Areas	Maximum of 10 (P0 through P9)
Number of Stacks	
Subroutine	8 levels
FOR ~ NEXT LOOP	4 levels
Operators	12 levels
Power Supply	2 lithium batteries (CR-2032) for main frame 1 lithium battery (CR-1220) for memory backup
Power Consumption	Maximum 0.07W
Battery Life	1) Continuous program execution: approximately 85 hours 2) Continuous display of 5555555555 at 68° F (20° C): approximately 200 hours or 5.5 months when unit is used 1 hour per day NOTE: 1 hour includes 10 minutes of condition 1) and 50 minutes of condition 2)
Memory protection battery	Approximately 2 years (with main batteries installed)
Auto Power-off	Approximately 6 minutes
Ambient Temperature	32° F to 104° F (0° C to 40° C)
Dimensions	3/4" H x 5-5/8" W x 2-3/4" D (18 mm x 142 mm x 71 mm)
Weight	5.8 oz (165 g), including batteries

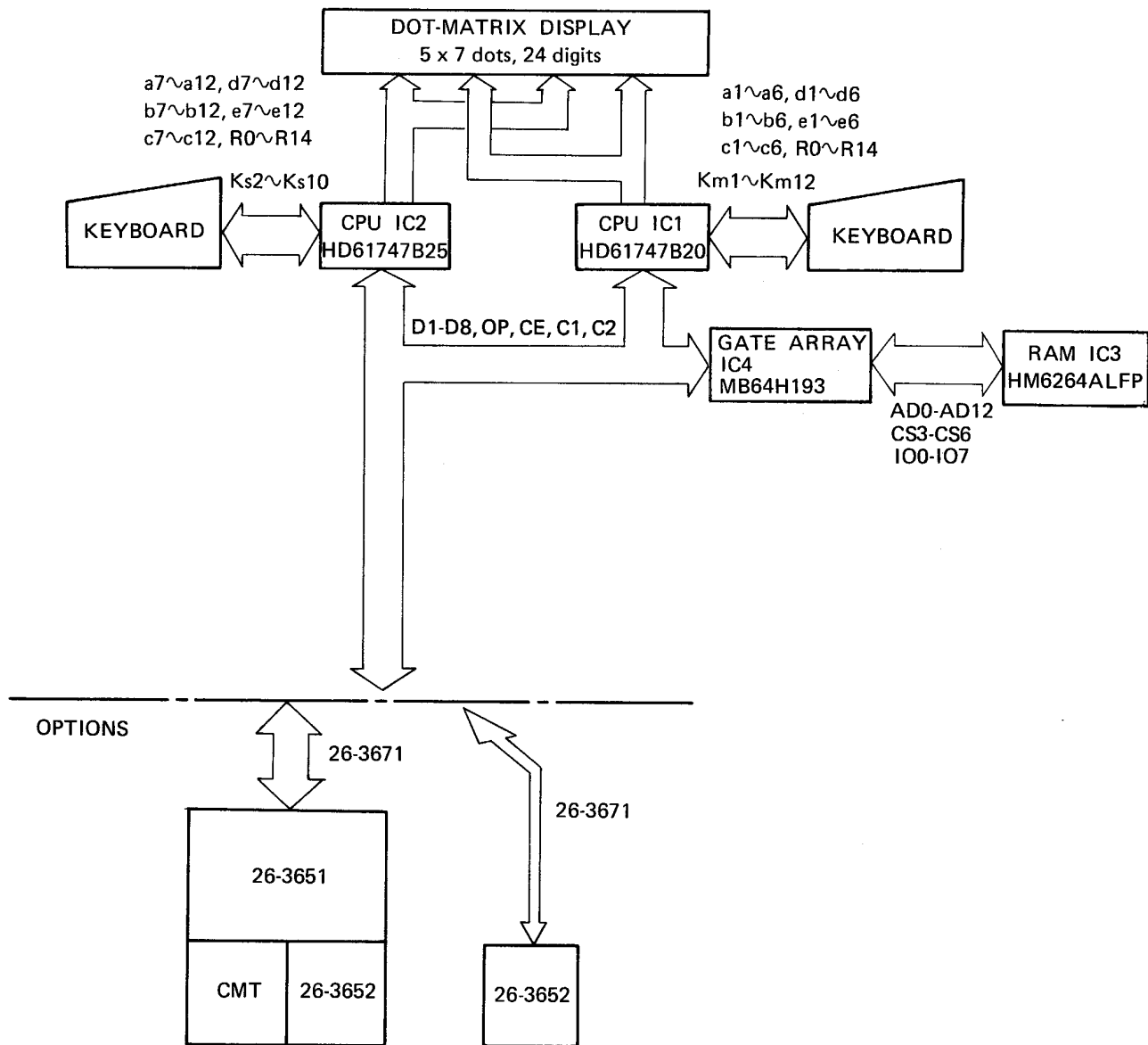
How To Open Upper Case



1. Insert the tweezers into the back of the upper case.
2. Move tweezers gently in the direction indicated by the arrow in the figure below.

THEORY OF OPERATION

Block Diagram



NOTE: Only one of 2 options can be connected at a time.

LSI

Main CPU

HD61747B20

BASIC control, Key signal input/output (lower keyboard), 1st to 6th digits and 13th to 18th digits display control.

Sub CPU

HD61747B25

Calculation, I/O control, key signal input/output (upper keyboard), 7th to 12th digits and 19th to 24th digits display control.

Gate Array

MB64H193

Interface between CPU and RAM, buzzer control.

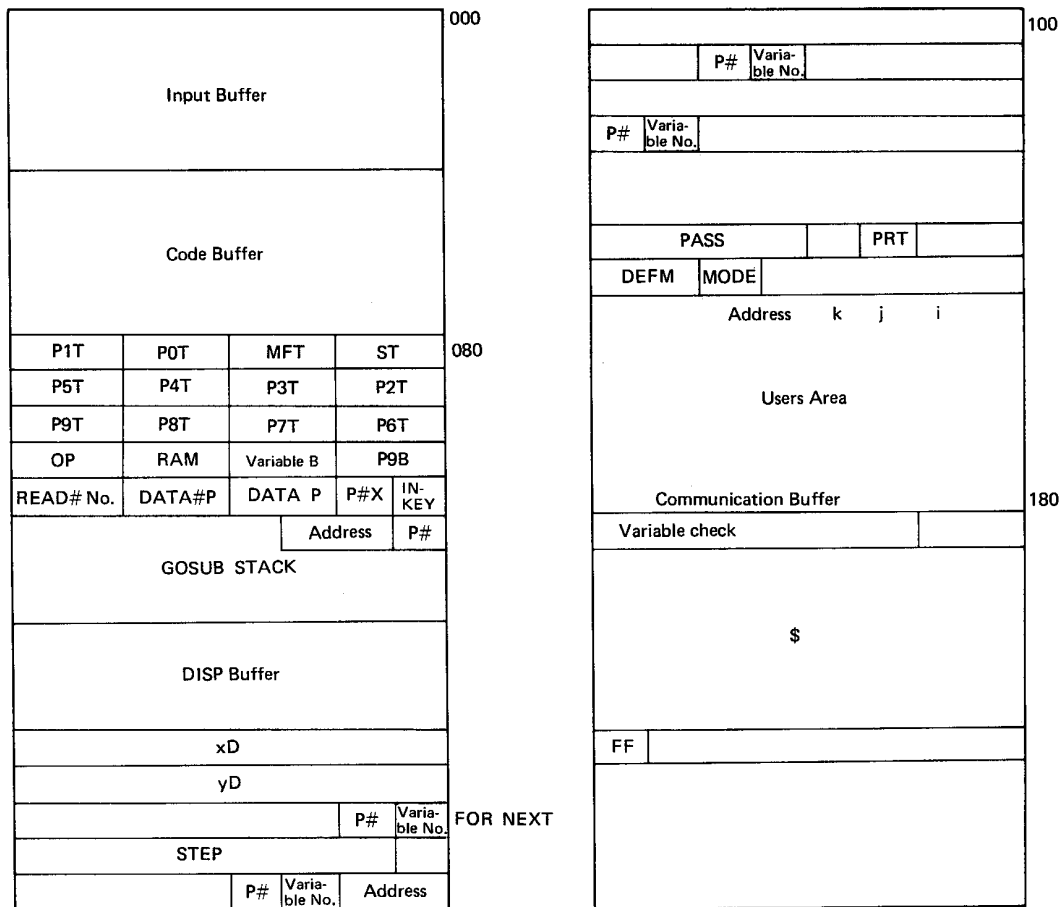
RAM

HM6264ALFP-50

Stores data and program. RAM has 8K bytes (64K bits) capacity and contains a self-control circuit.

LSI	ROM	RAM
CPU (Main) HD61747B20	113024 bits	1896 bits
CPU (Sub) HD61747B25	113024 bits	1896 bits
RAM HM6264ALFP-50		65536 bits
TOTAL	226056 bits	69328 bits

Ram Map



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