UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

MICRON TECHNOLOGY, INC., AND MICRON MEMORY JAPAN, INC.,
Petitioners

v.

MASSACHUSETTS INSTITUTE OF TECHNOLOGY
Patent Owner

Case: IPR2015-01087
U.S. Patent No. 6,057,221

MICRON TECHNOLOGY, INC.'S AND MICRON MEMORY JAPAN, INC.'S MOTION FOR *PRO HAC VICE* ADMISSION OF THOMAS R. MAKIN UNDER 37 C.F.R. § 42.10(c)

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TABLE OF EXHIBITS

Exhibit #	Exhibit Description (Citation is to page, column, or paragraph in original, except for Exhibits 1009, for which citation is to inserted page number)
1001	Declaration of Dr. Michael Thomas
1002	Curriculum Vitae of Dr. Michael Thomas
1003	U.S. Patent No. 6,057,221
1004	File History for U.S. Patent No. 6,057,221
1005	*The New IEEE Standard Dictionary of Electrical and Electronic Terms, Fifth Ed.*, Institute of Electrical and Electronics Engineers, Inc., New York (1993)
1006	Japan Pat. Appl. Publ. No. 8-213465 to Koyou (including English translation and supporting declaration)
1007	Japan Pat. Appl. Publ. No. 6-244285 to Wada, et al. (including English translation and supporting declaration)
1008	U.S. Patent No. 5,729,042 to Lou et al.
1009	U.S. Patent Application No. 514,800 filed August 14, 1995 (to which U.S. Pat. No. 5,729,042 claims priority)
1010	U.S. Patent No. 5,025,300 to Billig et al.
1011	*Ex Parte* Reexamination Application No. 90/011,607, Request for *Ex Parte* Reexamination filed March 30, 2011
1012	*Ex Parte* Reexamination Application No. 90/011,607, Corrected Pre-amendment under 35 C.F.R. 1.530 filed April 14, 2011
1013	*Ex Parte* Reexamination Application No. 90/011,607, Order Granting Request for *Ex Parte* Reexamination filed June 23, 2011
1014	*Ex Parte* Reexamination Application No. 90/011,607, Non-Final Office Action of January 26, 2012

1015	*Ex Parte* Reexamination Application No. 90/011,607, Request for Reconsideration filed March 26, 2012
1016	*Ex Parte* Reexamination Application No. 90/011,607, Declaration of Dr. Bernstein filed March 26, 2012 (including exhibits)
1017	*Ex Parte* Reexamination Application No. 90/011,607, Notice of Intent to Issue Ex Parte Reexamination Certificate of July 11, 2012
1018	"Thermal Conductivity of Metals," The Engineering ToolBox, http://www.engineeringtoolbox.com/thermal-conductivity-metals-d_858.html (last visited April 1, 2015)
1019	Pierson, *Handbook of Refractory Carbides and Nitrides: Properties, Characteristics, Processing, and Applications*, Noyes Publications (1996)
1020	U.S. Patent No. 5,872,389 to Nishimura et al.
1021	U.S. Patent No. 5,675,174 to Nakajima
1022	U.S. Patent No. 5,538,924 to Chen
1023	U.S. Patent No. 5,300,461 to Ting
1024	U.S. Patent No. 5,729,041 to Yoo
1025	U.S. Patent No. 5,747,869 to Prall
1026	Wilson et al., *Handbook of Multilevel Metallization For Integrated Circuits: Materials, Technology, and Applications*, Noyes Publications (1993)
1027	Wolf, *Silicon Processing for the VLSI ERA Volume 2: Process Integration*, Lattice Press, Sunset CA (1990)
1028	Construction Analyses of the Samsung KM44C4000J-7 16 Megabit DRAM, published by Integrated Circuit Engineering, Scottsdale AZ, Report No. SCA 9311-3001 (available at http://smithsonianchips.si.edu/ice/cd/9311_300.pdf)

1029	Construction Analyses of the Lattice ispLSI2032-180L CPLD, published by Integrated Circuit Engineering, Scottsdale AZ, Report No. SCA 9712-573 (available at http://smithsonianchips.si.edu/ice/cd/9712_573.pdf)
1030	Construction Analysis of the Intel Pentium Processor w/MMX, published by Integrated Circuit Engineering, Scottsdale AZ, Report No. SCA 9706-540 (available at http://smithsonianchips.si.edu/ice/cd/9706_540.pdf)
1031	"Intel Introduces The Pentium® Processor With MMX™ Technology," http://www.intel.com/pressroom/archive/releases/1997/dp010897.htm (last visited April 14, 2015)
1032	"Intel Microprocessor Quick Reference Guide," http://www.intel.com/pressroom/kits/quickreffam.htm#pentium (last visited April 26, 2015)
1033	Construction Analyses of the Motorola PC603R Microprocessor, published by Integrated Circuit Engineering, Scottsdale AZ, Report No. SCA 9709-551 (available at http://smithsonianchips.si.edu/ice/cd/9709_551.pdf)
1034	Construction Analyses of the Toshiba TC5165165AFT-50 64 Mbit DRAM, published by Integrated Circuit Engineering, Scottsdale AZ, Report No. SCA 9702-524 (available at http://smithsonianchips.si.edu/ice/cd/9702_524.pdf)
1035	"Material: Stainless steel, bulk," https://www.memsnet.org/material/stainlesssteelbulk/ (last visited April 14, 2015)
1036	"Material: Silicon Dioxide (SiO2), bulk," https://www.memsnet.org/material/silicondioxidesio2bulk/ (last visited April 14, 2015)
1037	Osaka, et al. "Development of new electrolytic and electroless gold plating processes for electronics applications," Science and Technology of Advanced Materials, vol. 7 (2006), pp. 425-437.

1038	Uttecht et al., "A four-level-metal fully planarized interconnect technology for dense high performance logic and SRAM applications," VLSI Multilevel Interconnection Conference, 1991, Proceedings, Eighth International IEEE, June 11-12, 1991, pp. 20-26
1039	*Ex Parte* Reexamination Application No. 90/011,607, Patent Owner Statement filed August 12, 2011
1040	Seshan ed., *Handbook of Thin-Film Deposition Processes and Techniques: Principles, Methods, Equipment and Applications*, Second Ed., Noyes Publications, New York (2002)
1041	Vlassak, et al., "A new bulge test technique for the determination of Young's modulus and Poisson's ratio of thin films", J. Mater. Res., Vol. 7, No. 12, Dec 1992
1042	*Ineos USA LLC v. Berry Plastics Corp.*, No 2014-1540, 2015 WL 1727013, (Fed. Cir. Apr. 16, 2015) (precedential)
1043	Affidavit of Thomas R. Makin in support of Motion for *Pro Hac Vice* Admission

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