Atty. Docket No. MIT-001-RX1

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE REEXAMINATION APPL'N OF:	:
Joseph BERNSTEIN et al.	: GROUP ART UNIT:
APPLICATION NO: New Reexam. Request	:
FILED: March 30, 2011	: EXAMINER:

FOR: LASER-INDUCED CUTTING OF METAL INTERCONNECT

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By: /Jennie Heaton/ Jennie Heaton

CORRECTED PRE-AMENDMENT UNDER 37 C.F.R. 1.530

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Prior to the re-examination of the above-identified application, Patentees submit herewith the following amendments to the claims. This corrected Pre-Amendment replaces the Amendment previously filed on March 30, 2011.

Amendments to the Claims

- 3. (Amended) [The] <u>A</u> method <u>for cutting a link between interconnected circuits[of claim 1], comprising the following steps:
 </u>
 - directing a laser upon an electrically-conductive cutlink pad conductively bonded between a first electrically-conductive line and a second electrically-conductive line on a substrate, the cut-link pad having substantially less thermal resistance per unit length than each of the first and second lines, wherein the width of the cut-link pad is at least ten percent greater than the width of each of the first and second electrically-conductive lines; and
 - maintaining the laser upon the cut-link pad until the laser infuses sufficient energy into the cut-link pad to break the conductive link across the cut-link pad between the pair of electrically-conductive lines, wherein the electrically-conductive cutlink pad has an inner surface facing the substrate and an opposing outer surface facing away from the substrate, the first and second electrically-conductive lines extending from the inner surface into the substrate.
- 4. (Original) The method of claim 3, wherein the laser beam extends across the entirety of the cut-link pad when the laser is directed upon the cut-link pad.
- 6. (Amended) The method of claim [1]3, wherein the width of the cut-link pad is at least twenty-five percent greater than the width of each of the first and second electrically-conductive lines.
- 7. (Amended) The method of claim [1]<u>3</u>, wherein the width of the cut-link pad is at least fifty percent greater than the width of each of the first and second electrically-conductive lines.

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- 8. (Amended) The method of claim 7, wherein the cut-link pad [is comprised of] <u>comprises</u> a composition substantially identical to the composition of the first and second electrically-conductive lines.
- 11. (Original) A method for cutting a link between interconnected circuits comprising the following steps:
 - directing a laser upon an electrically-conductive cut-link pad conductively bonded between a first electrically-conductive line and a second electrically-conductive line on a substrate, the cut-link pad having substantially less thermal resistance per unit length than each of the first and second lines, wherein the cut-link pad is formed of a material that has greater thermal conductivity than the material that forms each of the first and second electrically-conductive lines; and
 - maintaining the laser upon the cut-link pad until the laser infuses sufficient energy into the cut-link pad to break the conductive link across the cut-link pad between the pair of electrically-conductive lines.
- 13. (Amended) The method of claim [1]3, wherein a passivative layer covers the cut-link pad.
- 14. (Original) A method for cutting a link between interconnected circuits comprising the following steps:

directing a laser upon an electrically-conductive cut-link pad conductively bonded between a first electrically-conductive line and a second electrically-conductive line on a substrate, the cut-link pad having substantially less thermal resistance per unit length than each of the first and second lines, wherein the cut-link pad is covered with a passivative layer that is harder than the substrate; and maintaining the laser upon the cut-link pad until the laser infuses sufficient energy into the cut-link pad to break the conductive link across the cut-link pad between the pair of electrically-conductive lines.

- 15. (Amended) The method of claim 14, wherein the passivation layer [is] comprises [of] silicon nitride.
- 17. (Amended) [The] <u>A</u> method <u>for cutting a link between interconnected circuits[of claim 1], comprising the following steps:
 </u>
 - directing a laser upon an electrically-conductive cutlink pad conductively bonded between a first electrically-conductive line and a second electrically-conductive line on a substrate, the cut-link pad having substantially less thermal resistance per unit length than each of the first and second lines, wherein the width of the cut-link pad is at least ten percent greater than the width of each of the first and second electrically-conductive lines; and
 - maintaining the laser upon the cut-link pad until the laser infuses sufficient energy into the cut-link pad to break the conductive link across the cut-link pad between the pair of electrically-conductive lines, wherein a passivative layer covers the cut-link pad, and the electrically-conductive cut-link pad has an inner surface facing the substrate and an opposing outer surface facing away from the substrate, the first and second electrically-conductive lines extending from the inner surface into the substrate.
- (Original) The method of claim 17, wherein the laser beam extends across the entirety of the cut-link pad when the laser is directed upon the cut-link pad.

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- 21. (Amended) The method of claim 13, wherein the cut-link pad [is comprised of] <u>comprises a material with greater thermal conductivity than the material comprising each of the first and second electrically-conductive lines.</u>
- 22. (New) The method of claim 21, wherein the cut-link pad comprises aluminum.
- 23. (New) The method of claim 3, wherein the cut-link pad has a greater cross-sectional area than the first and second electrically-conductive lines, wherein the cross-sectional area of the cut-link pad is the product of a width and a height of the cut-link pad, the first and second electrically-conductive lines comprise vias, and the cross-sectional area of each of the first and second electrically-conductive lines is defined by a width of the corresponding via.
- 24. (New) The method of claim 13, wherein the passivative layer comprises silicon nitride.
- 25. (New) The method of claim 4, wherein the cut-link pad has a length of 2-3 microns, and the first and second electrically-conductive lines each have a diameter of about 0.5 microns.
- 26. (New) The method of claim 11, wherein the electrically-conductive cut-link pad has an inner surface facing the substrate and an opposing outer surface facing away from the substrate, the first and second electrically-conductive lines extending from the inner surface into the substrate.
- 27. (New) The method of claim 26, further comprising a passivative silicon nitride layer that covers the cut-link pad.
- 28. (New) The method of claim 11, wherein the cut-link pad comprises aluminum.

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