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Foreign priority claimed 35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	AS FILED →	STATE OR COUNTRY TW	SHEETS DRWGS. 2	TOTAL CLAIMS 15	INDEP. CLAIMS 2	FILING FEE RECEIVED \$730.00	ATTORNEY'S DOCKET NO. V880666
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ADDRESS
POLYTECHNIC COLLEGE
38 WINTERS DRIVE
MILLERSVILLE, NY 12543

TITLE
METHOD AND STRUCTURE FOR LASER REPAIR

PARTS OF APPLICATION FILED SEPARATELY		Applications Examiner		
NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED		
Assistant Examiner		Total Claims	Print Claim	
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.	Print Fig.
Label Area		Primary Examiner		ISSUE BATCH NUMBER
		PREPARED FOR ISSUE		
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FEE RECORD SHEET

250 28 09/08/95 08514800
1 101 730.00 CK VIS 84008

PTO-1556
(5/87)

An assignment of the invention to VANGUARD INTERNATIONAL SEMICONDUCTOR CORPORATION

A certified copy of a _____ application.

An associate power of attorney.

The filing fee has been calculated as shown below:

	(Col. 1)	(Col. 2)	OTHER THAN A SMALL ENTITY	
FOR:	NO. FILED	NO. EXTRA	RATE	FEE
BASIC FEE				\$730.
TOTAL CLAIMS	15 - 20 =	x	x 22 =	\$
INDEP CLAIMS	2 - 3 =	x	x 76 =	\$
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIM PRESENTED				
			SUB TOTAL	\$730.
			ASSIGNMENT (40.00)	\$40.
			TOTAL	\$770.

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Respectfully submitted,

George O. Saile
By _____
GEORGE O. SAILE

Registration No. 19572

(1) FIELD OF THE INVENTION

The invention relates to the general field of semiconductor integrated circuits, more particularly to circuits that may be personalized, repaired or modified by means of fusible links.

(2) DESCRIPTION OF THE PRIOR ART

Semiconductor integrated circuits (ICs) that have optimum density and/or performance cannot, in general, be repaired or modified. There exists, however, a large class of ICs that are intended to be repairable and/or modifiable. In certain cases, no real circuit exists until the IC has been personalized by breaking certain connections, thereby determining how the components are to be connected to one another.

One method for realizing circuits of this type is to arrange

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