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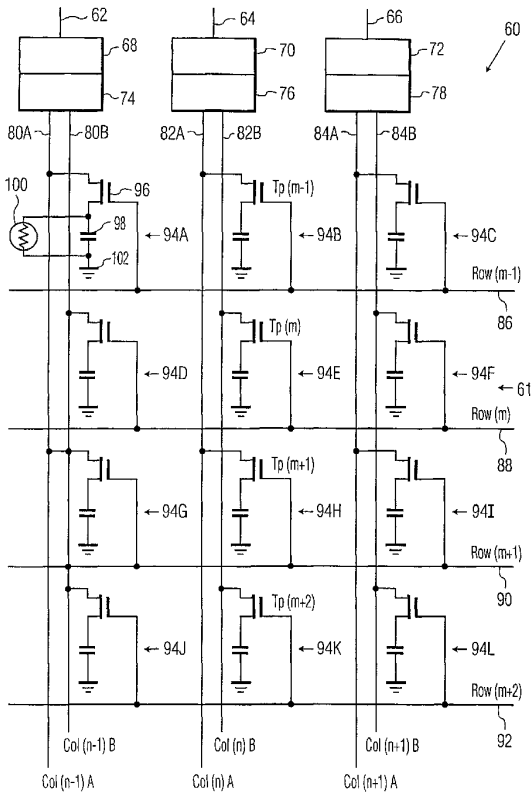
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(54) Title: COLUMN DRIVING CIRCUIT AND METHOD FOR DRIVING PIXELS IN A COLUMN ROW MATRIX



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(57) Abstract: A column driving circuit and method for driving pixels in a column row matrix. Specifically, the present invention provides a circuit and method that generally includes an input for receiving a signal, a multiplexing circuit for receiving the signal from the input, and a first and a second column line, wherein each column line alternates in receiving the signal from the multiplexing circuit. By splitting the signal between two column lines, overall line capacitance is reduced, as are problems associated with delays in ramp retrace.

A. CLASSIFICATION OF SUBJECT MATTER IPC 7 G09G3/20 G09G3/36		
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B. FIELDS SEARCHED		
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Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal, PAJ, WPI Data		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
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Y	the whole document ---	2,9,10
X	EP 0 273 995 A (HOSIDEN ELECTRONICS CO) 13 July 1988 (1988-07-13)	1,3-8
Y	the whole document ---	2,9-11
X	US 5 485 293 A (ROBINDER RONALD C) 16 January 1996 (1996-01-16)	1,3-8
Y	the whole document ---	2,9-11
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<input checked="" type="checkbox"/> Further documents are listed in the continuation of box C. <input checked="" type="checkbox"/> Patent family members are listed in annex.		
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Name and mailing address of the ISA European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl, Fax: (+31-70) 340-3016		Authorized officer Harke, M

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

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