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Janssen et al.

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(54) **COLUMN DRIVING CIRCUIT AND METHOD FOR DRIVING PIXELS IN A COLUMN ROW MATRIX**

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(51) **Int. Cl.**⁷ **G09G 3/36**

(52) **U.S. Cl.** **345/100**

(58) **Field of Search** 345/87, 92, 100, 345/103, 95, 88, 89, 93-96, 98; 349/33, 41, 42

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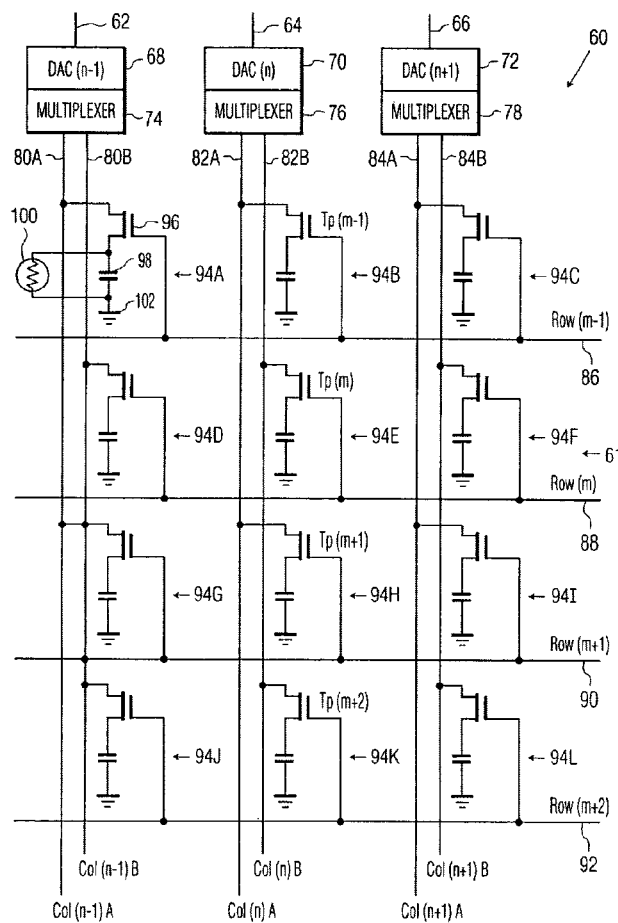
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(57) **ABSTRACT**

A column driving circuit and method for driving pixels in a column row matrix. Specifically, the present invention provides a circuit and method that generally includes an input for receiving a signal, a multiplexing circuit for receiving the signal from the input, and a first and a second column line, wherein each column line alternates in receiving the signal from the multiplexing circuit. By splitting the signal between two column lines, overall line capacitance is reduced, as are problems associated with delays in ramp retrace.

15 Claims, 6 Drawing Sheets



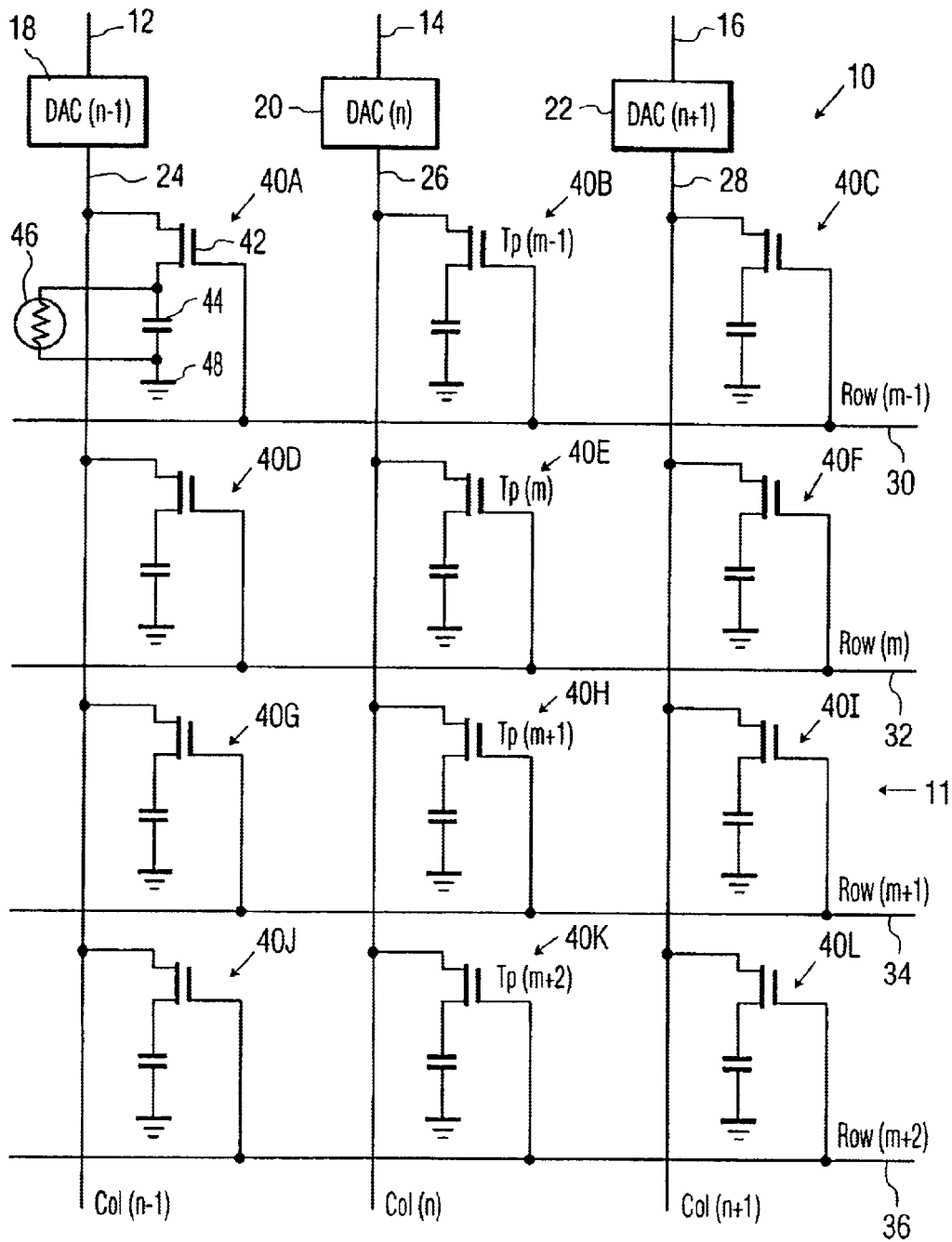


FIG. 1

PRIOR ART

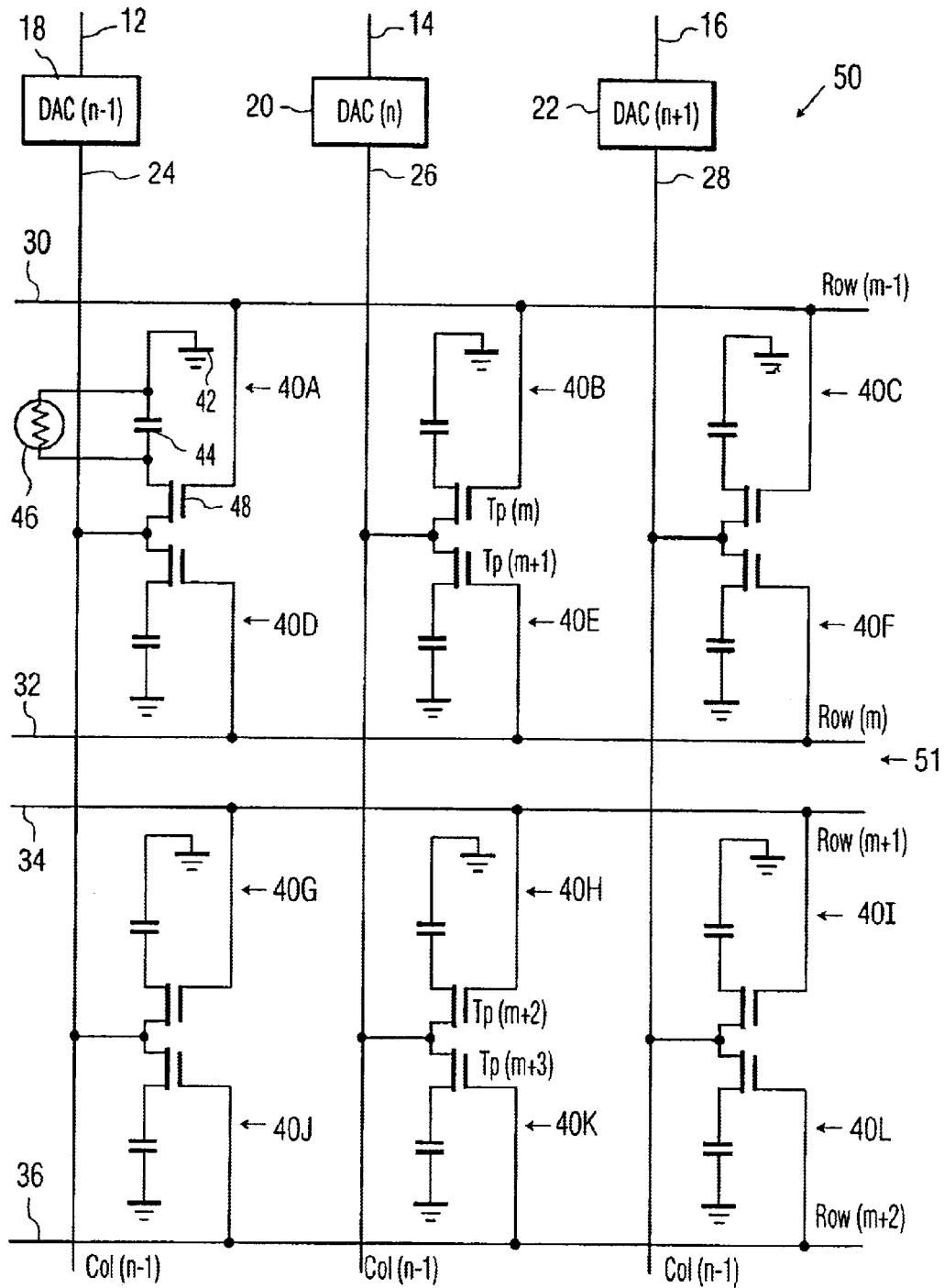


FIG. 2

PRIOR ART

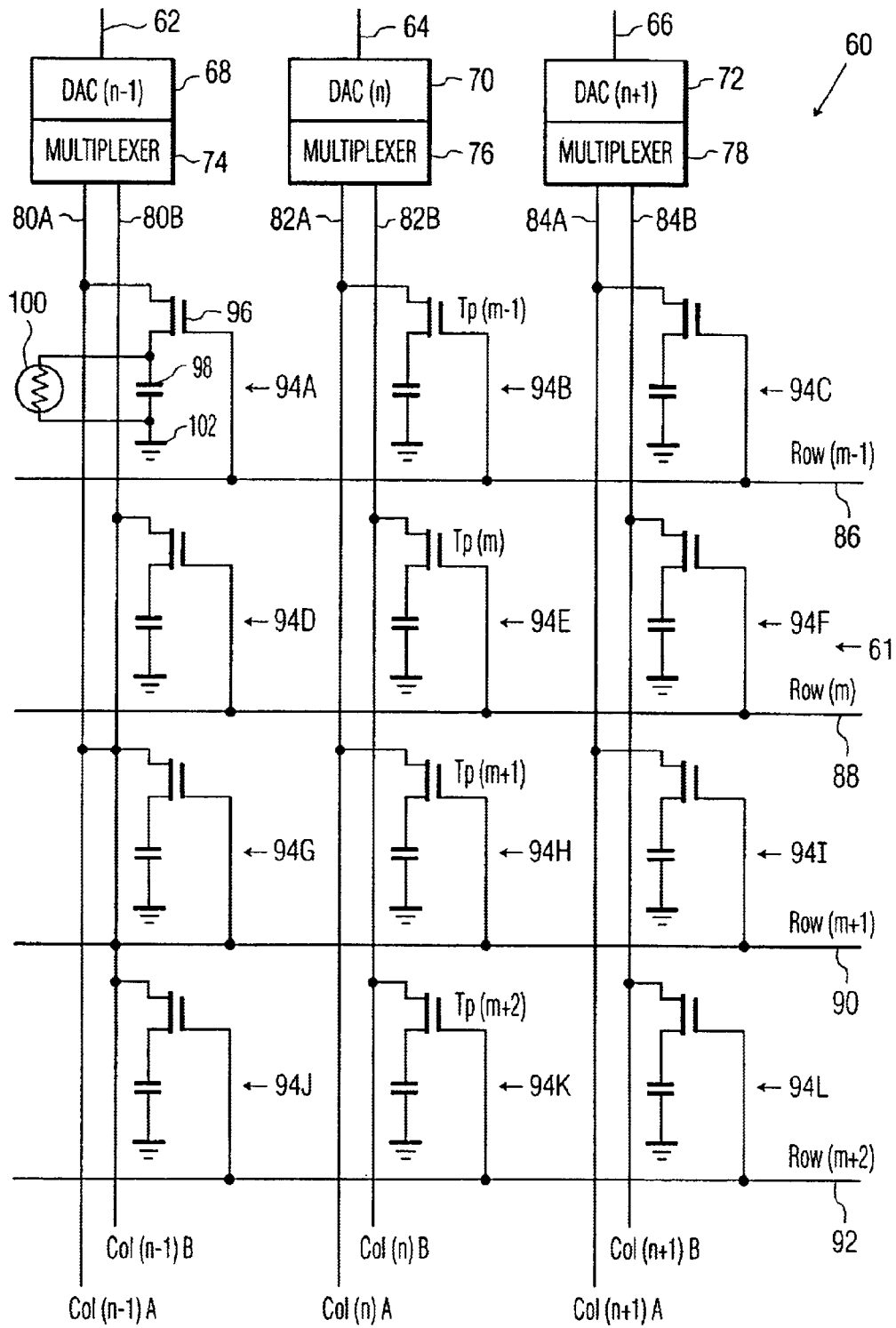


FIG. 3

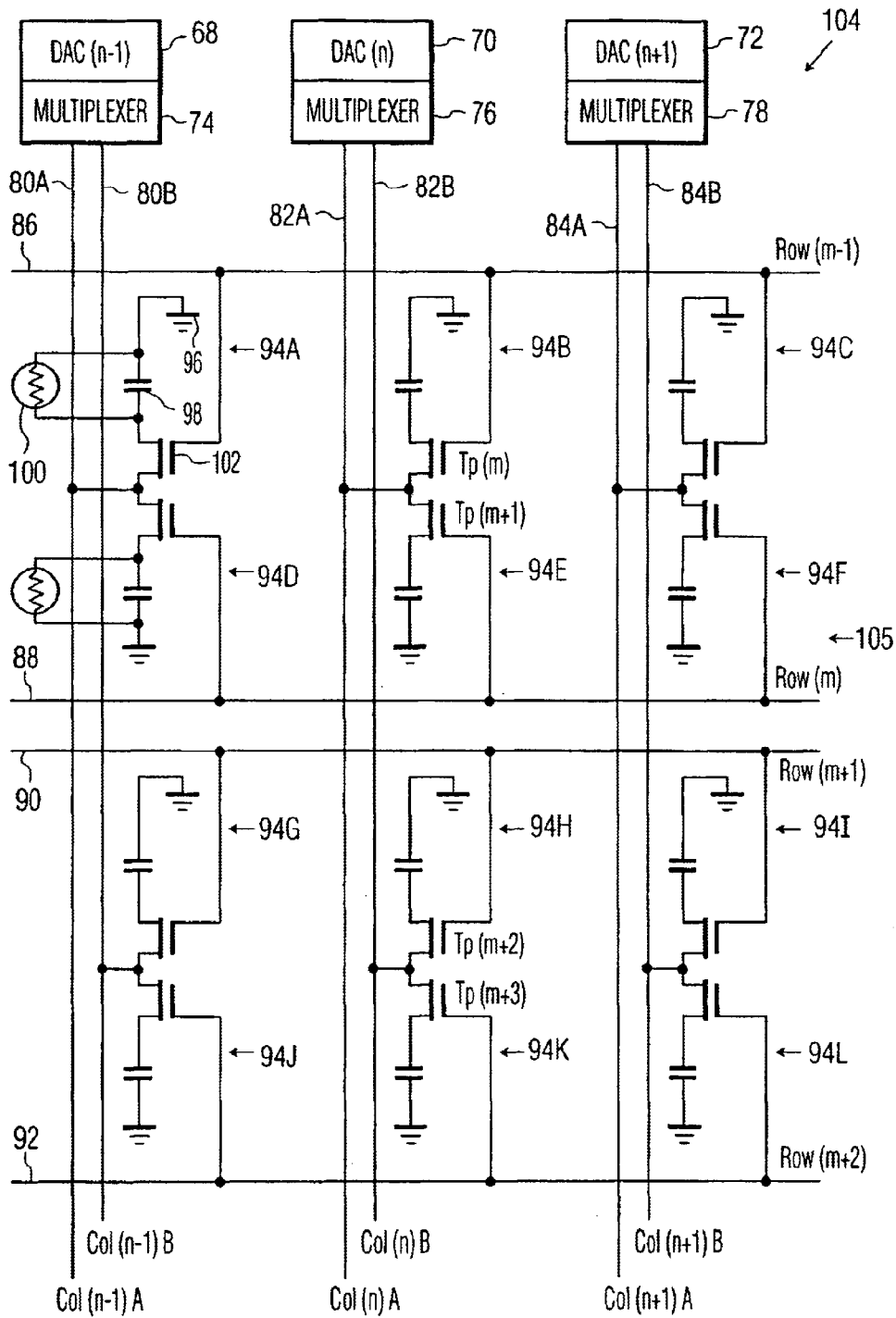


FIG. 4

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