

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

SONY CORPORATION, SAMSUNG ELECTRONICS CO., LTD.,
SAMSUNG DISPLAY CO., LTD

Petitioners

Patent No. 7,202,843

Issue Date: April 10, 2007

Title: DRIVING CIRCUIT OF A LIQUID CRYSTAL DISPLAY
PANEL AND RELATED DRIVING METHOD

CORRECTED PETITION FOR *INTER PARTES* REVIEW

OF U.S. PATENT NO. 7,202,843

No. IPR2015-00862

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| i. A driving circuit for driving an LCD panel, | 13 |
| ii. the LCD panel comprising: | 13 |
| a. a plurality of scan lines;..... | 15 |
| b. a plurality of data lines;..... | 15 |
| c. and a plurality of pixels | 16 |
| iii. the driving circuit comprising:..... | 18 |
| a. a blur clear converter for . . . ;..... | 18 |
| 1. for receiving frame data | 19 |
| 2. delaying current frame data to generate delayed frame data | 20 |
| 3. generating a plurality of overdriven pixel data | 21 |
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| c. a gate driver for applying a scan line voltage | 28 |
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|------|---|----|
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| c. | a first image memory for delaying the pixel data for a frame period;..... | 33 |
| d. | a processing circuit for generating the plurality of overdriven pixel data . . . ; | 34 |
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| c. | a first image memory for delaying the pixel data for a frame period;..... | 40 |
| d. | a processing circuit for generating the plurality of overdriven pixel data . . . ; | 40 |
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| ii. | the LCD panel comprising: | 44 |
| a. | a plurality of scan lines;..... | 45 |
| b. | a plurality of data lines;..... | 45 |
| c. | and a plurality of pixels | 46 |
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| a. | a blur clear converter for . . . ;..... | 47 |
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Exhibit List

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| Ex. Sony-1001 | U.S. Patent No. 7,202,843 |
| Ex. Sony-1002 | Publicly Available File History of U.S. Patent No. 7,202,843 |
| Ex. Sony-1003 | U.S. Patent Application Publication No. 2003/0156092 (August 21, 2003) |
| Ex. Sony-1004 | Japanese Laid Open Patent Application No. 2002-132224 (May 9, 2002) |
| Ex. Sony-1005 | Certified Translation of Japanese Laid Open Patent Application No. 2002-13224 |
| Ex. Sony-1006 | U.S. Patent Application Publication No. US 2003/0214473 (Nov. 20, 2003) |
| Ex. Sony-1007 | U.S. Patent Application Publication No. US 2002/0044115 (April 18, 2002) |
| Ex. Sony-1008 | Ernst Lueder, LIQUID CRYSTAL DISPLAYS (2001) |
| Ex. Sony-1009 | William C. O'Mara, LIQUID CRYSTAL FLAT PANEL DISPLAYS (1993) |
| Ex. Sony-1010 | Betty Prince, HIGH PERFORMANCE MEMORIES (1996) |
| Ex. Sony-1011 | Baek-woon Lee, et al., <i>Reducing Gray-Level Response to One Frame: Dynamic Capacitance Compensation</i> (2001) |
| Ex. Sony-1012 | <i>IEEE 100: The Authoritative Dictionary of IEEE Standards Terms</i> (7th ed., 2000) |
| Ex. Sony-1013 | MCGRAW-HILL DICTIONARY OF SCIENTIFIC AND TECHNICAL TERMS (6th ed., 2003) |
| Ex. Sony-1014 | MICROSOFT COMPUTER DICTIONARY (5th ed., 2002) |
| Ex. Sony-1015 | Declaration of Thomas Credelle |
| Ex. Sony-1016 | <i>Curriculum Vitae</i> of Thomas Credelle |
| Ex. Sony-1017 | U.S. Patent Application Publication No. 2003/0048247 (March 13, 2003) |

I. Mandatory Notices (37 C.F.R. § 42.8)

Real Party-in-Interest: Sony Corporation, Samsung Electronics Co., Ltd., and Samsung Display Co., Ltd. (“Petitioners”); Sony Electronics Inc., Sony Corporation of America, and Samsung Electronics America, Inc..

Related Matters: The following matters would affect or be affected by the decision in this proceeding: *Surpass Tech Innovation LLC ("Surpass") v. Sharp Corp., et al.*, No. 14-cv-00338-LPS (D. Del.); *Surpass v. Samsung Display Co., Ltd., et al.*, No. 14-cv-00337-LPS (D. Del.); *Surpass v. LG Display Co. Ltd., et al.*, No. 14-cv-00336-LPS (D. Del.); *Sharp Corp. v. Surpass*, IPR2015-00021 (P.T.A.B.); Petitioners’ IPR2015-00863 (P.T.A.B.).

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II. Grounds for Standing (37 C.F.R. § 42.104(a))

Petitioners certify that the patent for which review is sought, U.S. Patent No. 7,202,843 (“the ’843 Patent,” Sony-1001), is available for *inter partes* review and that Petitioners are not barred or estopped from requesting an *inter partes* review challenging the patent claims on the grounds identified in this petition.

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