

EXHIBIT

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Keegan et al.

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(54) **HIGH FREQUENCY SIGNAL RECEIVER WITH SELF-CALIBRATED GROUP DELAY COMPENSATION**

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(71) Applicant: **Deere & Company**, Moline, IL (US)

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(72) Inventors: **Richard G. Keegan**, Palos Verdes Estates, CA (US); **Jerry E. Knight**, Long Beach, CA (US)

(73) Assignee: **Deere & Company**, Moline, IL (US)

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(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

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This patent is subject to a terminal disclaimer.

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(21) Appl. No.: **13/892,209**

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Related U.S. Application Data

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Primary Examiner — Don N Vo

(74) *Attorney, Agent, or Firm* — Morgan, Lewis & Bockius LLP

(51) **Int. Cl.**

H04B 1/10 (2006.01)
H04B 1/12 (2006.01)

(52) **U.S. Cl.**

CPC **H04B 1/123** (2013.01)
USPC **375/350**

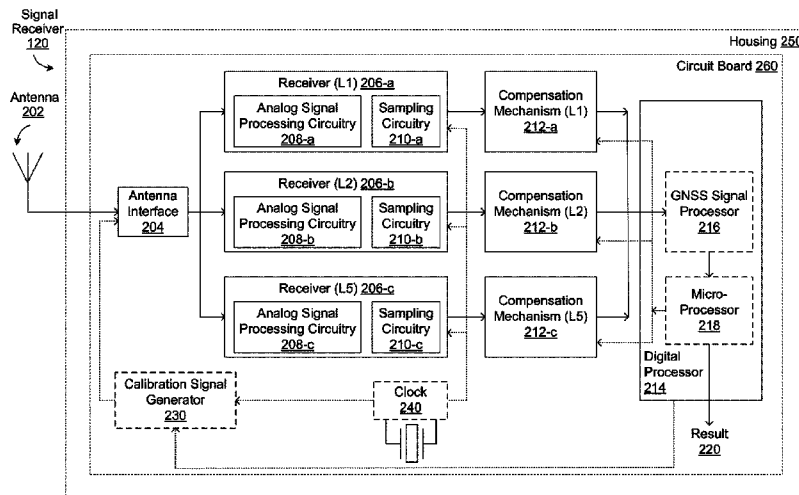
(58) **Field of Classification Search**

CPC H04B 1/123; H04L 25/061; H04L 7/0054; H04L 27/364
USPC 375/232–236, 350
See application file for complete search history.

(57) **ABSTRACT**

A signal receiver includes an antenna interface for receiving signals from an antenna, analog signal processing circuitry coupled to the antenna interface for processing the received signals to produce filtered signals, sampling circuitry to sample the filtered signals so as to produce digitized received signals, a digital compensator to receive the digitized received signals and compensate for non-uniform group delay introduced by the analog signal processing circuitry to produce compensated digitized received signals, and a digital processor to process the compensated digitized received signals so as to produce a result.

23 Claims, 13 Drawing Sheets



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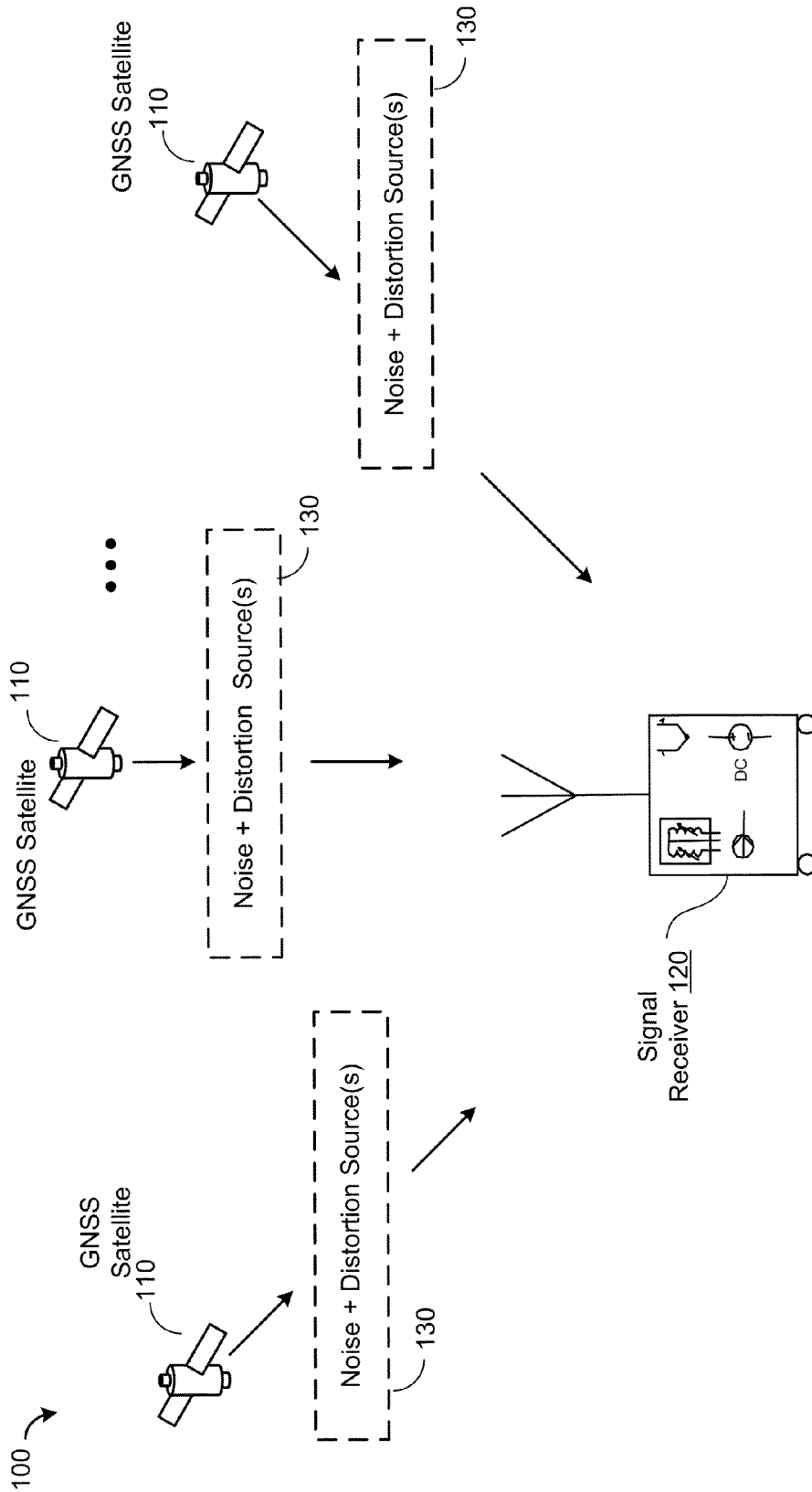


Figure 1

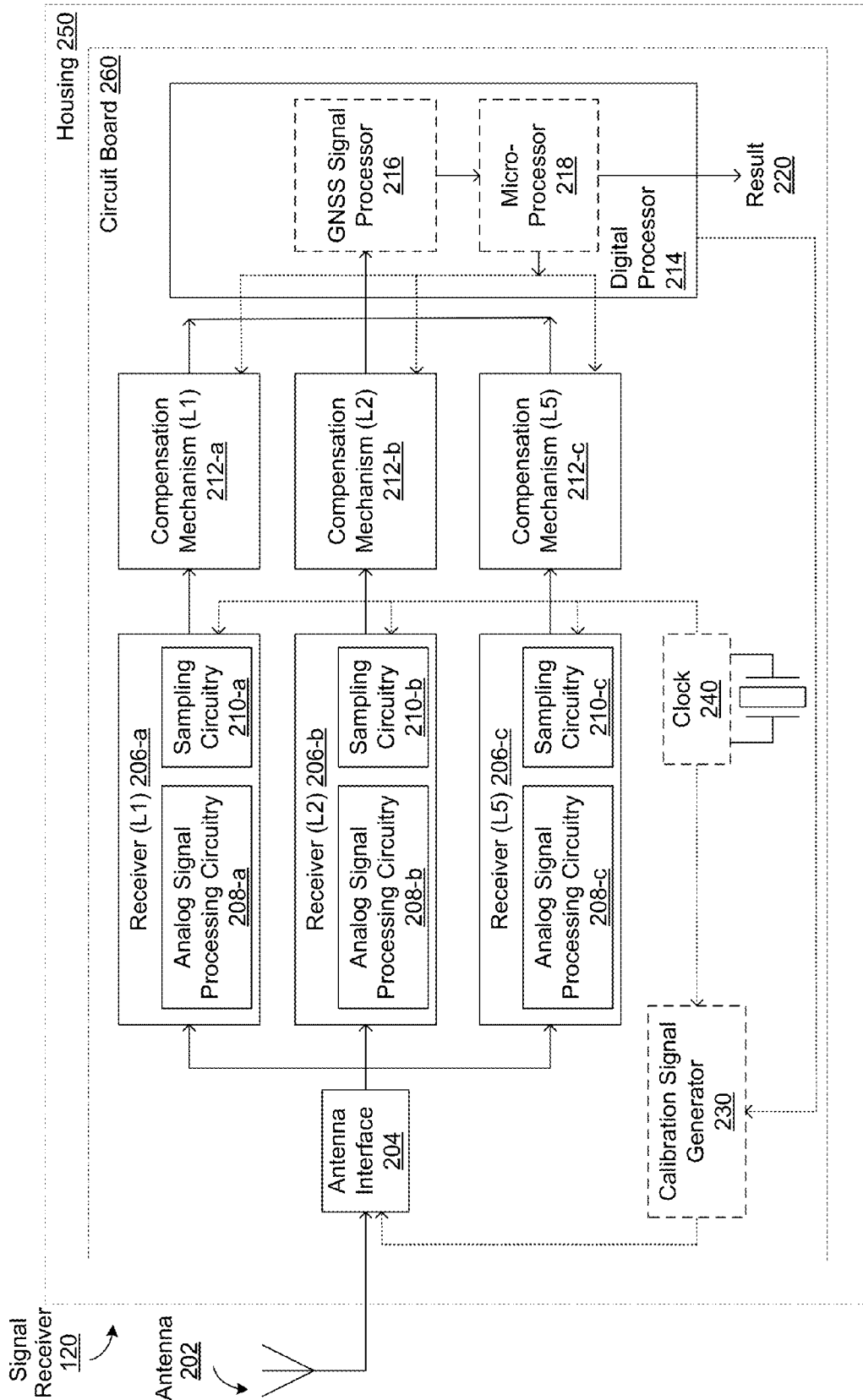


Figure 2

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