



US007281055B2

(12) **United States Patent**
Glasco et al.

(10) **Patent No.:** **US 7,281,055 B2**
(45) **Date of Patent:** **Oct. 9, 2007**

(54) **ROUTING MECHANISMS IN SYSTEMS HAVING MULTIPLE MULTI-PROCESSOR CLUSTERS**

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(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 797 days.

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(21) Appl. No.: **10/156,893**

(22) Filed: **May 28, 2002**

(65) **Prior Publication Data**

US 2003/0225938 A1 Dec. 4, 2003

(51) **Int. Cl.**
G06F 15/16 (2006.01)
G06F 13/00 (2006.01)

(52) **U.S. Cl.** **709/232; 709/217; 709/238; 711/148**

(58) **Field of Classification Search** **709/217-219, 709/230-245, 212-216; 370/389-393; 712/10-15; 711/150, 147-148**

See application file for complete search history.

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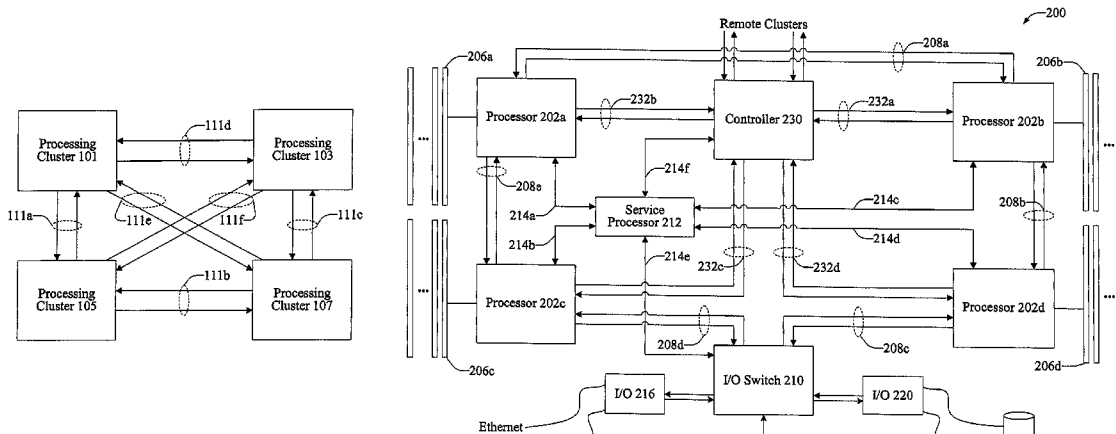
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(57) **ABSTRACT**

A multi-processor computer system is described in which address mapping, routing, and transaction identification mechanisms are provided which enable the interconnection of a plurality of multi-processor clusters, wherein the number of processors interconnected exceeds limited address, node identification, and transaction tag spaces associated with each of the individual clusters.

18 Claims, 9 Drawing Sheets



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Fig. 1A

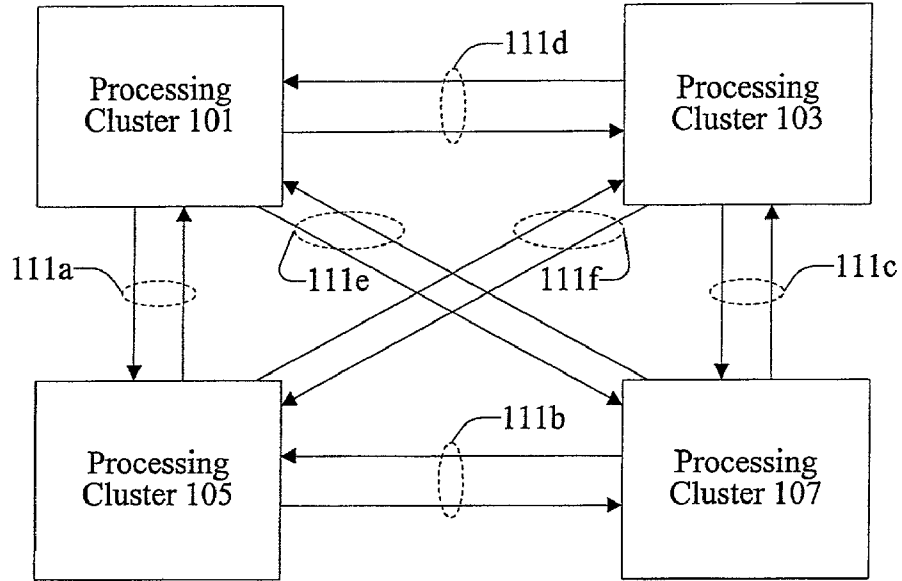


Fig. 1B

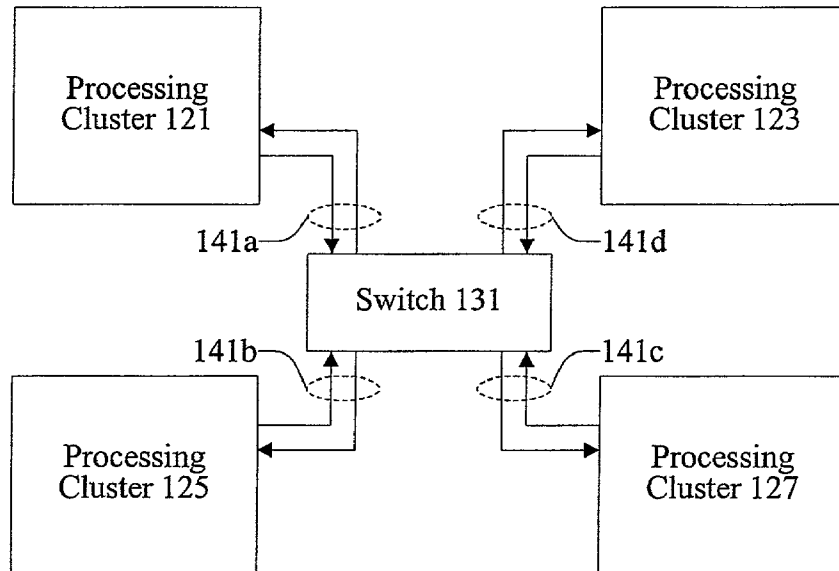
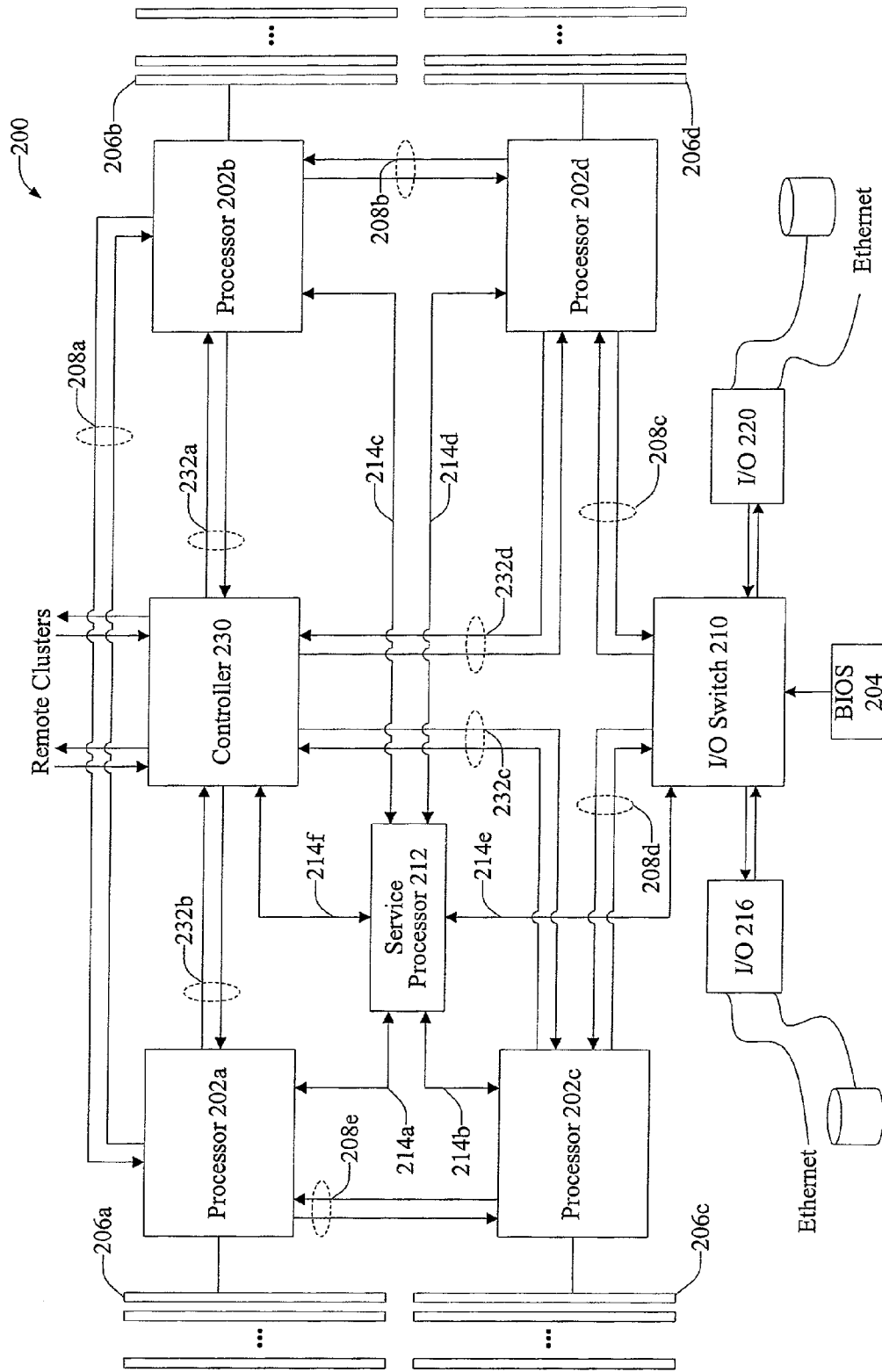


Fig. 2



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