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**Glasco et al.**

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- (54) **TRANSACTION MANAGEMENT IN SYSTEMS HAVING MULTIPLE MULTI-PROCESSOR CLUSTERS** 5,682,512 A 10/1997 Tetrick ..... 711/202  
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- (\* ) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 739 days.

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**G06F 15/167** (2006.01)

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(52) **U.S. Cl.** ..... **709/229**; 709/212; 709/216; 709/228

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(58) **Field of Classification Search** ..... 709/201, 709/212-216, 227, 228, 229; 714/100  
See application file for complete search history.

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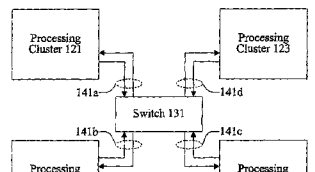
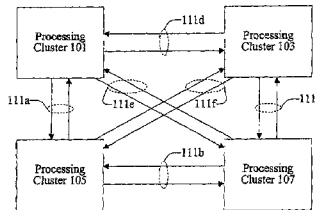
(57) **ABSTRACT**

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A multi-processor computer system is described in which address mapping, routing, and transaction identification mechanisms are provided which enable the interconnection of a plurality of multi-processor clusters, wherein the number of processors interconnected exceeds limited address, node identification, and transaction tag spaces associated with each of the individual clusters.

**30 Claims, 9 Drawing Sheets**



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Fig. 1A

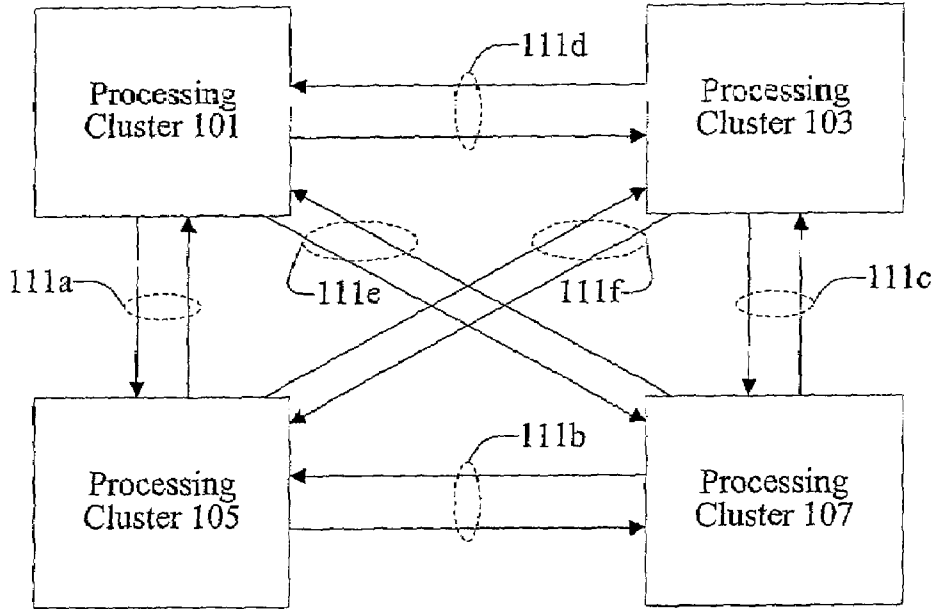


Fig. 1B

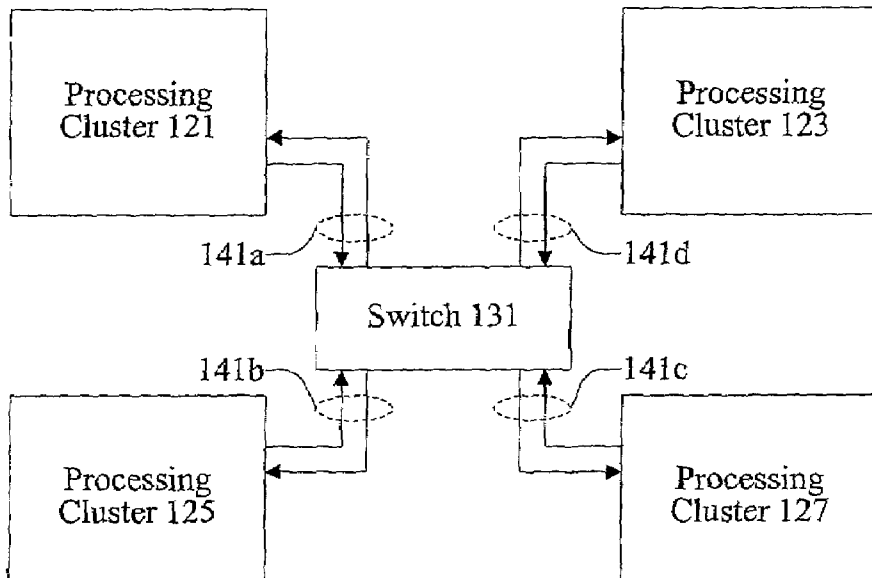


Fig. 2

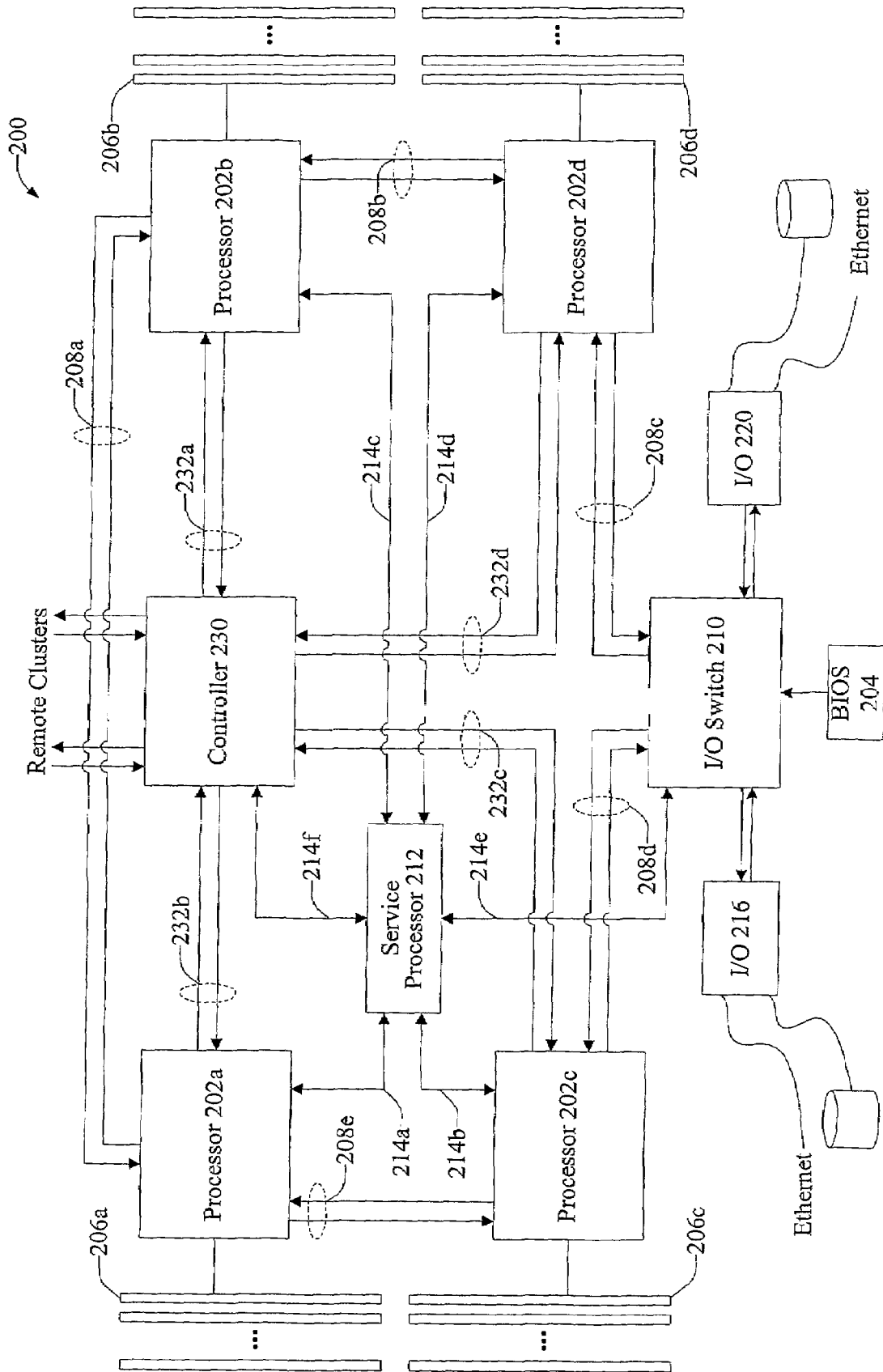
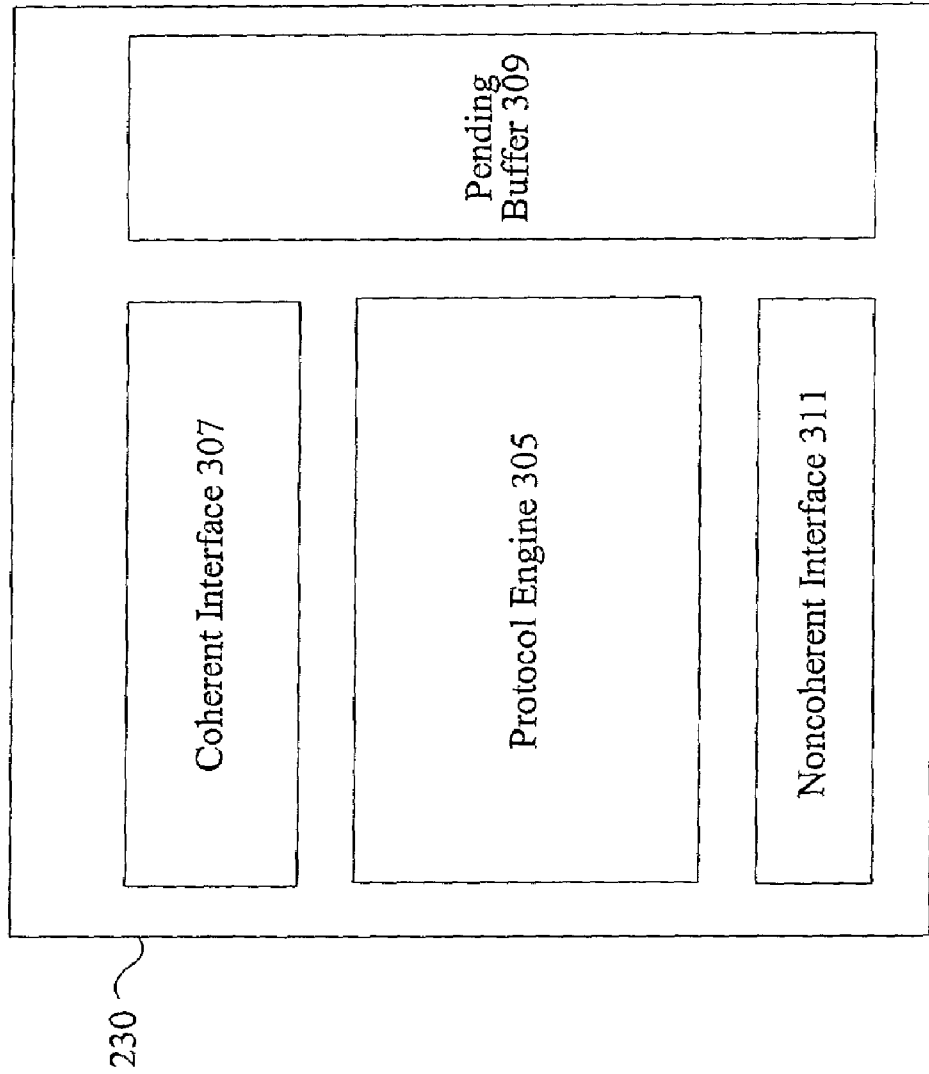


Fig. 3



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