

ncy

ison

processors) support shared
cores may read and write to
consistency model defines
definitions provide rules about
memory. As part of supporting
proto-cols that ensure that
r is to provide readers with
cludes both the issues that
concepts as well as specific,

OD

A PRIMER ON MEMORY CONSISTENCY AND CACHE COHERENCE

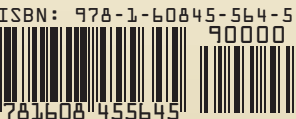
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A Primer on Memory Consistency and Cache Coherence

Daniel J. Sorin
Mark D. Hill
David A. Wood

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Apple Inc., et al. v.
Memory Integrity, LLC
IPR2015-00159, -00163
EXHIBIT
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