UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE PATENT TRIAL AND APPEAL BOARD

APPLE INC., HTC CORPORATION, HTC AMERICA, INC., SAMSUNG ELECTRONICS CO. LTD, SAMSUNG ELECTRONICS AMERICA, INC., SAMSUNG TELECOMMUNICATIONS AMERICA, LLC AND AMAZON.COM, INC., Petitioners

uuoners

V.

MEMORY INTEGRITY, LLC
Patent Owner

U.S. Patent No. 7,296,121

Inter Partes Review Case No. 2015-00161

MEMORY INTEGRITY, LLC'S PATENT OWNER PRELIMINARY RESPONSE PURSUANT TO 37 CFR § 42.107(a)



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	В.		ites associated with selected ones of the cache memories" ims 1, 16, 25)
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		2.	A cache coherence protocol state is the current state of a data block in a protocol used to maintain the coherency of caches, in which a data block can only be in one current state at a time, and in which the current state can transition to a different state upon one or more triggering events or conditions
		3.	"states associated with selected ones of cache memories" refers to the cache coherence protocol state(s) of data block(s) which are <i>stored</i> in the selected cache memories
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В.

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EXHIBIT LIST

Exhibit No.	Description
Memory Integrity-2001	Plaintiff Memory Integrity, LLC's Initial Identification
	of Asserted Claims And Accused Products, served on
	Petitioners in Memory Integrity LLC v. Amazon.com
	Inc., et al., Nos. 1:13-cv-01795, -01796, -01802,
	-01808 (D. Del. served Oct. 13, 2014)
Memory Integrity-2002	Excerpts from D. E. Culler, J. P. Singh, and A. Gupta
	PARALLEL COMPUTER ARCHITECTURE, pp. 279-280
	(1999)
Memory Integrity-2003	Sorin et al., "Specifying and Verifying a Broadcast and
	a Multicast Snooping Cache Coherence Protocol,"
	IEEE Transactions on Parallel and Distributed
	SYSTEMS, Vol. 13, No. 6, pp. 1-23(June 2002)
Memory Integrity-2004	Excerpts from Merriam-Webster's Collegiate
	Dictionary (10 th ed. 1999)
Mamany Integrity 2005	,
Memory Integrity-2005	Excerpts from David A. Patterson, et al., COMPUTER
	ORGANIZATION AND DESIGN (3d ed. 2005)



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