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**Glasco et al.**

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(54) **ROUTING MECHANISMS IN SYSTEMS HAVING MULTIPLE MULTI-PROCESSOR CLUSTERS**

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(57) **ABSTRACT**

A multi-processor computer system is described in which address mapping, routing, and transaction identification mechanisms are provided which enable the interconnection of a plurality of multi-processor clusters, wherein the number of processors interconnected exceeds limited address, node identification, and transaction tag spaces associated with each of the individual clusters.

18 Claims, 9 Drawing Sheets

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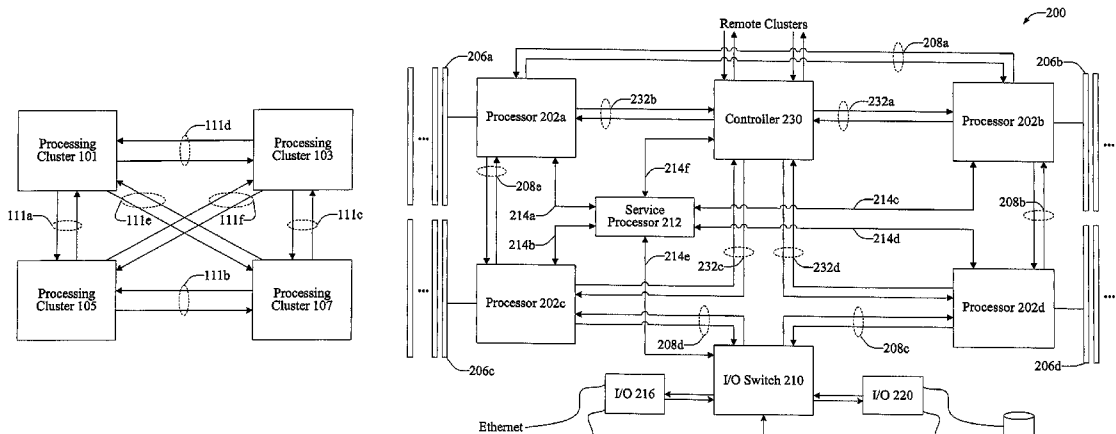
(58) **Field of Classification Search** ..... **709/217–219, 709/230–245, 212–216; 370/389–393; 712/10–15; 711/150, 147–148**

See application file for complete search history.

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Fig. 1A

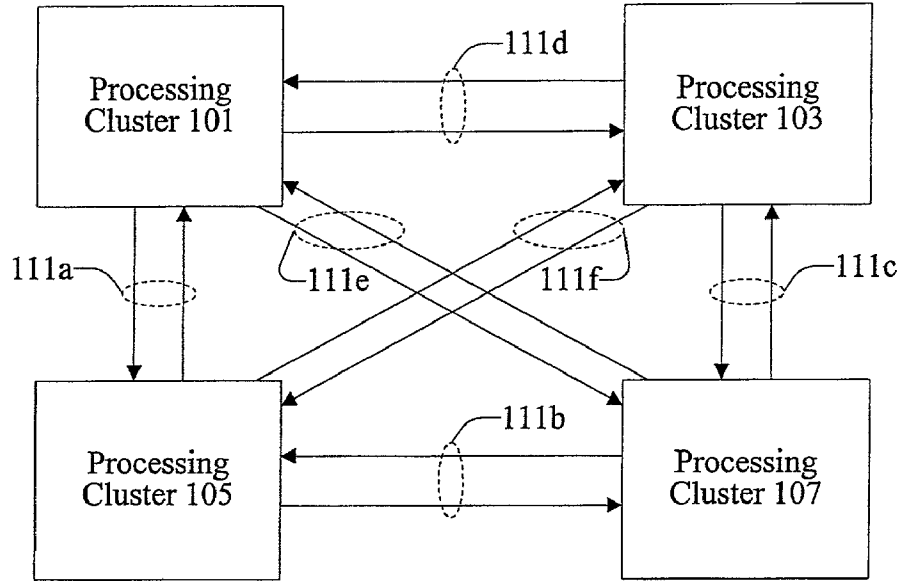


Fig. 1B

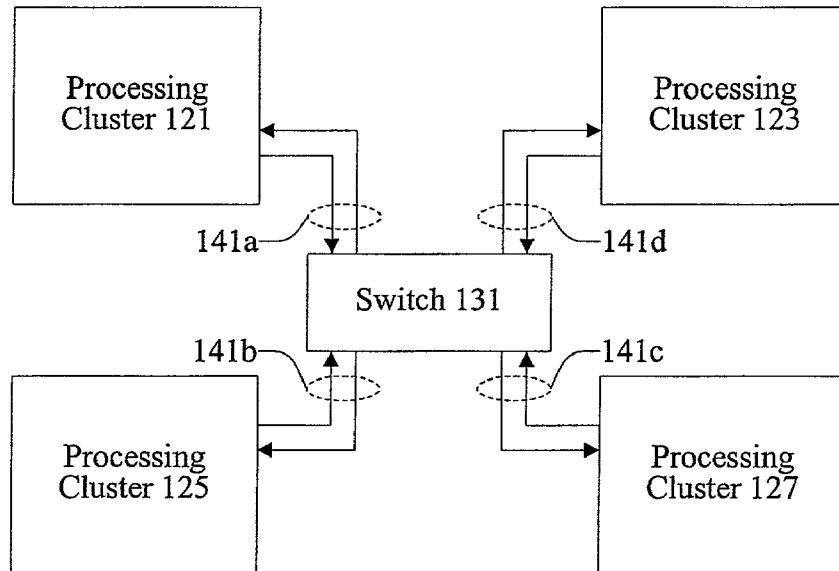
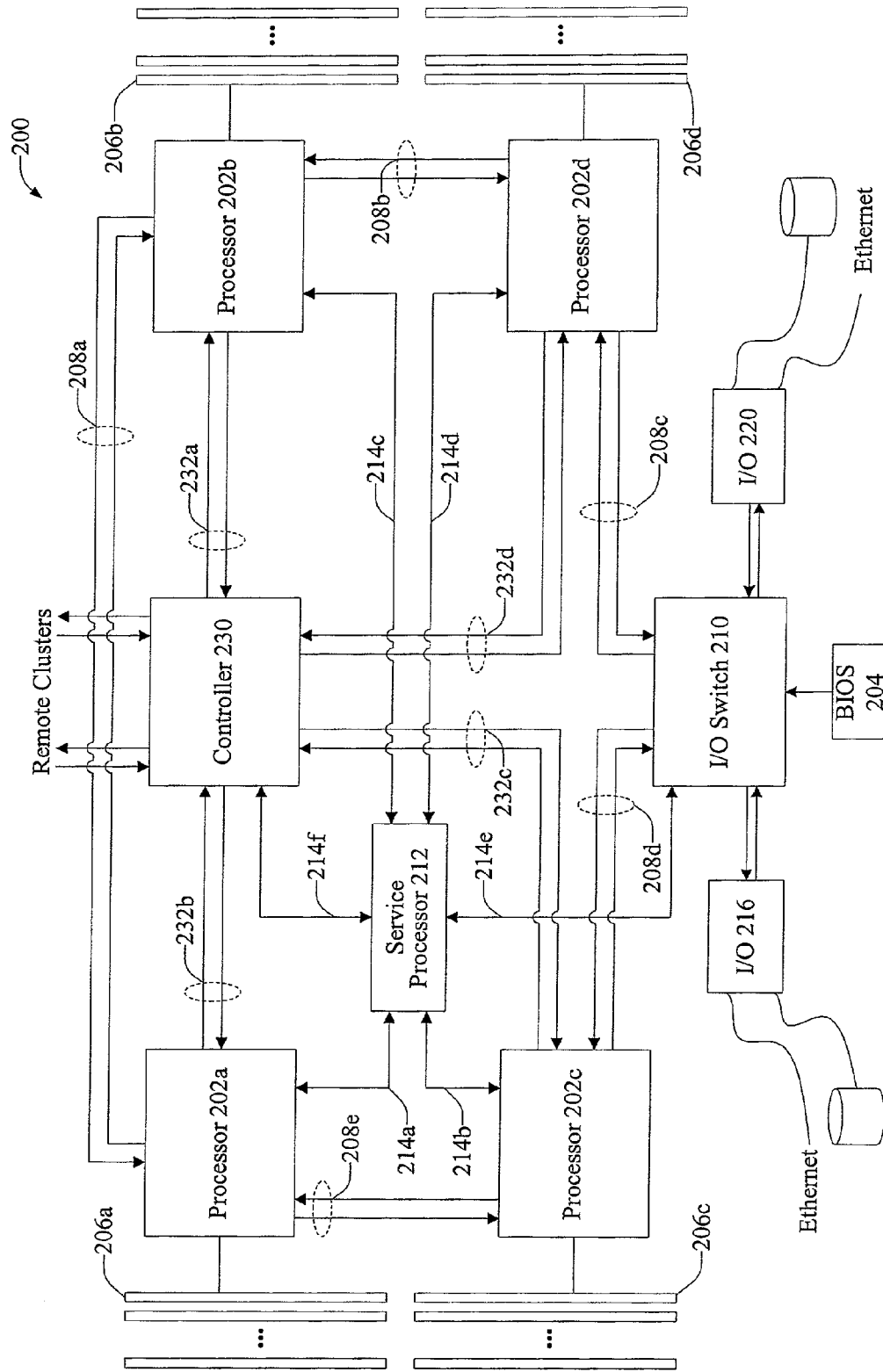


Fig. 2



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