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Glasco et al.

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(54) **ADDRESS SPACE MANAGEMENT IN SYSTEMS HAVING MULTIPLE MULTI-PROCESSOR CLUSTERS**

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(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 834 days.

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G06F 15/16 (2006.01)

(52) **U.S. Cl.** **709/237**; 709/220; 711/202

(58) **Field of Classification Search** 709/237, 709/220; 711/202

See application file for complete search history.

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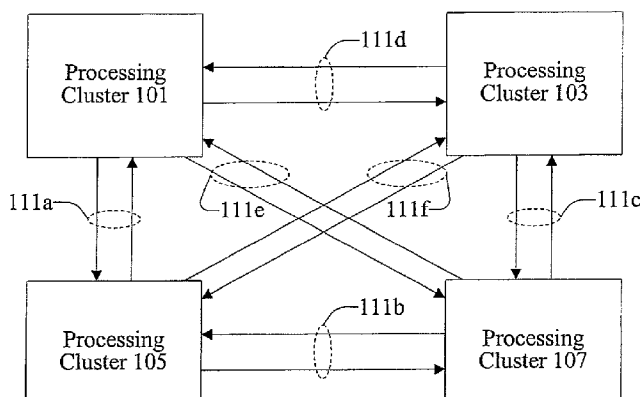
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(57) **ABSTRACT**

A multi-processor computer system is described in which address mapping, routing, and transaction identification mechanisms are provided which enable the interconnection of a plurality of multi-processor clusters, wherein the number of processors interconnected exceeds limited address, node identification, and transaction tag spaces associated with each of the individual clusters.

23 Claims, 9 Drawing Sheets



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Fig. 1A

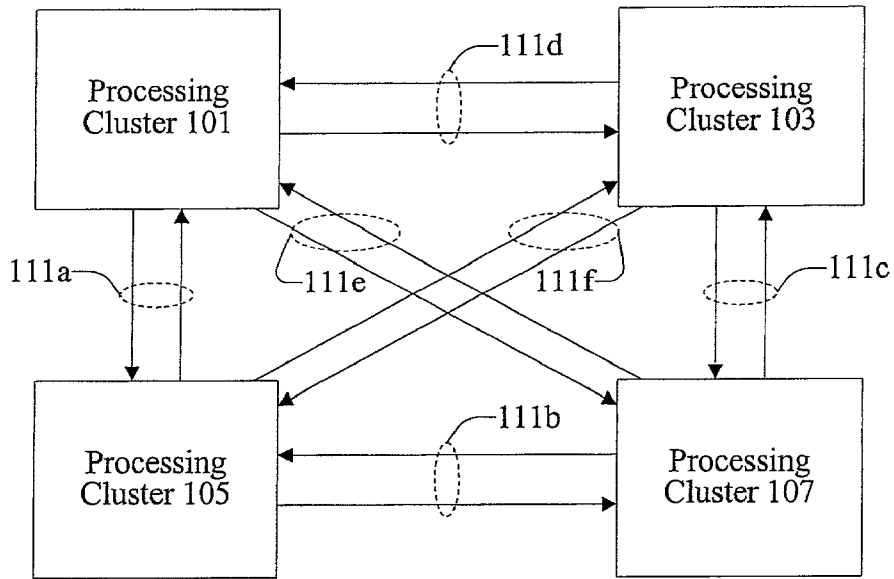


Fig. 1B

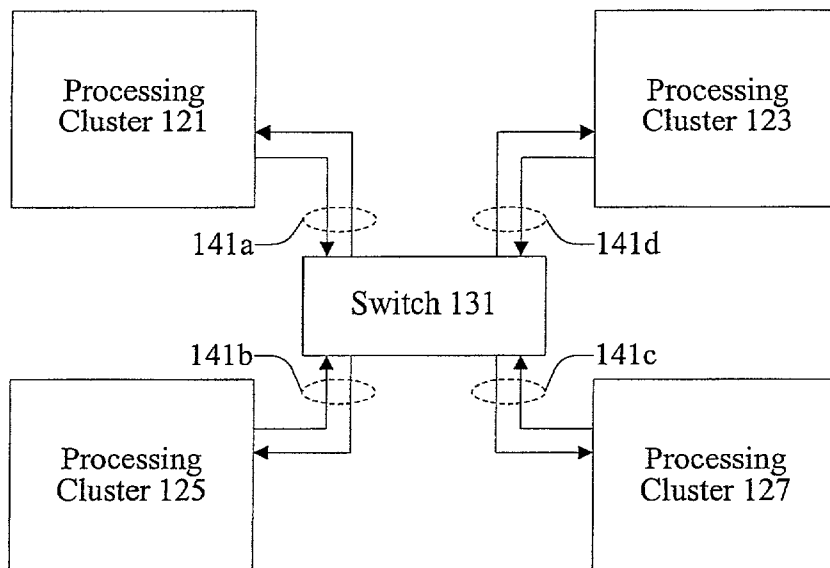


Fig. 2

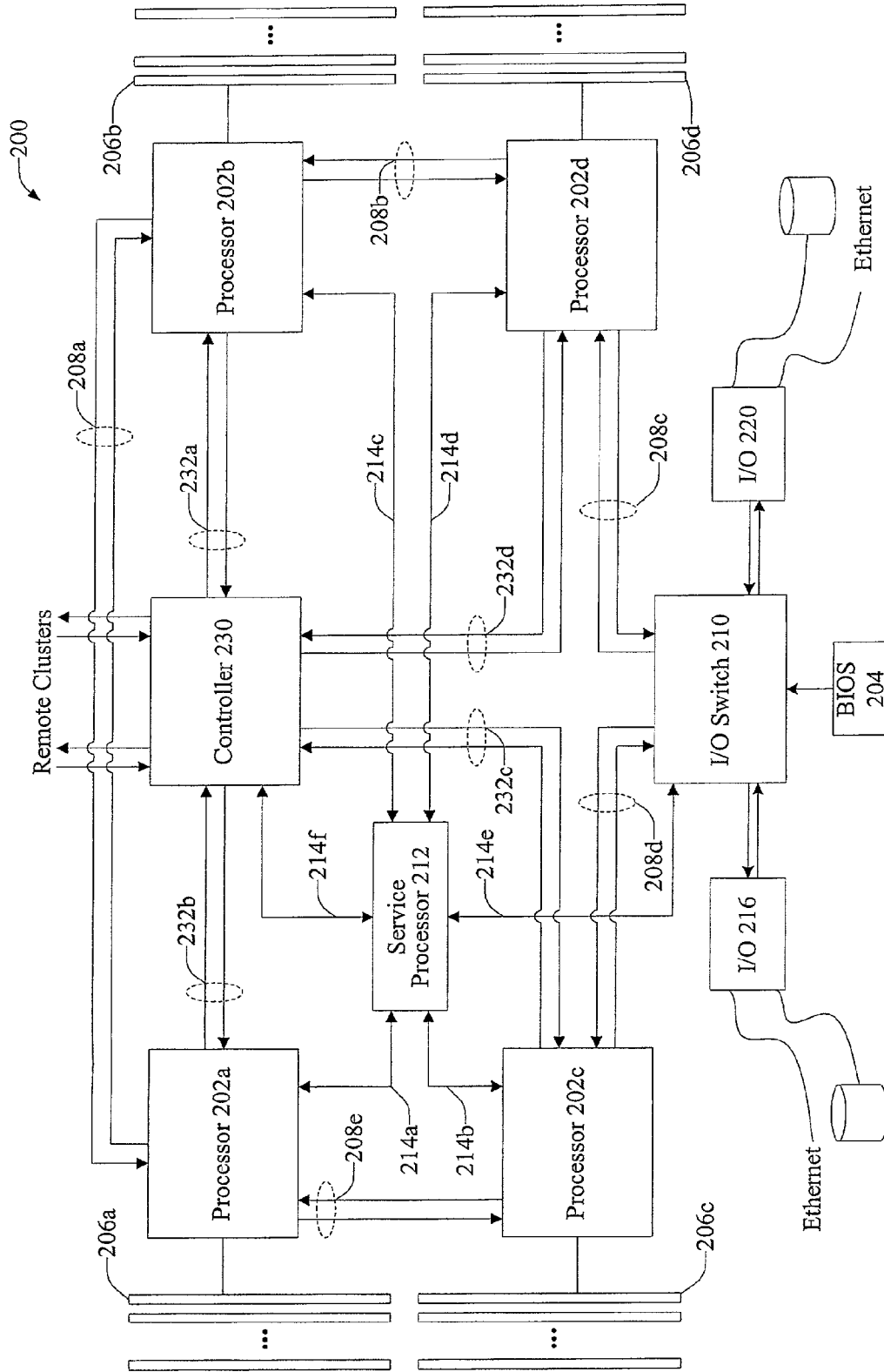
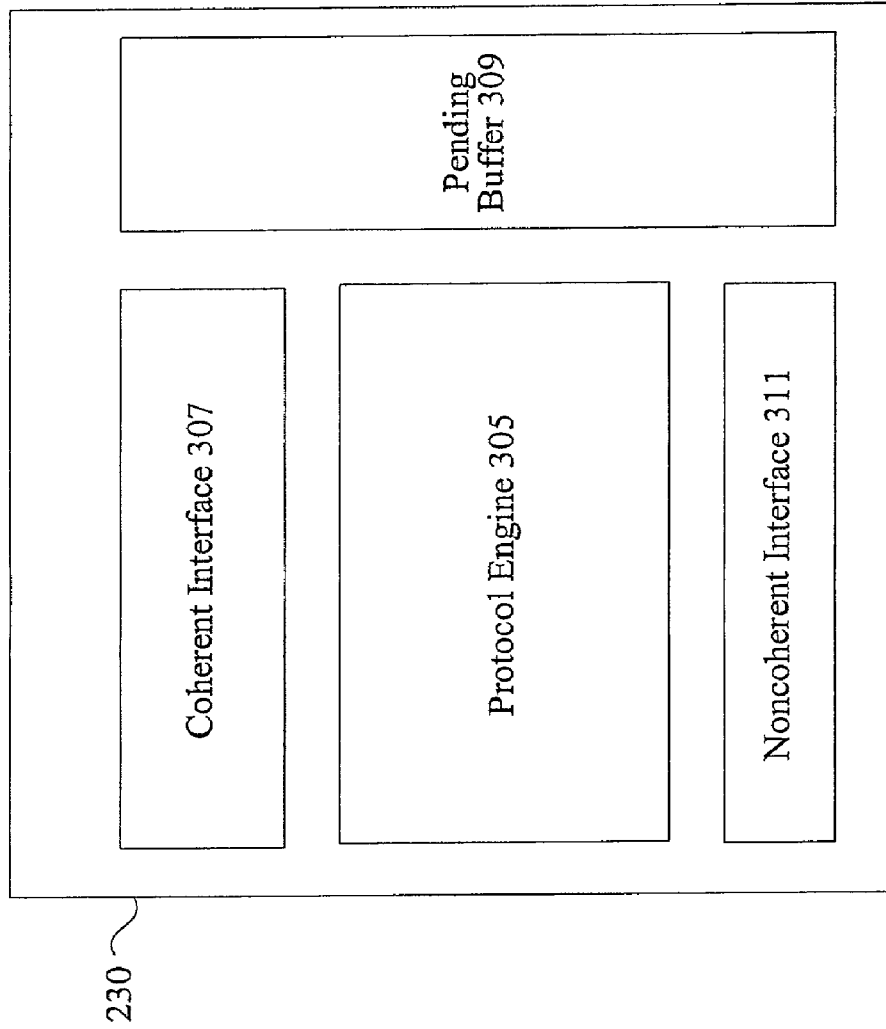


Fig. 3



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