



Apple Inc. et al. (Petitioners)
v.
Memory Integrity, LLC (Patent Owner)

Substitute Claims Demonstratives
Trial Nos. IPR2015-00159 and -00163
U.S. Patent No. 7,296,121

Before Hon. J. S. BISK, N. T. POWELL, and K. BEGLEY,
Administrative Patent Judges

Summary of Amendment

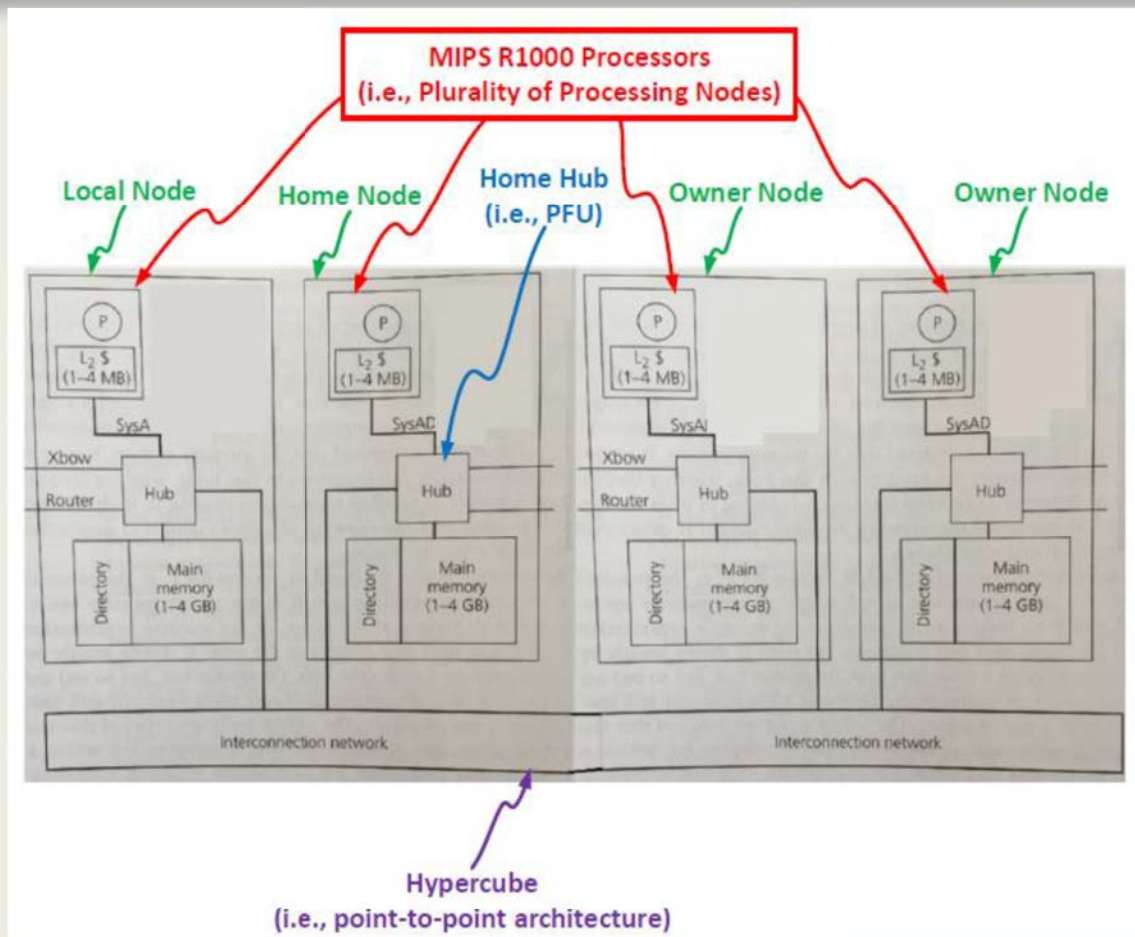
● **Cache Coherency States**

- “wherein said states comprise cache coherency states of a cache coherence protocol, and wherein said cache coherence protocol includes at least a modified state, an exclusive state, a shared state, and an invalid state”

● **Protocol Interface**

- “wherein said probe filtering unit is coupled to a coherent protocol interface and a non-coherent protocol interface”

Combination of Culler and Laudon

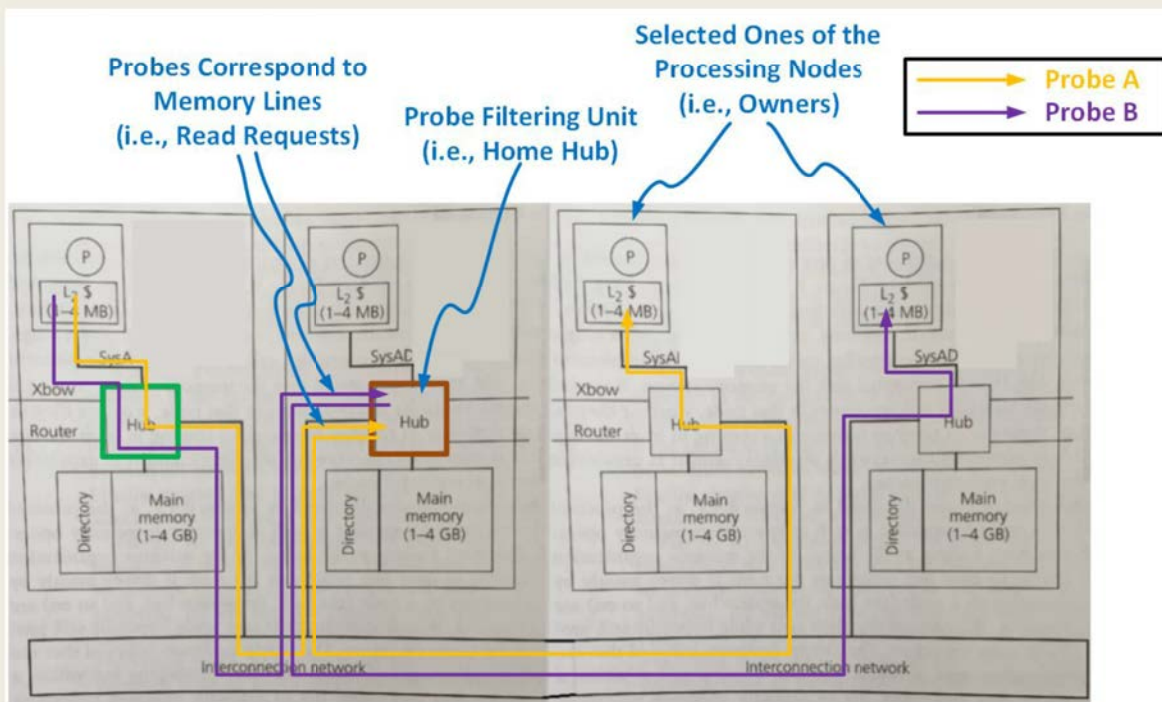


IPR2015-00159, Opposition to Motion to Amend (Paper 36) at 12

Combination of Culler and Laudon

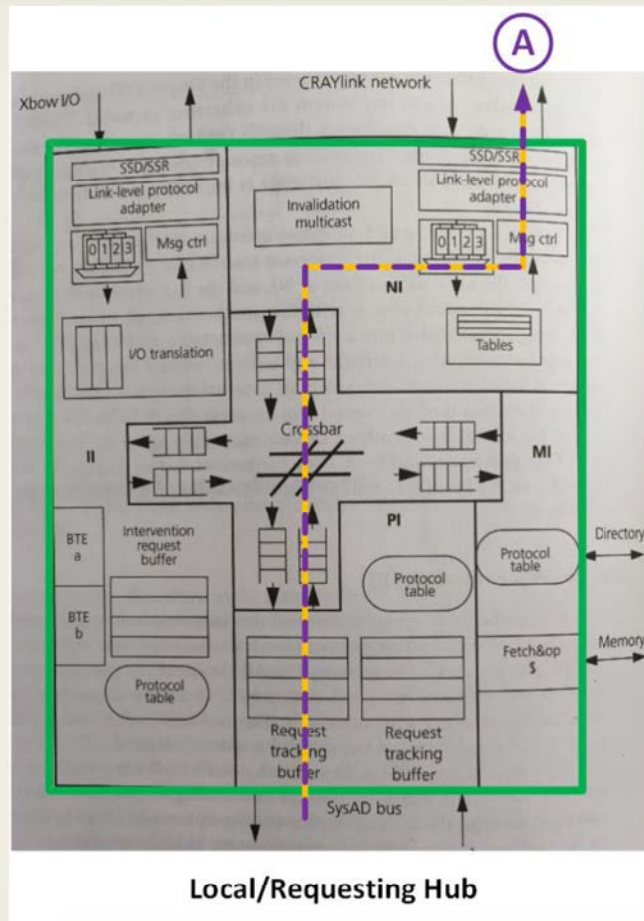
the **probe filtering unit** being operable to receive probes corresponding to memory lines from the processing nodes and to transmit the probes only to **selected ones of the processing nodes**

'121 Patent, Claim 26.



IPR2015-00159, Opposition to Motion to Amend (Paper 36) at 13

Combination of Culler and Laudon



IPR2015-00159, Opposition to Motion to Amend (Paper 36) at 15

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