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(54) **APPARATUS AND A METHOD TO PROVIDE HIGHER BANDWIDTH OR PROCESSING POWER ON A BUS**

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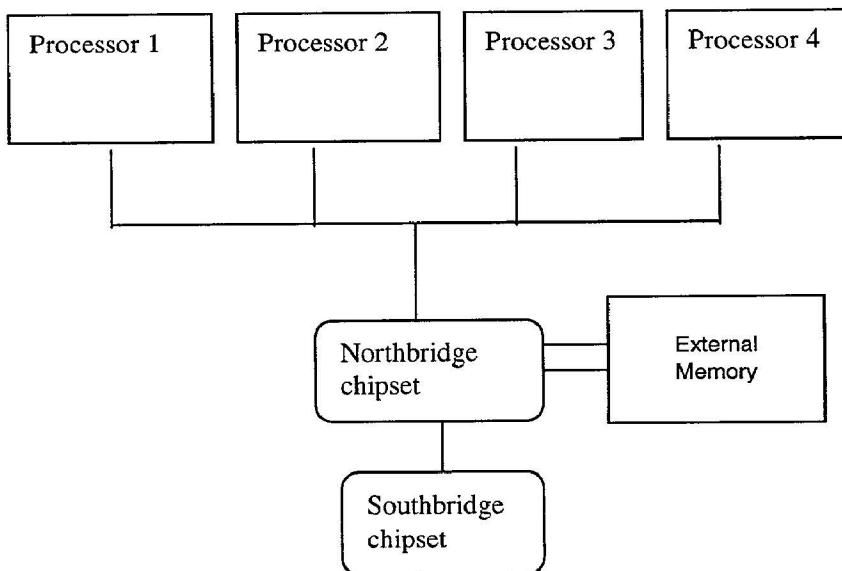
(51) **Int. Cl.⁷ G06F 12/00; G06F 13/14; G06F 13/38**

(52) **U.S. Cl. 710/240**

(57) **ABSTRACT**

A method and apparatus in which an arbiter links to a processor having a flexible architecture, and the processor connects to a device through a point to point bus.

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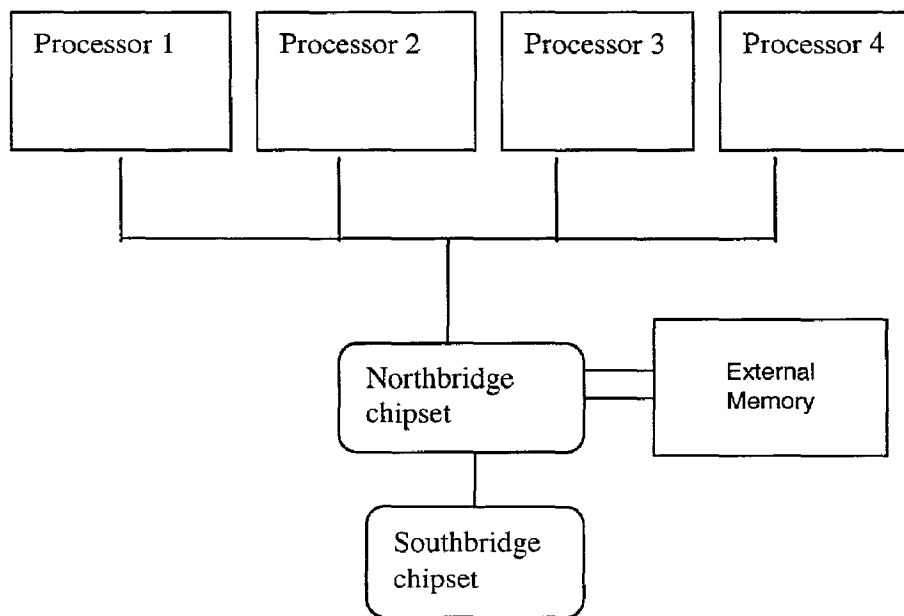


Figure 1

200

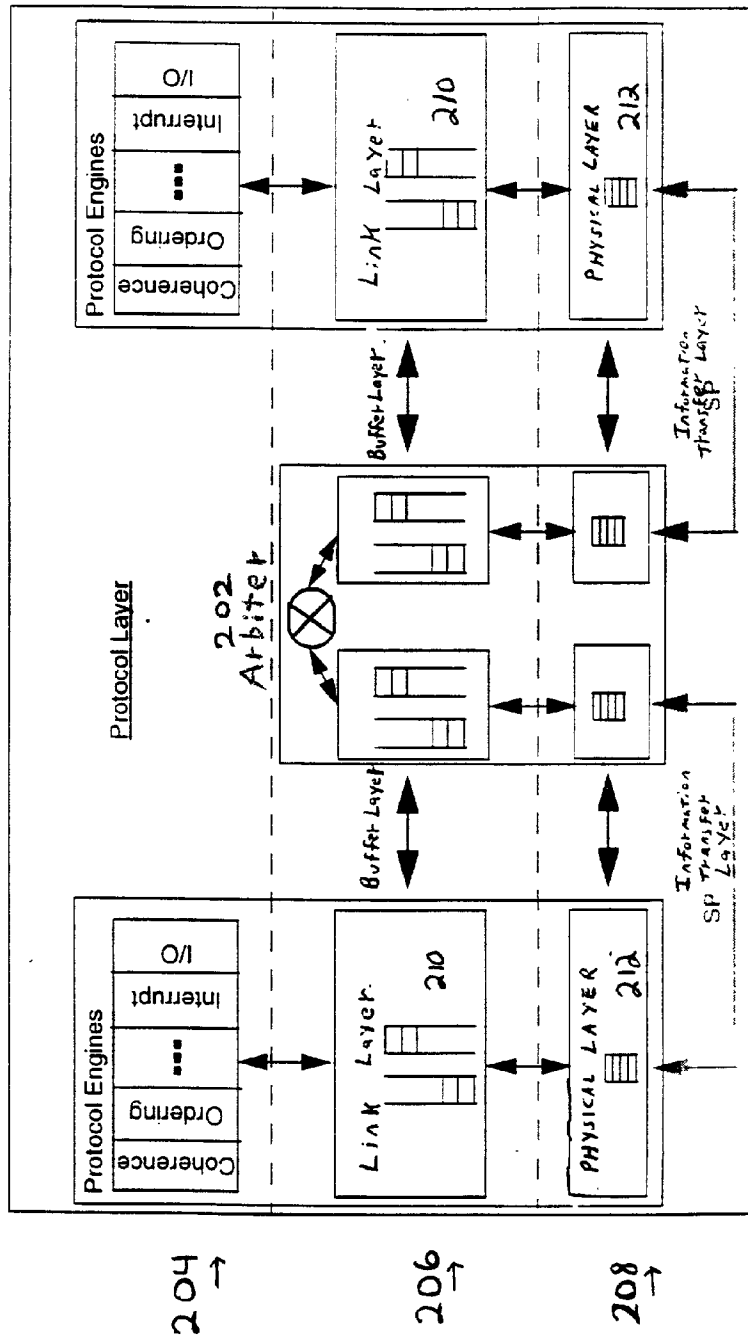


Figure 2

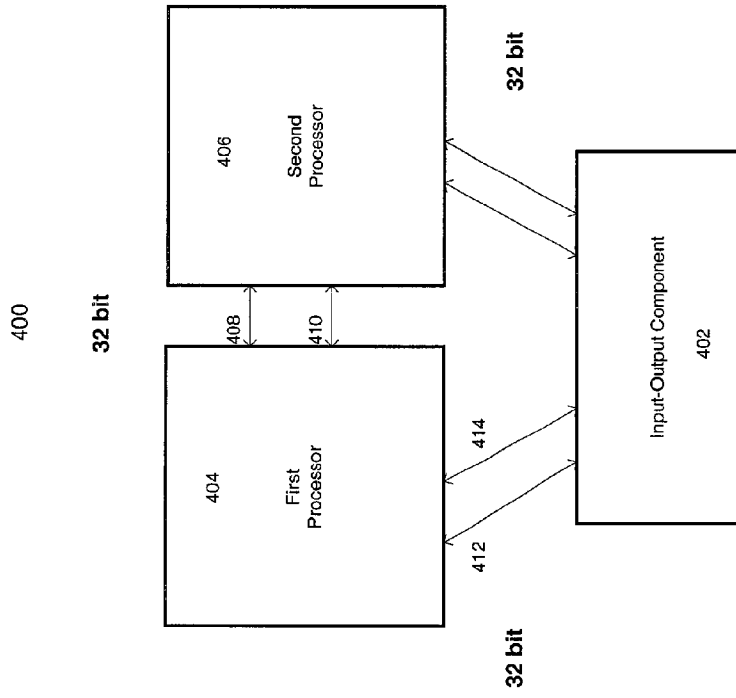


Figure 4

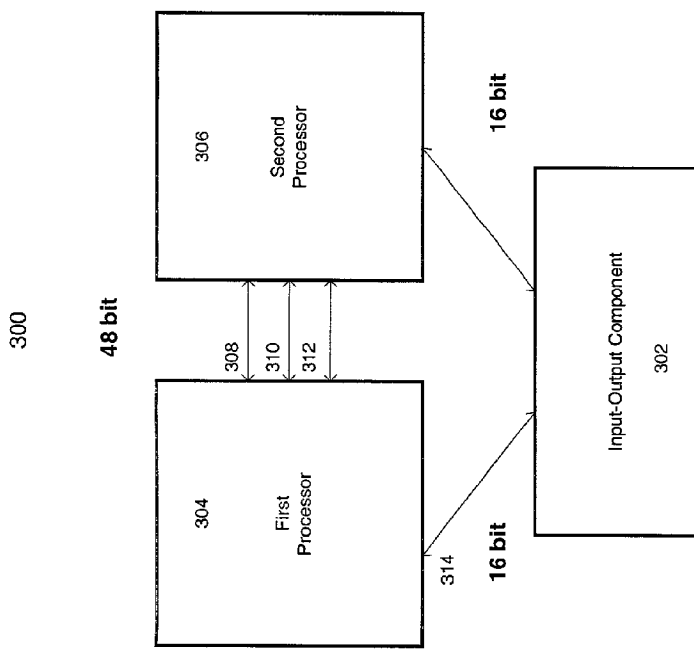


Figure 3

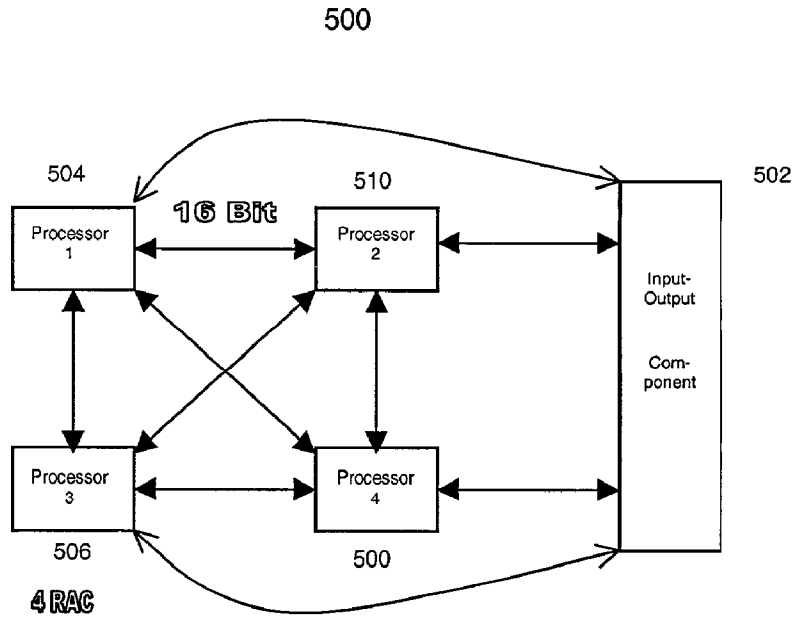


Figure 5

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