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Luick et al.

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[54]	MULTIPROCESSOR CACHE COHERENCE
	DIRECTED BY COMBINED LOCAL AND
	GLOBAL TABLES

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154(a)(2).

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[51]	Int. Cl. ⁷	• • • • • • • • • • • • • • • • • • • •			G06	F 12/00
[52]	U.S. Cl.		711/141;	711/1	18; 7	11/146;
				711/3	144;	711/124

711/165, 141, 146–148, 124, 152, 144

[56] **References Cited**

U.S. PATENT DOCUMENTS

4,747,043	5/1988	Rodman 395/925	
4,843,542	6/1989	Dashiell et al 364/200	
4,928,225	5/1990	McCarthy et al 364/200	
5,025,365	6/1991	Mathur et al 711/121	
5,303,362	4/1994	Butts, Jr. et al 711/121	
5,490,261	2/1996	Bean et al 711/121	
5,537,569	7/1996	Masubushi	
5,577,204	11/1996	Brewer et al 710/132	
5,590,308	12/1996	Shih	
5,604,882	2/1997	Hoover et al 395/448	
5,623,628	4/1997	Brayton et al 395/468	
5,710,907	1/1998	Hagersten et al 711/148	
5,749,095	5/1998	Hagersten 711/141	
5,751,995	5/1998	Sarangdhar	
5,778,429	7/1998	Sukegawa et al 711/129	
5,822,763	10/1998	Baylor et al 711/118	
5,864,854	1/1999	Boyle 707/10	
5,892,970	4/1999	Hagersten 711/141	

5,895,487	4/1999	Boyd et al	711/122
5,897,664	4/1999	Nesheim et al	711/206

OTHER PUBLICATIONS

Leonidas I. Kontothanassis et al. "Lazy Release Consistency for Hardware-Coherent Multiprocessors", University of Rochester, 1994.

Sandra Johnson Baylor et al., "An Evaluation of Cache Coherence Protocols for MIN-Based Multiprocessors", International Symposium on Shared Memory Multiprocessing, pp. 230-241, Apr. 1991.

Proceedings of the 17th International Symposium on Computer Architecture, Jun. 1990, pp. 148-159, "The Directory-Based Cache Coherence Protocol for the DASH Multiprocessor" by Daniel Lenoski et al.

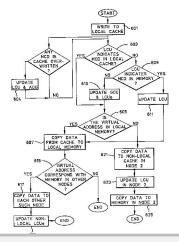
IEEE Computer, vol. 25, No. 03, Mar. 1992, pp. 63-79, "The Stanford Dash Multiprocessor" by Daniel Lenoski et al.

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ABSTRACT

A method and apparatus for maintaining coherence between shared data stored within a plurality of memory devices, each memory device residing in a different node within a tightly coupled multiprocessor system. Each node includes a "local coherence unit" and an associated processor. A cache unit is associated with each memory/processor pair. Each local coherence unit maintains a table which indicates whether the most current copy of data stored within the node resides in the local memory, in the local cache, or in a non-local cache. The present invention includes a "global coherence" unit coupled to each node via the logical interconnect. The global coherence unit includes a interconnect monitoring device and a global coherence table. When data which resides within the memory of a first node is transferred to a second node, the interconnect monitoring device updates the global coherence table to indicate that the data is being shared. The global coherence table also preferably indicates in which node a copy of the most current data resides.

6 Claims, 7 Drawing Sheets



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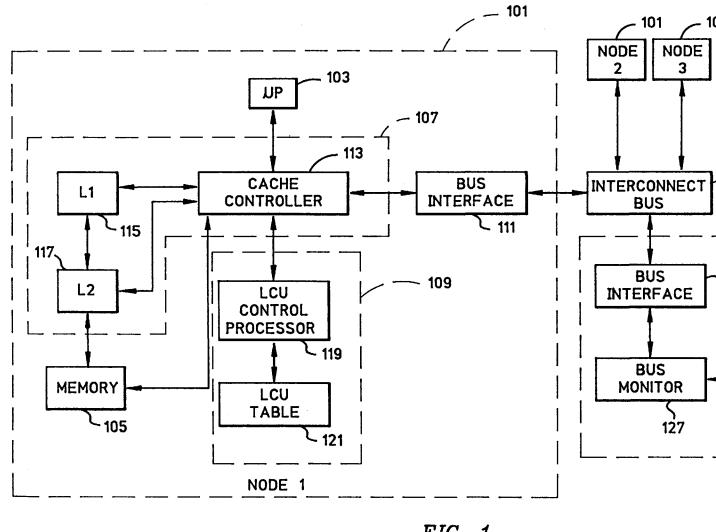
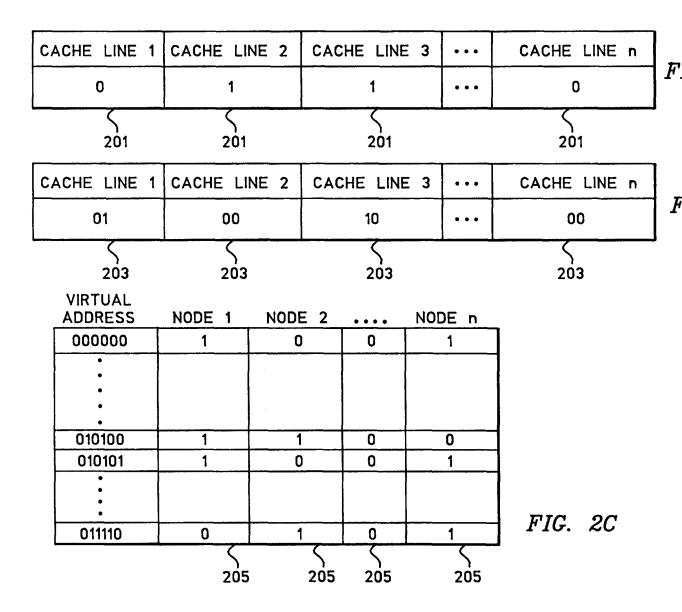


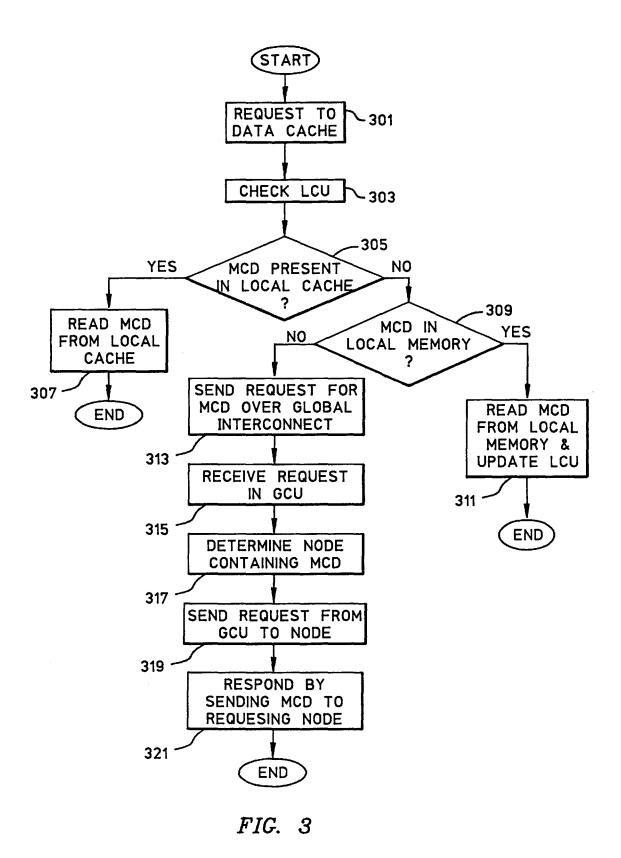
FIG. 1

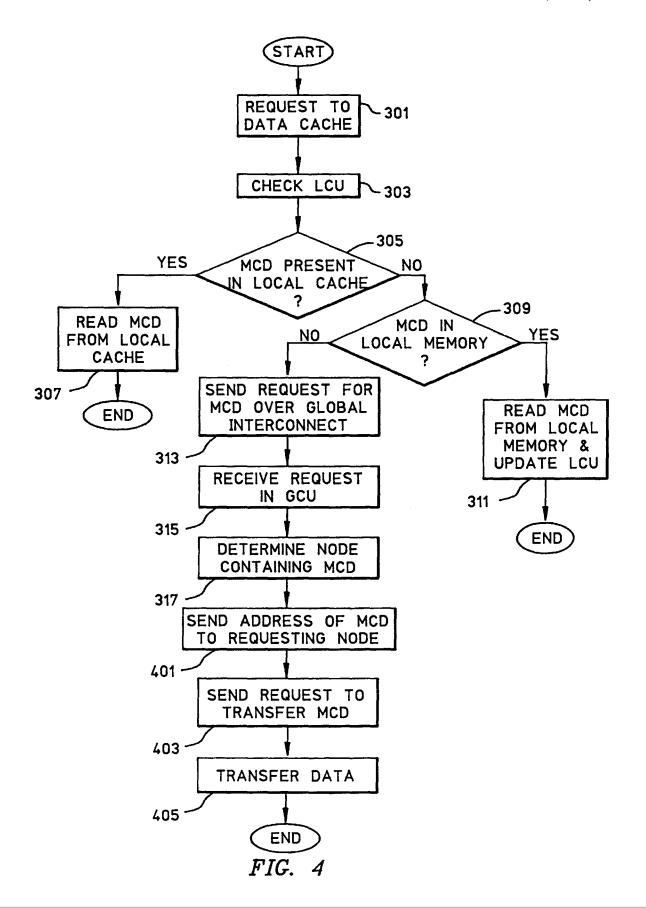




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