

ncy

ison

processors) support shared  
cores may read and write to  
consistency model defines  
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emory. As part of supporting  
proto-cols that ensure that  
r is to provide readers with  
cludes both the issues that  
concepts as well as specific,

OD

A PRIMER ON MEMORY CONSISTENCY AND CACHE COHERENCE

MORGAN & CLAYPOOL

# A Primer on Memory Consistency and Cache Coherence

**Daniel J. Sorin**  
**Mark D. Hill**  
**David A. Wood**

*SYNTHESIS LECTURES ON  
COMPUTER ARCHITECTURE*

Mark D. Hill, *Series Editor*

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Sony v.  
Memory Integrity, LLC  
IPR2015-00158  
EXHIBIT  
Memory Integrity - 2010

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# A Primer on Memory Consistency and Cache Coherence

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