Sony Corporation et al., Petitioners

v.

Memory Integrity, LLC, Patent Owner

Before Jennifer S. Bisk, Neil T. Powell, and Kerry Begley,
Administrative Patent Judges.

Case No. IPR2015-00158
Patent No. 7,296,121
February 8, 2016

Walter E. Hanley, Lead Counsel
Zaed M. Billah, Backup Counsel

Grounds for Institution

Trial Instituted On Claims 19-24:

- Claims 19-23 obvious over Koster and Kuskin

- Claim 24 obvious over Koster

Claim Construction of "States"

Board: "[T]he term is not limited to cache coherence protocol states and is broad enough to include the condition of presence—i.e., what is stored in memory."

Patent Owner: The term "'state' means a cache coherence protocol state in the field of cache coherency."

Institution Decision, Paper No. 7 at 10; Patent Owner Response, Paper No. 17 at 5

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The Board's Construction of "States" is Correct

'121 Patent:

Although the coherence directory **701** includes the four states of modified, owned, shared, and invalid, it should be noted that <mark>particular implementations may use a different set of states.</mark> In one example, a system may have the five states of modified, exclusive, owned, shared, and invalid. The techniques of the present invention can be used with a <mark>variety of different possible memory line states.</mark>

Sony-1001 at 14:30-36; Institution Decision, Paper No. 7 at 9-10; Petitioner's Reply, Paper No. 25 at 2

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The Board's Construction of "States" is Correct

Dr. Sorin: "A person of ordinary skill in the art would understand the term 'states associated with selected ones of the cache memories' to not be limited to cache coherence protocol states, and be <mark>broad enough to include the condition of presence</mark>—i.e., what is stored in cache memory."

Sony-1015 at ¶ 17; Petitioner's Reply, Paper No. 25 at 4

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