

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent of: Sartore et al.
U.S. Patent No.: 6,249,825
Issue Date: June 19, 2001
Appl. Serial No.: 09/476,923
Filing Date: January 4, 2000
Title: Universal Serial Bus Interface System and Method

Mail Stop Patent Board

Patent Trial and Appeal Board
U.S. Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

PATENT OWNER'S POWER OF ATTORNEY IN AN *INTER PARTES* REVIEW

Patent Owner, Cypress Semiconductor Corporation, hereby appoints the following practitioners as its attorneys to transact all business in the United States Patent & Trademark Office associated with the *inter partes* review of the above-captioned patent:

Robert R. Laurenzi, Reg. No. 45,557

Robert S. Magee, Reg. No. 70,227

and all practitioners associated with Kaye Scholer LLP.

I have the authority to execute this document on behalf of Cypress Semiconductor Corporation.

CYPRESS SEMICONDUCTOR CORPORATION

By: 

Name: VICTORIA VALENZUELA

Title: VICE PRESIDENT OF LEGAL AFFAIRS & GENERAL COUNSEL

Date: 10/01/2014