United States Patent [19]

Matthews

[54] HARDENING OF PHOTORESIST

- [75] Inventor: John C. Matthews, Columbia, Md.
- [73] Assignee: Fusion Semiconductor Systems, Rockville, Md.
- [21] Appl. No.: 497,466
- [22] Filed: May 23, 1983

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[11] Patent Number: 4,548,688

[45] Date of Patent: Oct. 22, 1985

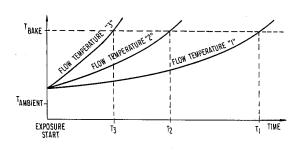
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Primary Examiner—Howard S. Williams Attorney, Agent, or Firm—Pollock, Vande Sande & Priddy

[57] ABSTRACT

Photoresist is hardened by exposing it to UV radiation while subjecting it to elevated temperatures upon an increase in the degree of polymerization due to exposure to the radiation. The temperature of the photoresist can be controlled by a thermal chuck which contains a thermal ballast of heat conducting material.

10 Claims, 3 Drawing Figures



CONDITION "I" - RESIST IS MAINTAINED AT AMBIENT TEMPERATURE DURING THE EXPOSURE.

- CONDITION "2" -RESIST IS MAINTAINED AT A CONSTANT ELEVATED TEMPERATURE BELOW THE INITIAL RESIST FLOW TEMPERATURE DURING THE EXPOSURE.
- CONDITION "3" RESIST TEMPERATURE IS ALLOWED TO RISE DURING UV EXPOSURE , BUT IS MAINTAINED BELOW THE FLOW TEMPERATURE OF THE RESIST. FINAL RESIST TEMPE-RATURE EXCEEDS INITIAL FLOW TEMPERATURE OF THE RESIST .

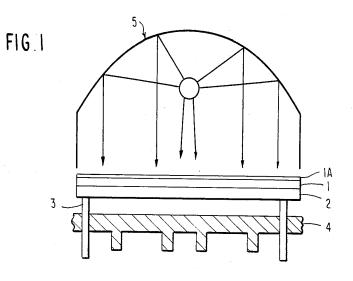
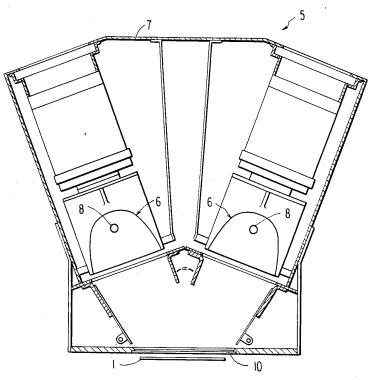
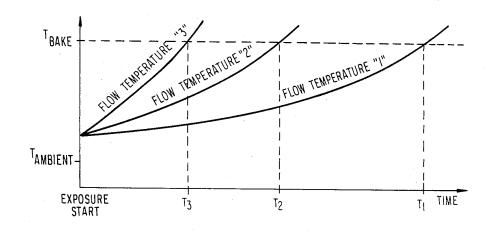


FIG.2



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CONDITION "I" - RESIST IS MAINTAINED AT AMBIENT TEMPERATURE DURING THE EXPOSURE_

- CONDITION "2" -RESIST IS MAINTAINED AT A CONSTANT ELEVATED TEMPERATURE BELOW THE INITIAL RESIST FLOW TEMPERATURE DURING THE EXPOSURE_
- CONDITION "3" RESIST TEMPERATURE IS ALLOWED TO RISE DURING UV EXPOSURE, BUT IS MAINTAINED BELOW THE FLOW TEMPERATURE OF THE RESIST. FINAL RESIST TEMPE-RATURE EXCEEDS INITIAL FLOW TEMPERATURE OF THE RESIST_

FIG 3

HARDENING OF PHOTORESIST

DESCRIPTION

1. Technical Field

The present invention is concerned with a method and apparatus for hardening of photoresist materials. In particular, the present invention is directed to an improved method for hardening positive photoresist material employing UV radiation and especially deep to mid ¹⁰ UV radiation. The present invention is particularly advantageous in the fabrication of integrated circuits. The present invention is also concerned with a device capable of controlling the temperature of the photoresist during exposure to UV radiation. ¹⁵

2. Background Art

In the fabrication of various articles, it is often necessary to protect preselected areas of the surface while other areas of that same surface are exposed to particular treatments and/or process procedures. For instance, ²⁰ in the fabrication of semiconductor devices wherein, for example, an oxide layer is formed on a semiconductor substrate, it is often necessary to remove selected portions of the oxide layer so as to allow diffusion of a suitable impurity through the oxide layer openings into ²⁵ the underlying semiconductor substrate. Exemplary of such procedures is the fabrication of semiconductor devices, such as single crystal field effect transistors.

The above type of devices are formed by vapor diffusing a suitable impurity into a wafer of a single silicon 30 crystal to form suitably doped regions therein. In order to provide distinct P or N regions which are necessary for the proper operation of the device, diffusion should occur through only a limited portion of the substrate. Usually, this is accomplished by masking the substrate 35 with a diffusion resistant material, such as silicon dioxide, which is formed into a protective mask to prevent diffusion through preselected areas of the substrate.

The silicon dioxide mask is typically provided by forming a uniform oxide layer over the wafer substrate 40 and thereafter creating a series of openings through the oxide layer to allow the passage of the impurity directly into the underlying surface within a limited area. These openings are readily created by coating the silicon dioxide with a material known as a photoresist. Photoresists 45 can be negative photoresist or positive photoresist materials. A negative photoresist material is one which is capable of polymerizing and insolubilizing on exposure to light. Accordingly, when employing a negative photoresist material, the photoresist layer is selectively 50 exposed to light, causing polymerization to occur above those regions of the silicon dioxide which are intended to be protected during a subsequent operation. The unexposed portions of the photoresist are removed by a solvent which is inert to the polymerized portion of the 55 resist and a suitable etchant for the silicon dioxide, such as hydrogen fluoride or plasma is applied to remove the unprotected oxide regions.

The positive resist material is one that upon exposure to light is capable of being rendered soluble in a solvent 60 in which the unexposed resist is not soluble. Accordingly, when employing a positive photoresist material, such is selectively exposed to light, causing reaction to occur above those regions of the silicon oxide which are not intended to be protected during the subsequent 65 processing. The exposed portions of the photoresist are removed by a solvent which is not capable of dissolving the unexposed portion of the resist. Then the silicon

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dioxide is etched to remove the unprotected oxide regions.

Likewise, photoresist materials are used to define other regions of such devices such as polycrystalline silicon regions and metallic type interconnections.

Many of the commonly used photoresists are not especially resistant to flow or distortion when exposed to relatively high temperatures encountered in more recently employed processing techniques such as plasma etching. In addition, it is desired to use the photoresist remaining after defining for instance the oxide regions, in many applications as a protecting layer in the processing such as the doping. This also exposes the photoresist to elevated temperatures including that above about 200° C.

In order to be suitable for such purposes, materials which are resistant to flow at higher temperatures have been developed and used as photoresists. In addition, it has been suggested to subject certain positive photoresists to exposure to deep UV radiation to cross-link and stabilize the material against elevated temperatures after the pattern in the photoresist has been developed. Examples of such discussions of the use of deep UV radiation to cure or cross-link positive photoresists can be found in Yen, et al., Deep UV and Plasma Hardening of Positive Photoresist Patterns, Integrated Circuit Laboratory, Xerox Palo Alto Research Center, Palo Alto, Calif. Hiraoka, et al., High Temperature Flow Resistance of Micron Sized Images in AZ Resists, AZ Resists, Vol. 128, No. 12, pages 2645-2647; and Ma, Plasma Resist Image Stabilization Technique (PRIST) update, SPIE Vol. 333, Submicron Lithography, 1982, pages 1-23.

The use of deep UV radiation for such stabilization is not entirely satisfactory since the time of exposure for many of the commonly used photoresists has been reported as being about 10–30 minutes, which is relatively long.

SUMMARY OF THE INVENTION

The present invention provides a process for hardening positive photoresist materials by UV radiation which makes it possible to significantly reduce the necessary exposure time. For instance, the present invention makes it possible to effect stabilization of many positive photoresists using exposure times for UV radiation containing wavelenghts of up to 320 nm of less than one minute. In addition, the present invention makes it possible, in many instances, to eliminate postbaking or to postbake at higher temperatures and/or shorter times.

In particular, the present invention is concerned with hardening a positive photoresist by exposing a film of photoresist material to UV radiation while subjecting the film to elevated temperatures upon an increase in the degree of polymerization of the photoresist due to exposure to the UV radiation. The elevated temperature is maintained below the flow temperature of the film. The flow temperature as used herein refers to that temperature which causes the photoresist to distort when applied to the photoresist for 30 minutes. The resist during the exposure to the UV radiation is heated to an elevated temperature which is greater than the flow temperature of the photoresist at the start of the exposure to the UV radiation.

The present invention is also concerned with a thermal chuck for use in the above described process to control the temperature of the photoresist during exposure to UV radiation. The thermal chuck contains a thermal ballast which supports the substrate and which is composed of a thermally conductive material for transferring heat during exposure to the light to and 5 from the substrate and photoresist. The mass of the thermal ballast is selected so that the temperature of the photoresist is raised above the flow temperature of the photoresist which existed at the start of the exposure to the UV radiation, but below the flow temperature of the 10 photoresist at the time the temperature is raised.

The present invention is also concerned with apparatus for hardening a photoresist which comprises the thermal chuck described hereinabove and a lamp capable of emitting UV radiation and heat. 15

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a schematic diagram of apparatus suitable for carrying out the present invention.

FIG. 2 is a schematic diagram of a lamp suitable for 20 supplying UV radiation for use in the present invention.

FIG. 3 is a graph illustrating flow temperature versus exposure time for different temperature conditions during exposure to UV radiation.

DESCRIPTION OF PREFERRED AND VARIOUS ²⁵ MODES FOR CARRYING OUT INVENTION

A wide variety of photoresist materials can be employed in the process of the present invention. Among those photoresist materials found to be especially suit- 30 able are the positive photoresists which are cross-linkable when exposed to UV radiation of up to 320 nm, and particularly, those photoresists sensitized with diazo compounds. Examples of such diazo sensitizers are discussed on pages 48-55 of DeForest, Photoresist Materi- 35 als and Processes, McGraw-Hill Book Company, 1975, disclosure of which is incorporated herein by reference. Some diazo compounds are benzoquinone 1,2-diazide-4sulfochloride; 2-diazo-1-naphthol-5-sulphonic acid ester; naphthoquinone-1,2-diazide-5-sulfochloride; naph- 40 thoquinone-1,2-diazide-4-sulfochloride; naphthoquinone-2,1-diazide-4-sulfochloride; and naphthoguinone 2,1-diazide-5-sulfochloride.

The preferred photoresist materials employed are the phenolic-formaldehyde type novolak polymers sensitized with a diazo compound. The phenols include phenol and substituted phenols such as cresol. A particular example of such is Shipley 1350 which is an m-cresolformaldehyde novolak polymer composition. Such is a positive resist composition and includes therein a diazoketone, such as 2-diazo-1-naphthol-5-sulphonic acid ester. In such a composition, the ortho-diazoketone during the photochemical reaction is converted to a carboxylic acid. Other commercially available photoresists are suitable for carrying out the process of the 55 present invention, including other novolak types.

The UV radiation employed must contain sufficient dosage of deep to mid UV radiation having wavelengths about 320 nm and below to cause the polymer to cross-link; preferably about 180 to 320 nm and more 60 usually, about 190–260 nm. In many instances, the dosage to the photoresist of the UV radiation of wavelength of 320 nm and below is at least about 10 Joule/cm².

Although the present invention can be carried out 65 with any deep UV output lamp, a particularly suitable lamp is schematically illustrated as numeral 5 in FIG. 2. This lamp 5 in addition to providing the necessary deep

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to mid UV radiation, also emits IR irradiation sufficient to supply the necessary heat to the photoresist for carrying out the process of the present invention. Lamp 5 is a microwave powered electrodeless lamp comprising two separate microwave powered electrodeless lamps 6 having an elongated lamp envelope 8 within housing 7. Since the lamps are fully described in U.S. patent application Ser. No. 393,856 to Ury, et al., filed June 30, 1982 now U.S. Pat. No. 4,504,768, entitled "Electrodeless Lamp Using a Single Magnetron and Improved Lamp Envelope Therefore", disclosure of which is incorporated herein by reference; a more detailed discussion thereof is not deemed necessary. The basic difference between the disclosure of Ser. No. 393,856 and the lamp illustrated in FIG. 2 is that two separate individually microwave powered lamps 6 are contained in housing 7 as contrasted to one such lamp explicitly described in said Ser. No. 393,856. The lamps 6 are at α angle of about 19°-20° to each other. The lamps include an elongated envelope and elliptical reflector as described in Ser. No. 393,856. Two lamps are employed in this embodiment merely as a precaution to insure uniform irradiation of the wafer and photoresist. However, as understood, any lamp capable of providing the needed irradiation can be used.

A lamp as disclosed is available from Fusion Semiconductor Systems Corporation, a subsidiary of Fusion Systems Corporation under the trademark "Microlite 126A Resist Stabilizer Unit".

The exposure time required is significantly reduced according to the present invention by maintaining the resist at an elevated temperature during the exposure to the UV radiation. The temperature must be below the flow temperature of the resist which would cause deformation or distortion of the coating or of any pattern therefrom. Although the photoresist need not be subjected to elevated temperatures throughout the exposure to the radiation, it is preferred that elevated temperatures be used during at least substantially the entire exposure to radiation. During the exposure to the radiation the photoresist is heated to elevated temperature which is above the flow temperature of the resist at the start of the exposure to the radiation.

The flow temperature of the photoresist steadily increases throughout the duration of the radiation as result of an increase in the degree of polymerization or cross-linking of the photoresist. Accordingly, the temperature to which the photoresist can be heated without distortion increases as the cross-linking or polymerization due to the radiation procedes.

In a preferred embodiment of the present invention, the temperature of the photoresist is controlled during the exposure to radiation so that it rises at approximately the same rate as does the flow temperature while insuring that the temperature of the photoresist never equals or exceeds the flow temperature. The exposure is continued until the flow temperature exceeds the desired hard bake temperature of the photoresist.

Another embodiment of the invention involves a two-step process which includes a first step of exposing the photoresist to said UV irradiation at a temperature below the flow temperature of the unexposed resist, and a second step of exposing the resist to said UV irradiation at a temperature substantially exceeding the flow temperature of the unexposed photoresist. If desired, the source of the UV irradiation of the first step can differ from that of the UV irradiation of the second step.

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