EXHIBIT 2001

United States Patent [19]

Sartore et al.

[11] **Patent Number:** 6,012,103

[45] **Date of Patent:** **Jan. 4, 2000**

[54] **BUS INTERFACE SYSTEM AND METHOD**

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[21] Appl. No.: **08/886,923**

[22] Filed: **Jul. 2, 1997**

[51] **Int. Cl.**[7] G06F 13/368
[52] **U.S. Cl.** **710/8**; 710/9; 710/10
[58] **Field of Search** 379/395; 709/220, 709/250; 711/114, 115, 130; 713/100; 710/9

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Primary Examiner—Thomas C. Lee
Assistant Examiner—Chien Yuan
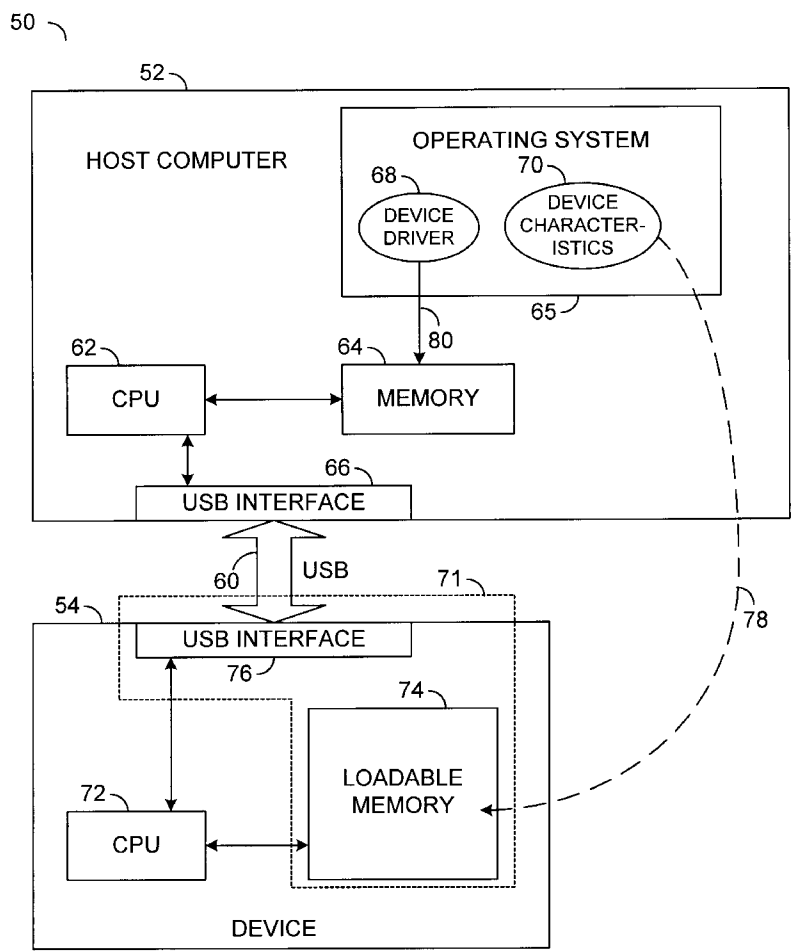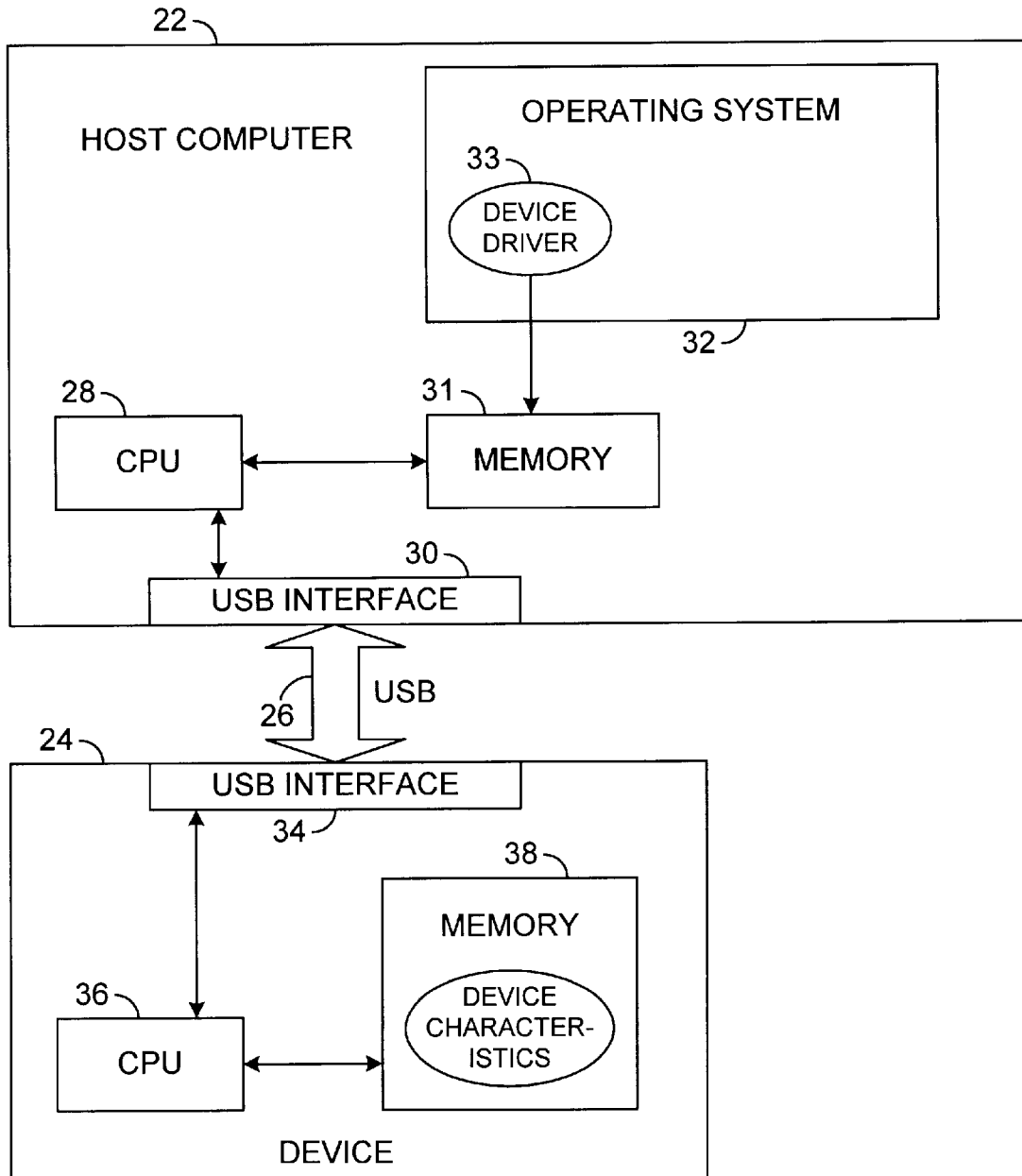*Attorney, Agent, or Firm*—Christopher P. Maiorana, P.C.

[57] **ABSTRACT**

A system and method for reconfiguring a peripheral device connected by a computer bus and port to a host from a first generic configuration to a second manufacturer specific configuration is provided in which the configuration of a peripheral device may be electronically reset. A peripheral interface device for a standardized computer peripheral device bus and port is also provided in which a physical disconnection and reconnection of the peripheral device is emulated to reconfigure the bus and port for a particular peripheral device.
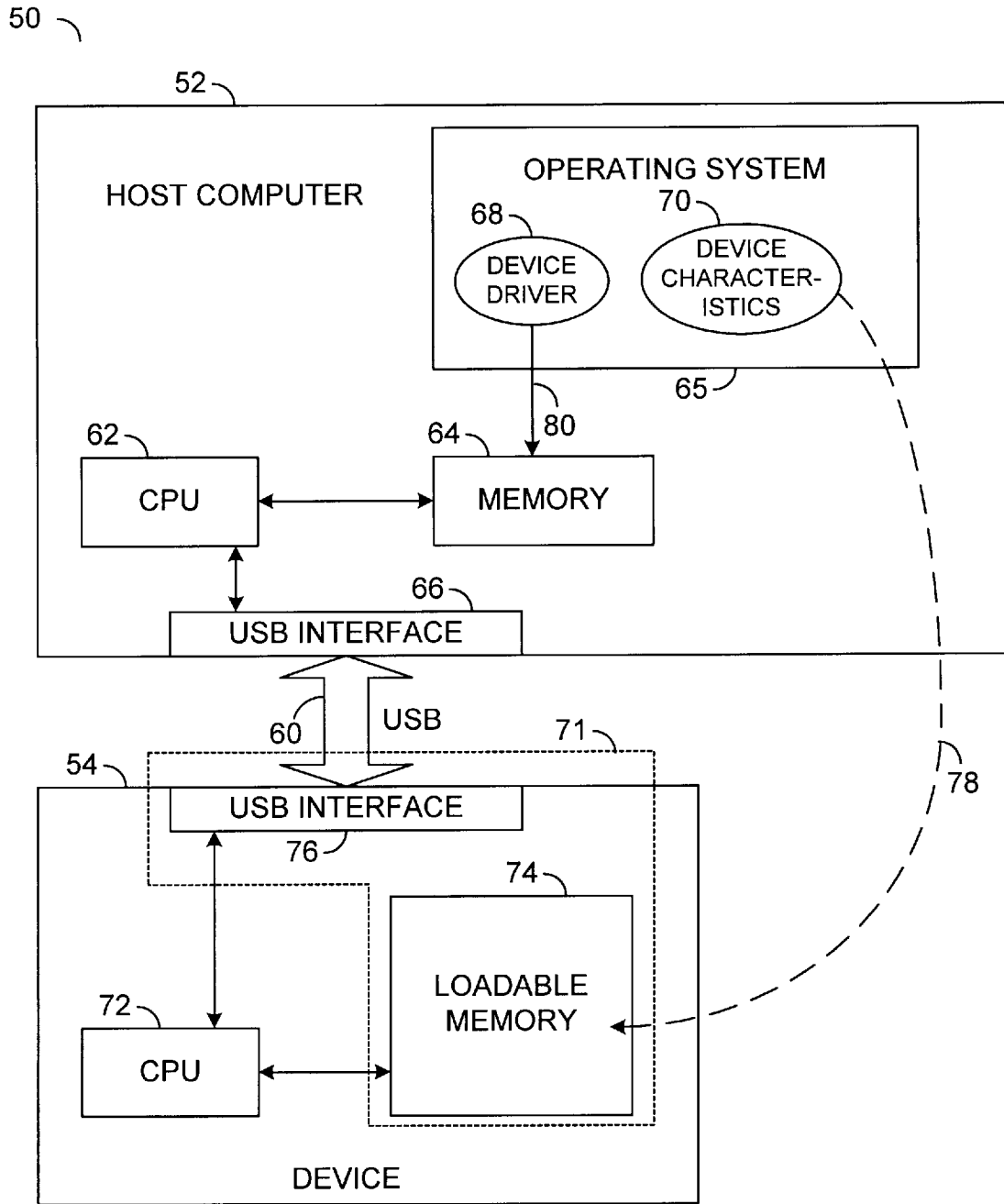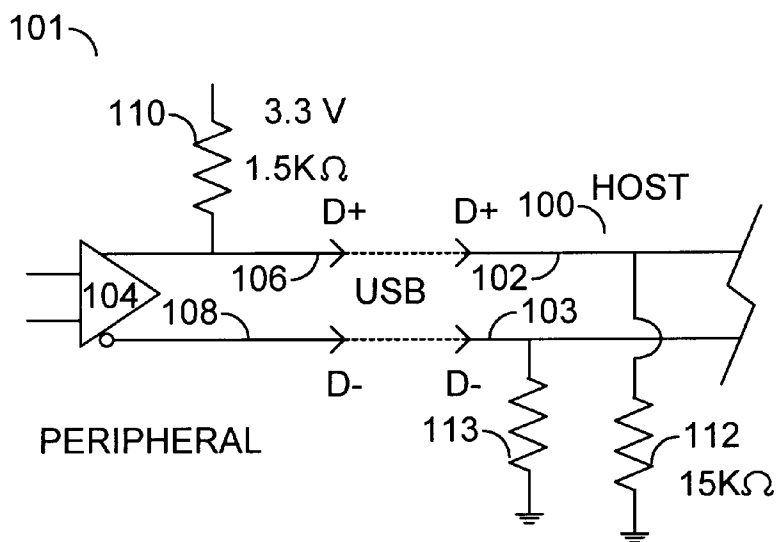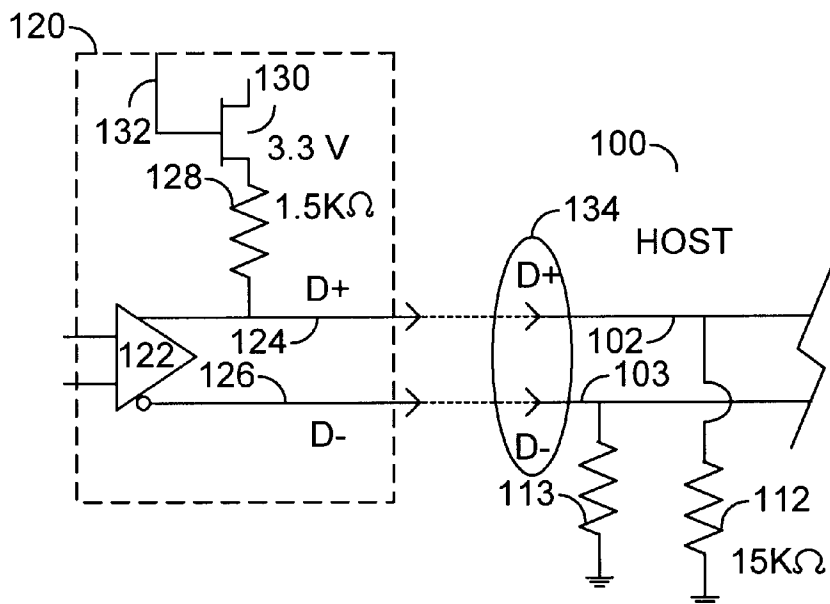
33 Claims, 7 Drawing Sheets

FIG. 1
(PRIOR ART)

FIG. 2

FIG. 3

(PRIOR ART)

FIG. 4

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