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United States Patent [19]

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Shinohara

[45] Date of Patent: **Nov. 11, 1997**

[54] **PC CARD AND PC CARD SYSTEM WITH DUAL PORT RAM AND SWITCHABLE REWRITABLE ROM**

5,537,558	7/1996	Fletcher et al.	395/309
5,544,087	8/1996	Nakajima et al.	364/443
5,574,929	11/1996	Pieterse et al.	395/800

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[57] **ABSTRACT**

[21] Appl. No.: **535,639**

The present invention relates to a PC card comprising: a CPU; a memory storing an operating program for said CPU; interface means connected to a host system for performing configuration under the control of said host system and exchanging data with said host system; and a dual port RAM connected between said CPU and said interface means for storing at a time all of data inputted from and outputting to said host system through said interface means, wherein said data being accessed sequentially by said CPU. This invention resides in a PC card system comprising the PC card and a host system.

[22] Filed: **Sep. 28, 1995**

[30] **Foreign Application Priority Data**

Sep. 30, 1994 [JP] Japan 6-237858

[51] Int. Cl.⁶ **G06F 13/00**

[52] U.S. Cl. **395/430; 395/200.08; 395/280; 395/311; 395/442; 395/458; 395/476; 364/DIG. 1**

[58] Field of Search **395/200.08, 280, 395/311, 430, 442, 458, 476**

[56] **References Cited**

U.S. PATENT DOCUMENTS

5,359,570 10/1994 Hsu et al. 365/230.01

8 Claims, 6 Drawing Sheets

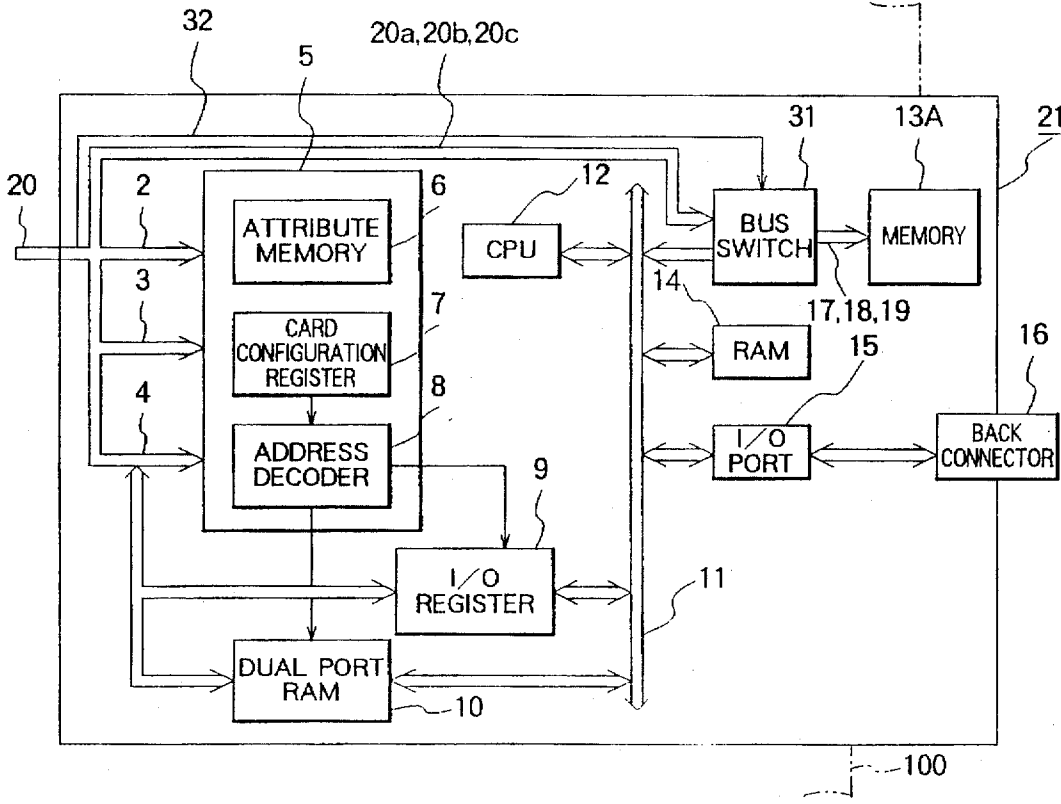


EXHIBIT 2016
LG Elecs. v. Cypress Semiconductor
IPR2014-01405, U.S. Pat. 6,493,770

FIG. 1

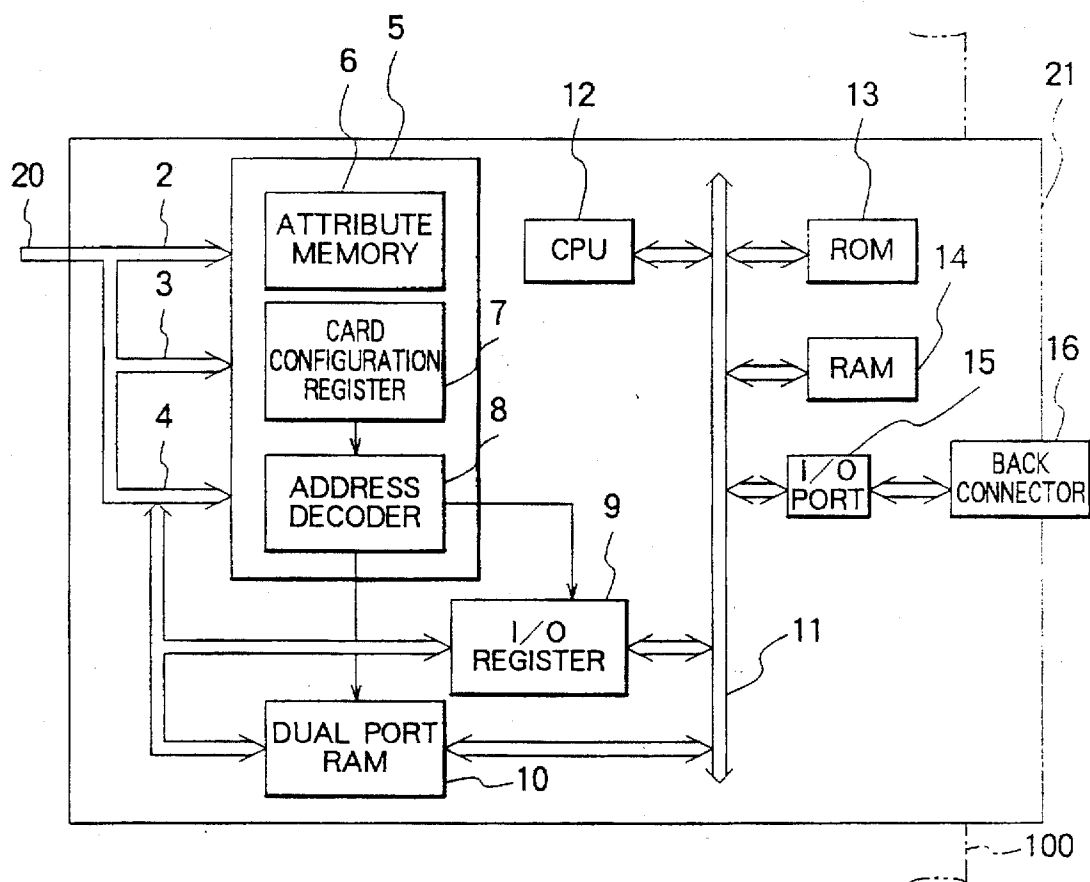


FIG. 2

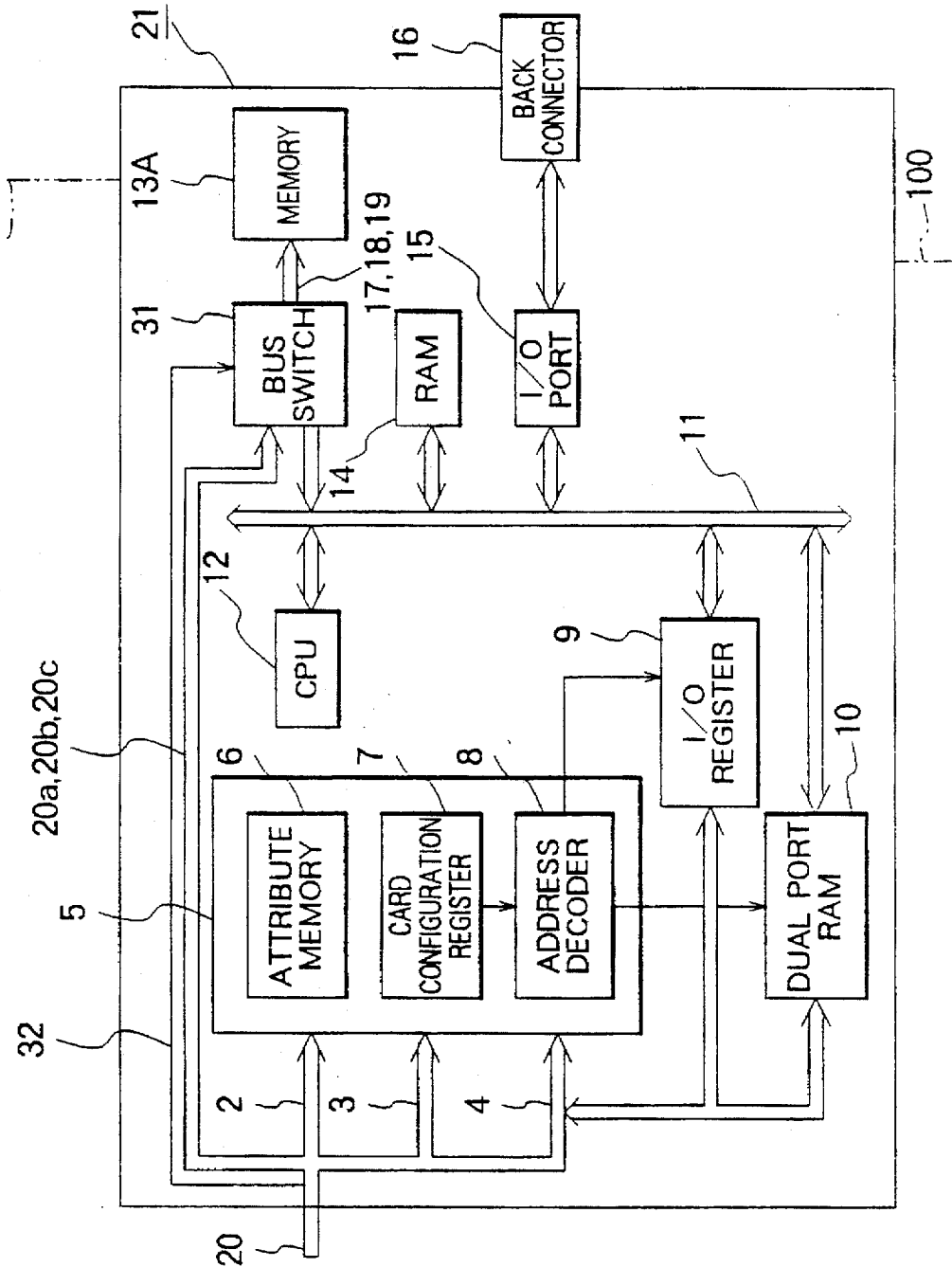


FIG. 3

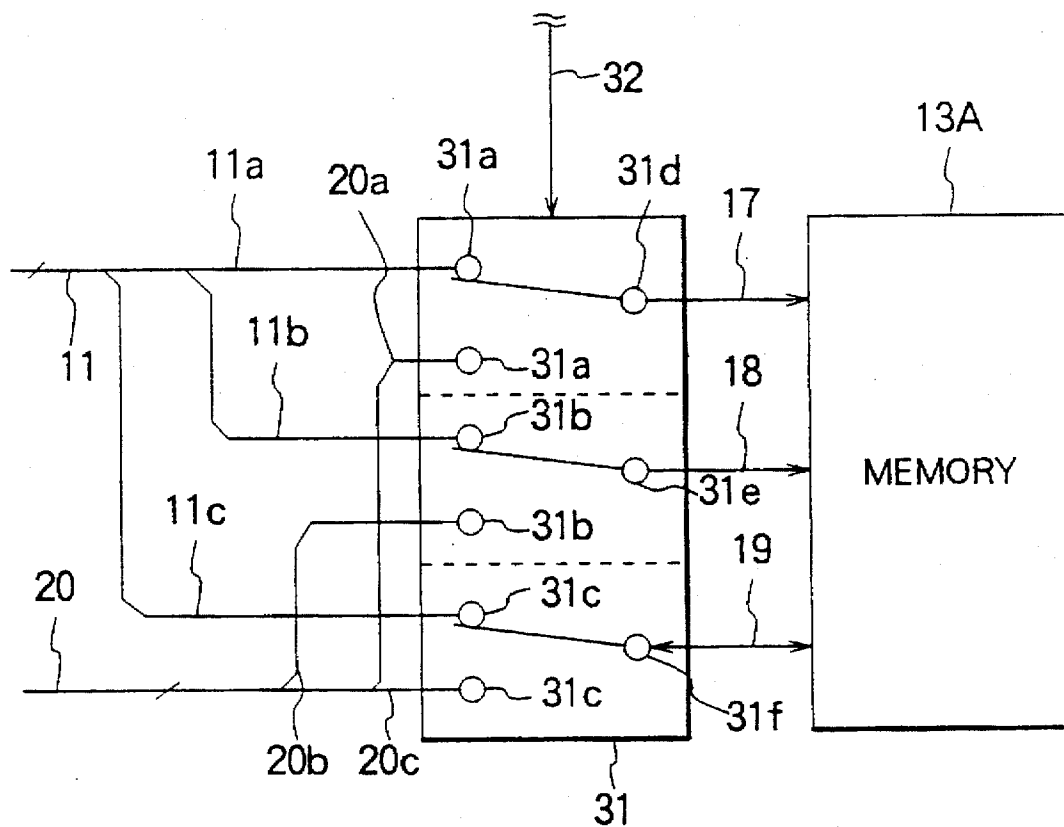
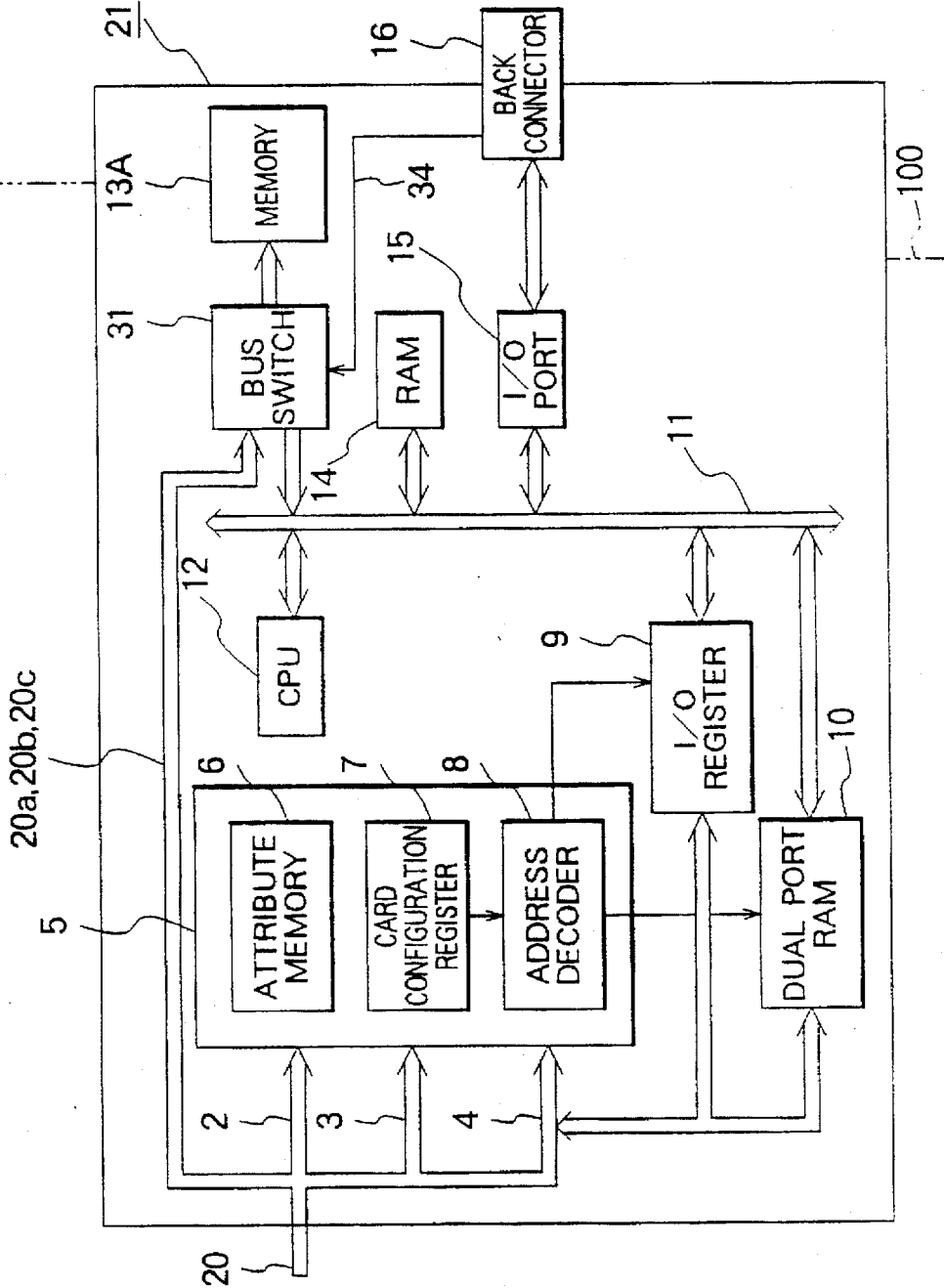


FIG. 4



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